Verifiable Policy-based Routing with DRIVER

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Abstract. The Internet today runs on a complex routing protocol called the Border Gateway Protocol (BGP). BGP is a policy-based protocol, in which autonomous Internet Service Providers (ISPs) impose their local policies on the propagation of routing information. Over the past few years, there has been a growing consensus on the complexity and fragility of BGP routing. To address these challenges, we present the DRIVER system for designing, analyzing and implementing policy-based routing protocols. Our system utilizes a declarative network verifier (DNV) which leverages declarative networking’s connection to logic programming by automatically compiling high-level declarative networking program into formal specifications, which can be directly used in a theorem prover for verification. In addition to verifying declarative networking programs using a theorem prover, the DRIVER system enables a similar transformation of verified formal specifications limited to fragment of second order logic to declarative networking programs for execution. As our main use case, we demonstrate the verification of a component-based specification of BGP protocol where DRIVER enables the analysis of convergence properties of Internet routing protocols with customizable policy configuration components. We show that the properties verified with DRIVER are indeed preserved in the synthesized implementation by performing experimental evaluation in a local cluster, where the equivalent declarative networking programs derived from the verified specifications displayed consistent behavior with regard to DRIVER verification.

1 Introduction

The Internet today runs on a complex routing protocol called the Border Gateway Protocol or BGP in short. BGP enables Internet-service providers (ISP) world-wide to exchange reachability information to destinations over the Internet, and simultaneously, each ISP acts as an autonomous system that imposes its own import and export policies on route advertisements exchanged among neighboring ISPs.

Over the past few years, there has been a growing consensus on the complexity and fragility of BGP routing. Even when the basic routing protocol convergences, conflicting policy decisions among different ISPs have lead to route oscillation and slow convergence. Several empirical studies such as reference [12] have shown that there are prolonged periods in which the Internet cannot reliably route data packets to specific
destinations due to routing errors induced by BGP. In response, the networking community has proposed several Internet architectures and policy mechanisms (e.g. [17, 6]) aimed at addressing these challenges.

Given the proliferation of proposed techniques, there is a growing interest in formal software tools and programming frameworks that can facilitate the design, implementation, and verification of routing protocols. These proposals can be broadly classified as: (1) algebraic and logic frameworks (e.g. [10]) that enable protocol correctness check in the design phase; (2) runtime debugging platforms that provide mechanisms for runtime verification and distributed replay, and (3) programming frameworks that enable network protocols to be specified, implemented, and in the case of the Mace toolkit, verified via model checking [11].

In this paper, we present the DRIVER (Declarative Routing Implementation and VERification) system for designing, analyzing and implementing network protocols within a unified framework. Our work is a significant step towards bridging network specifications, protocol verification, and implementation within a common language and system. The DRIVER framework achieves this unified capability via the use of declarative networking [14, 13], a declarative domain-specific approach for specifying and implementing network protocols, and theorem proving, a well established verification technique based on logical reasoning.

DRIVER leverages our prior work on a declarative network verifier (DNV) [19] which demonstrates that one can leverages declarative networking’s connection to logic programming by automatically compiling high-level declarative networking program written in the Network Datalog (NDlog) query language into formal specifications, which can be directly used in a theorem prover for verification. The proving process guided by the user is then carried out in a general-purpose theorem prover and proofs are mechanically checked. Declarative networking programs that have been verified in DRIVER can be directly executed as implementations, hence bridging specifications and implementations within a unified declarative framework.

In addition to verifying declarative networking programs using a theorem prover, the DRIVER system enables a similar transformation of verified formal specifications (limited to fragment of second order logic) to NDlog program for execution. This enables a network designer to either directly verify network implementation specified in NDlog or conceptualize and verify the design of a network in components aided by a theorem prover prior to implementation.

Theorem proving provides an expressive and powerful verification framework that is particularly well-suited for analyzing the complexities of BGP policies. We introduce a component-based specification of BGP system that is based on the BGP model first proposed by Griffin et al. [18]. Components modularize our analysis, enable reuse of code, hence enable us to study the impact of import and export policies on overall protocol behavior. For example, we demonstrate the use of DRIVER and component-based reasoning for detecting instances where policy conflicts may lead to protocol divergence, a well-known limitation of the existing BGP system. In DRIVER, verified component-based specifications of specific BGP protocol can be easily translated to equivalent NDlog programs for execution. We experimentally validate verified properties based on our BGP analysis in equivalent NDlog programs executed and evaluated in a local cluster using the P2 declarative networking engine [1].
2 Background

In this section, we will provide a brief overview of declarative networking. Declarative networks are specified using Network Datalog (NDlog), a distributed logic-based recursive query language first introduced in the database community for querying network graphs. In prior work, it has been shown that traditional routing protocols can be specified in a few lines of declarative code [14], and complex protocols such as Chord DHT in orders of magnitude less code [13] compared to traditional imperative implementations. This compact and high-level specifications enables rapid prototype development, ease of customization, optimizability, and the potentiality for protocol verification. When executed, these declarative networks perform efficiently relative to imperative implementations, as demonstrated by the P2 declarative networking system [1].

2.1 Datalog Language

NDlog is primarily a distributed variant of Datalog. We first provide a short review of Datalog, following the conventions in Ramakrishnan and Ullman’s survey [16]. A Datalog program consists of a set of declarative rules. Each rule has the form \( p \leftarrow q_1, q_2, \ldots, q_n \), which can be read informally as “\( q_1 \) and \( q_2 \) and \( \ldots \) and \( q_n \) implies \( p \)”. Here, \( p \) is the head of the rule, and \( q_1, q_2, \ldots, q_n \) is a list of literals that constitutes the body of the rule. Literals are either predicates with attributes (which are bound to variables or constants by the query), or boolean expressions that involve function symbols (including arithmetic) applied to attributes. In Datalog, rule predicates can be defined with other predicates in a cyclic fashion to express recursion. The order in which the rules are presented in a program is semantically immaterial; likewise, the order predicates appear in a rule is not semantically meaningful. Commas are interpreted as logical conjunctions (AND). The names of predicates, function symbols, and constants begin with an lowercase letter, while variable names begin with an upper letter.

2.2 Path-vector Protocol

We present an example NDlog program that implements the path-vector protocol.

\[
\begin{align*}
p_1 & \text{ path}(\$S,D,P,C) :- \text{link}(\$S,D,C), p=f_{\text{init}}(S,D). \\
p_2 & \text{ path}(\$S,D,P,C) :- \text{link}(\$S,Z,C1), \text{ path}(\$Z,D,P2,C2), C=C1+C2, \\
 & \quad P=f_{\text{concatPath}}(S,P2), f_{\text{inPath}}(P2,S)=false. \\
p_3 & \text{ bestPathCost}(\$S,D,\text{min}<C>) :- \text{path}(\$S,D,P,C). \\
p_4 & \text{ bestPath}(\$S,D,P,C) :- \text{bestPathCost}(\$S,D,C), \text{ path}(\$S,D,P,C).
\end{align*}
\]

The program takes as input link(\$S,D,C) tuples, where each tuple corresponds to a copy of an entry in the neighbor table, and represents an edge from the node itself (S) to one of its neighbors (D) of cost c. NDlog supports a location specifier in each predicate, expressed with “\$” symbol followed by an attribute. This attribute is used to denote the source location of each corresponding tuple. For example, link tuples are stored based on the value of the \$S field.

Rules p1-p2 recursively derive path(\$S,D,P,C) tuples, where each tuple represents the fact that the path from S to D is via the path P with a cost of C. Rule p1 computes one-hop reachability trivially given the neighbor set of S stored in link(\$S,D,C). Rule P2 computes transitive reachability as follows: if there exists a link from S to Z
with cost \( C_1 \) and \( Z \) knows about a shortest path \( P_2 \) to \( D \) with cost \( C_2 \), then transitively, \( S \) can reach \( D \) via the path \( f\text{\_concatPath}(S, P_2) \) with cost \( C_1 + C_2 \). Note that \( p_1-p_2 \) also utilize two list manipulation functions to maintain path vector \( p \): \( f\text{\_init}(S, D) \) initializes a path vector with two elements \( S \) and \( D \), while \( f\text{\_concatPath}(S, P_2) \) prepends \( S \) to path vector \( P_2 \).

Rules \( p_3-p_4 \) take as input hop tuples generated by rules \( p_1-p_2 \), and then derive the hop along the path with the minimal cost for each source/destination pair. The output of the program is the set of \( \text{bestPath}(@S,D,Z,C) \) tuples, where each tuple stores the next hop \( Z \) along the shortest path from \( S \) to \( D \). To prevent computing paths with cycles, an extra predicate \( f\text{\_inPath}(P, S) = \text{false} \) is used in rule \( p_2 \), where the function \( f\text{\_inPath}(P, S) \) returns true if node \( S \) is in the path vector \( P \).

Event predicates are used to denote transient tables which are used as input to rules but not stored. For example, utilizing the built-in \text{periodic} keyword, the following rule enables node \( X \) to generate a ping event every 10 seconds to its neighbor \( Y \) denoted in the \( \text{link}(X,Y) \) predicate: \( \text{ping}(Y,X) :- \text{periodic}(X,10), \text{link}(X,Y) \).

### 3 Overview of DRIVER

![Overview of DRIVER](image)

Figure 1 provides an overview of DRIVER’s basic approach towards unifying specifications, verification, and implementation within a common declarative framework. The approach is broken up into the following four phases: design, specification, verification, and implementation.

In the initial design phase of DRIVER, a network designer develops a conceptual model for the routing protocol. In practice, this step may be optional, but having such a model is often useful both from the implementation standpoint, and for verifying one’s protocol design, as we will demonstrate in Section 4.

Based on the design, two options are available. First, \text{N}D\text{log} networking programs can be synthesized from the design, and then the \text{N}D\text{log} implementations can be directly verified in an theorem prover. Second, the designer can first verify the design using a theorem prover and then automatically generate the corresponding \text{N}D\text{log} program.

Considering the first option, DRIVER takes as input \text{N}D\text{log} program representation of the routing protocol we are interested in. In order to carry out the formal verifica-
tion process, the \textit{NDlog} programs are automatically compiled into formal specifications recognizable by a standard theorem prover (e.g. PVS [2], Coq [3]) using the \textit{axiom generator}, as depicted in the left-part of Figure 1.

At the same time, the protocol designer specifies high-level invariant properties of the protocol to be checked via two mechanisms: invariants can be written directly as theorems in the theorem prover, or expressed as \textit{NDlog} rules which can be automatically translated into theorems using the axiom generator. The first approach increases the expressiveness of invariant properties, where one can reason with invariants that can be only expressible in higher order logic. The second approach has restricted expressiveness based on \textit{NDlog}'s use of Datalog, but has the added advantage that the same properties expressed in \textit{NDlog} can be verified in both theorem prover and checked at runtime.

From the perspective of network designers, as depicted in the left part of Figure 1, they reason about their protocols using the high-level protocol specifications and invariant properties. The \textit{NDlog} high-level specifications are directly executed and also proved within the theorem prover. Any errors detected in the theorem prover can be corrected by changing the corresponding \textit{NDlog} programs. Our initial \textit{DRIVER} prototype uses the PVS theorem prover, due to its substantial support for proof strategies which significantly reduce the time required in the interactive proof process. However, the techniques describe in this paper are agnostic to other theorem provers. We have also verified some of the properties presented in this paper using the Coq [3] proof assistant.

As a second option, \textit{DRIVER} allows the network designer to first utilize a theorem prover to check the protocol design. This requires a network designer first develop formal specifications for the routing protocol of interests. Once the formal representation of the protocol is verified by the prover, corresponding \textit{NDlog} programs are then generated for execution. Similar to the first option, this approach is made possible by the use of \textit{NDlog}, which is particularly amenable to the translation into formal specification recognizable by existing theorem provers (and vice versa), due to its logic-based nature.

Reference [19] provides details on the translation process from \textit{NDlog} programs into formal specification in theorem prover, as well as several verification use cases for standard network routing protocols. In the rest of the paper, we focus on the second approach, using policy-based routing as our driving example.

4 Verifying Policy-based Inter-domain Routing

We present a compositional approach towards the verification of policy-based BGP routing protocol. Our approach demonstrates the second option in \textit{DRIVER} verification as described in Section 3, where we begin with a component-based model of BGP protocol, then formalize and verify the BGP components in theorem prover PVS, and finally translate the verified BGP components into \textit{NDlog} for execution.

BGP assumes a network model in which routers are grouped into various Autonomous Systems (\textit{AS}) administrated by Internet Server Provider (\textit{ISP}). Individual \textit{ASes} exchange route advertisements with neighboring \textit{ASes} using the path-vector protocol described in Section 2.2. Upon receiving a route advertisement, an \textit{AS} may choose to accept or ignore the advertisement based on its \textit{import policy} dictated by its business
considerations and peering agreements. Similarly, an AS may choose to export only selected routes that it knows to its neighboring ASes. The route advertisements received by each node is then used to compute the next hop (neighboring AS) along the best paths to each destination.

We have selected BGP as one of our main use cases as complex policy interactions have been known to result in delayed protocol convergence. The problem is exacerbated by the lack of global knowledge on policies. While DRIVER does not directly address the lack of global knowledge, our goal here is to demonstrate that DRIVER provides a clean foundation for network administrators to formally reason about such policy interactions and their impact on protocol convergence. Moreover, the verified specifications are directly implementable as declarative networks.

In our formalization, we adopt Griffin’s BGP model [9, 18], which views BGP protocol as a series of route transformations, where each transformation is represented as a component that takes as input received routes, performs internal transformation based on the component specifications, and generates the output routes.

Figure 5 shows the general structure of a component that takes as input received routes, performs internal transformation based on the component specifications, and generates the output routes.

In our formalization, we adopt Griffin’s BGP model [9, 18], which views BGP protocol as a series of route transformations, where each transformation is represented as a component that takes as input received routes, performs internal transformation based on the component specifications, and generates the output routes.

Our use of components enables us to modularize our analysis, hence enabling us to study the impact of import and export policies on overall protocol behavior. This compositional approach towards verification is well-explored in the formal methods community, and has been successfully used in domains such as hardware verification [2]. In addition, by formalizing BGP protocol in a component-based fashion, it eases the translation of the verified PVS formalization to the equivalent NDlog rules for execution.

Figure 2 shows an overview of the abstract BGP model, which consists of the following five components: activeAS, export, pvt, import, and bestRoute. The triggering component activeAS(u, w, t) specifies at time t, AS w advertises its current best routes r0 to each neighbor AS u. The export policies are imposed by the export component where output route r1 is generated. This is followed by route propagation via the path-vector protocol (pvt) component which transforms route r1 to r2. Upon receiving the route advertisement r2, AS u applies its import policies via the import component and produces route r3. Finally, in the bestRoute component, AS u recomputes/selected its best route based on the all new route advertisement.
In the next iteration $t+1$, the same process repeats itself when triggered by the
\( \text{activeAS}(u, w, t+1) \) event.

One should view \( \text{activeAS} \) as a periodic event predicate (see Section 2.2) that is
invoked at each AS node to propagate its routes to neighbors, and all routes propagated
have a lifetime set to the duration of the period. If a current route is no longer valid, it
will no longer be advertised in the next time period.

### 4.1 Component-based Specification Generation

First consider the peer-transformation component \( (\text{pt}) \), which consists of three sub-
components \( (\text{export}, \text{pvt}, \text{and import}) \). The equivalent PVS specification is as
follows:

\[
\text{pt}(u, w, r_0, r_3, t) : \text{INDUCTIVE bool} = \\
\text{EXISTS } (r_1, r_2) : \text{activeAS}(u, w, t) \text{ AND export}(u, w, r_0, r_1, t) \\
\text{AND } \text{pvt}(u, w, r_1, r_2, t) \text{ AND import}(u, w, r_2, r_3, t) \text{ AND bestRoute}(w, t, r_0)
\]

The concept of peer-transformation was proposed in the original BGP model, and
involves neighboring ASes exchanging routes and updating their respective routing ta-
bles. \( \text{pt}(u, w, r_0, r_3, t) \) represents the route propagation between neighboring AS
\( u \) and \( w \), such that AS \( w \) advertises its best route \( r_0 \) to its neighbor AS \( u \) at time \( t \). The
advertised route \( r_0 \) undergoes a series of transformation and eventually is received by
\( u \) as route \( r_3 \).

More specifically, the route propagation is first triggered by \( \text{activeAS}(u, w, t) \). The
predicate \( \text{export}(u, w, r_0, r_1, t) \) is true if \( w \) can export route \( r_0 \) to \( u \) at time
\( t \), in which case, the exported route would be \( r_1 \). Route \( r_1 \) is then propagated via the
path-vector component \( \text{pvt} \), resulting in a new route \( r_2 \) with \( u \) prepended to \( r_1 \). In the
final transformation, an import policy is applied at \( u \), in which the route \( r_2 \) is accepted
as \( r_3 \) based on the policies specified at \( u \). Note that our formalization assumes that
the sub-components are executed within the same time period \( t \) before the next round
of route updates. This is consistent with the idealized BGP model described in prior
works.

Given the above \( \text{pt} \) component, we next proceed to define each sub-component as
follows:

\[
\text{export}(u, w, r_0, r_1, t) : \text{INDUCTIVE bool} = \\
\text{activeAS}(u, w, t) \text{ AND exportPolicy}(w, r_0, r_1) \text{ AND bestRoute}(w, r_0, (t-1))
\]

\[
\text{pvt}(u, w, r_1, r_2, t) : \text{INDUCTIVE bool} = \\
\{\text{NOT member } (u, \text{as_path}(r_1)) \Rightarrow \text{as_path}(r_2)=\text{cons}(u, \text{as_path}(r_1))
\text{AND dst}(r_2)=\text{dst}(r_1) \text{ AND loc_pref}(r_2)=\text{loc_pref}(r_1)\}
\]

\[
\text{import}(u, w, r_2, r_3, t) : \text{INDUCTIVE bool} = \\
\text{EXISTS } r_1: \text{pvt}(u, w, r_1, r_2, t) \text{ AND importPolicy}(u, r_2, r_3)
\]

Note that BGP route consists of additional parameters is defined as a record in the
PVS as follows:

\[
\text{routeRecord: TYPE} = \\
[# \text{dst: nat, as_path: list[AS], loc_pref: nat}]
\]
The above type definition indicates that each route record (used as \( r, r_0, ..., r_3 \) variables) has three parameters: \( dst \) denotes the destination of the route, \( as\_path \) the AS path, and \( loc\_pref \) the local preference that are manipulated by export and import policies\(^3\).

One of the main takeaways from the prior PVS formalization is its natural composable given the BGP model, where components are defined and expanded in a top-down fashion. The export and import sub-components are further defined by their respective policies \( exportPolicy \) and \( importPolicy \) which are supplied based on the actual policy configurations.

The path-vector transformation (\( pvt \)) component takes as input route \( r_1 \), and generates a new route \( r_2 \) with the same destination address (\( dst \)) and local preferences (\( loc\_pref \)). At the same time, \( u \) is prepended to the input route \( r_1 \).

Finally, we define the \( bestRoute \) component, which unlike the earlier transformations, is a select operation determining the best route for a given destination applied after aggregation. The PVS specifications are as follows:

\[
\text{bestRoute}: AXIOM
\]

\[
\text{route}(u,t,r_1) \iff (\exists r_2: (\text{bestRoute}(u,(t-1),r_2) \land r_1=r_2)
\land (\forall w: \neg \text{activeAS}(u,w,t)) \lor
(\exists (r_2,r_4): (\text{pt}\_\text{best}(u,t,r_4) \land \text{bestRoute}(u,(t-1),r_2) \land
\exists w: \text{activeAS}(u,w,t) \land
\neg \text{conflicting}(u,w,t) \land \text{best}(r_4,r_2,r_3) \land r_1=r_3) \lor
(\exists (r_3): (\text{pt}\_\text{best}(u,t,r_3) \land
(\exists w: \text{activeAS}(u,w,t) \land \text{conflicting}(u,w,t)) \land r_3=r_1))
\]

The above axiom indicates that an AS can compute its bestRoute under three scenarios (connected by the logical OR):

**Case 1:** If the AS did not receive any route updates from any neighbors in the previous time period, the bestRoute computed in the previous time period should be retained as the best route in the current period.

**Case 2:** If the AS is active and receives route updates from some of its neighbors, then the new best route is selected from its previous best route and route updates received.

**Case 3:** Similar to case 2, except that the AS receives a route update that conflicts (based on policies defined in the \( \text{conflicting} \) predicate) with its current best route. In this case, the current best route is invalidated and a new one is computed.

Next, we present the definition of \( \text{conflicting} \) in PVS as follows:

\[
\text{conflicting}(u,v,t): \text{INDUCTIVE bool} =
\exists (w,r_1,r_2,r_3): \text{bestRoute}(u,(t-1),r_1) \land \text{pt}(u,w,r_2,r_3,t)
\land (\text{member}(u,as\_path(r_3))) \land (\text{member}(w,as\_path(r_1)))
\]

A conflicting situation occurs when the best route at two neighboring ASes both set their path to go through its neighbor. Such circulated routes should be dropped and re-computation of best route is required. Note that we have explicitly define conflicting condition at neighboring ASes that are caused by the importing policies. The circulated route dropping is implicit in Griffin’s original presentation.

The definition of \( \text{pt}\_\text{best} \) as follows:

\(^3\) Note that the equivalent \( NDL\)og program expands each record definition into attributes in the predicate since it does not support nested tuples.
pt_best(u,t,r4): INDUCTIVE bool = EXISTS (w,r0): pt(u,w,r0,r4,(t-1))
   AND (FORALL r3: pt(u,w,r0,r3,(t-1)) => best(r3,r4,r4))

which says at AS u time t, r4 is the best route among all routes received by component pt, where best is defined as follows:

best(r1,r2,r3): INDUCTIVE bool =
   (loc_pref(r1)>loc_pref(r2) => r3=r1) OR
   (loc_pref(r2)>loc_pref(r1) => r3=r2) OR
   (loc_pref(r1)=loc_pref(r2) =>
      (length(as_path(r1)>length(as_path(r1))) => r3=r1) OR
      (length(as_path(r1)<length(as_path(r1))) => r3=r1) OR
      (length(as_path(r1)=length(as_path(r1))) => r3=r2))

which selects the route with higher local preference, or route with shorter path if the values of local preference are the same.

4.2 Analyzing Policy Conflicts

Figure 3 shows a specific network of three ASes based on the Disagree scenario [9, 18]. This scenario leads to delayed convergence when two neighboring ASes have conflicting policies. We demonstrate the use of DRIVER to verify that delayed convergence indeed occurs, and can be further generalized to an arbitrary network.

Specific Instance of Delayed Convergence Figure 4 shows the sequence of best route updates that leads to convergence delay. The delay is caused by route propagation sequence and the conflicting import policies at AS 1 and 2. These two ASes prefer to traverse each other to a common destination 0, hence violating conflicting defined in Section 4.1. From time 1 to 3, we note that AS 1 oscillates between two best paths [1,0] and [1,2,0], and this oscillation can repeat indefinitely.

Similarly, AS 2 oscillates between paths [2,0] and [2,1,0] indefinitely. Again the oscillation is caused by the inherent policy conflict: the best paths of 1,2,0] and [2,1,0] computed at time 2 contradict each other. The conflict however can be detected and resolved at each AS locally based on route updates. For instance, when AS 1 receives a route update for [2,1,0], it will invalidate its existing best route [1,2,0] and replace it with [1,0], resulting in the oscillation above. The protocol eventually convergences at time T given a different ordering of route updates, where only one of the two ASes sends its route updates to the other but not vice versa.
Formally Verifying Route Oscillation  Due to the compositional structure of our BGP specifications, the verification of the above behavior in PVS only requires the specification of conflicting policies at AS 1 and AS 2. More specifically, the policies at AS 1 are defined as follows:

\[
\text{importPolicy}(u, r1, r2): \text{INDUCTIVE bool} = \\
(u=1 \land \text{as\_path}(r1)=\{1,0\} \land \text{dst}(r2)\!=\!\text{dst}(r1) \\
\quad \land \text{as\_path}(r2)\!=\!\text{as\_path}(r1) \land \text{loc\_pref}(r2)=\text{dlp}) \lor \\
(u=1 \land \text{as\_path}(r1)=\{1,2,0\} \land \text{dst}(r2)\!=\!\text{dst}(r1) \\
\quad \land \text{as\_path}(r2)\!=\!\text{as\_path}(r1) \land \text{loc\_pref}(r2)=\text{dlp}+1)
\]

Where AS 1 assigns higher local preference to route received from 2 (those with \text{as\_path}=\{1,2,0\}), i.e. AS 1 prefers route going through its neighbor 2. The conflicting policies at AS 2 that prefers route via neighbor 1 is defined similarly.

To demonstrate delayed convergence, first we need to show that route oscillation such as that shown in time 1 to 3 in Figure 4 occurs. This is captured by the following PVS theorem:

\[
\text{route\_oscillate\_1: THEOREM} \\
\text{FORALL } t: (t\geq 1) \land \text{route}(1,t,r1) \land \text{route}(2,t,r2) \land \\
\text{as\_path}(r1)=\{1,0\} \land \text{as\_path}(r2)=\{2,0\} \\
\Rightarrow \text{route}(1,(t+1),r3) \Rightarrow \text{as\_path}(r3)=\{1,2,0\})
\]

which says at time \(t\), ASes 1 and 2 have routes \{1,0\} and \{2,0\} respectively implies that at the next time \(t+1\), AS 1’s route would be set to \{1,2,0\}. This theorem can be proven easily in PVS by expanding \text{import} definition introduced in Section 4.1. Similarly, we prove a similar theorem that the best route at 2 will revert back to \{2,1,0\} in the next period, hence resulting in the oscillation.

With these two theorems, we further prove the general divergence theorem at AS 1 as follows:

\[
\text{route\_diverge\_1: THEOREM} \\
\text{FORALL } t: t\geq 1 \land \text{route}(1,t,r1) \land \text{route}(1,(t+1),r2) \\
\Rightarrow \text{NOT } (r1=r2)
\]

The theorem states that at any time \(t\), the route \(r1\) stored at AS 1 will be replaced by a different route \(r2\) in the next round of route exchange at time \((t+1)\). We can specify and prove divergence at AS 2 in a similar way.

Formally Verifying Delayed Convergence  The Disagree scenario eventually converges once an alternative sequence of route updates occurs. For instance, in one time period, either AS 1 or 2 receives neighbor updates but not both. Under this circumstances, the following convergence theorem can be proved:

\[
\text{route\_converge: THEOREM} \\
\text{EXIST } T: \{\text{FORALL } t: t\geq T \Rightarrow \\
\quad \{\text{EXISTS } r1: \text{route}(1,t,r1)\Rightarrow\text{route}(1,t+1,r1)\}\}
\]

which states that there exists an initialization time \(T\) for Disagree to reach convergence state such that best route at AS 1 will reach \(r1\) and keep \(r1\) as its best route from then on.

Note that, due to the componentized nature of our formalization, the BGP axioms specified in Section 4.1 are reused in its entirety.
4.3 Generalizing the Conflict Analysis

Our earlier analysis demonstrates that delayed convergence due to route oscillation occurred in a three-AS network. In this section, we demonstrate the strength of theorem proving to scale to large and even infinite large network. Interestingly, while such a generalization proof is simple to achieve in theorem prover, it would be much harder to perform such checks using a model checker due to the state explosion problem. We also benefit from the formal argument by adopting component-based specification of BGP.

The basic observation is that route oscillation phenomenon that causes convergence delay can be generalized to arbitrary large network given the Disagree-like topology depicted in Figure 3 occurs: two connected ASes 1 and 2 have their paths p1, p2 to reach a third AS 0, and both 1 and 2 prefers routes via each other. By using component-based specification generation, we can reuse all the codes we present in 4.1, and modify the policy conflicts in Section 4.2 as follows. We show the generalized policy at AS 1 as example:

\[
\text{importPolicy}(u, r1, r2): \text{INDUCTIVE bool} = \\
(u=1 \text{ AND } \neg \text{member}(2, \text{as_path}(r1)) \text{ AND } \text{dst}(r2) = \text{dst}(r1) \\
\text{AND as_path}(r2) = \text{as_path}(r1) \text{ AND } \text{loc_pref}(r2) = \text{dlp}) \text{ OR } \\
(u=1 \text{ AND member}(2, \text{as_path}(r1)) \text{ AND } \text{dst}(r2) = \text{dst}(r1) \\
\text{AND as_path}(r2) = \text{as_path}(r1) \text{ AND } \text{loc_pref}(r2) = \text{dlp+1})
\]

The above definition states that AS 1 would assign a higher local preference to routes that includes AS 2 in the path. It is essentially a more general version of the earlier import policy, because there is no restrictions that 1 and 2 be immediate neighbors as long as they are connected.

Based on this generalized import policy, we show route oscillation by prove the \text{route_diverge1} theorem we have seen in Section 4.2. Not surprising, the proof also follows exactly the same structure as the one for Disagree. Our experience in proving route oscillations on a generalized network demonstrates the strength of theorem proving in formal argument and formal proof reuse.

4.4 Verifying a BGP Alternative

To demonstrate the flexibility and expressiveness of DRIVER, we outline an additional use case based on a BGP alternative called Hybrid Link-state and Path-vector Protocol (HLP) [17]. As its name suggests, HLP is a variant of link-state and path-vector protocol. Intuitively, in HLP, the network is organized into domains of ASes, where within each domain, ASes maintain a customer-provider hierarchy and execute the link-state (LS) protocol. Across domains, peer nodes run the fragmented path vector (FPV) protocol to exchange routes, where the internal structure of each domain is hidden.

Due to the compositional nature of our specifications, given LS and PV sub-components, HLP can be specified with minimal changes as follows:

\[
\text{FPV}(u, p, c): \text{INDUCTIVE bool} = \\
(\exists (p1, w): \text{ AND PEERS}(u, w) \text{ AND } \text{LSA}(w, p1, (c-1)) \\
\text{AND } p=[u,w]) \text{ OR } (\text{FPV}(w, p1, (c-1)) \text{ AND } p=\text{cons}(u, p1))\text{ OR } \\
(\exists (p1, w): \text{FPV}(w, p1, (c-1)) \text{ AND } \\
\text{DirectCP}(w, u) \text{ AND } \neg \text{member}(u, p1) \text{ AND } p=\text{cons}(u, p1))
\]

\text{LSA}(w, p, c)\text{ says the path } p\text{ computed by link state protocol which is used by node } w\text{ to reach } d.
FPV(w, p, c) says that path p computed by fragmented path vector protocol is used by w to reach d. The above definition therefore states that a HLP path p used to reach d at node u can be derived in the following two ways as connected by the logical OR:

**Peer AS updates:** AS u receives a route advertisement from its immediate neighbor (peer) in the form of a link state update (LSA) or a fragmented path vector update (FPV). AS u can then conclude that path (u, p1) with cost c can be used to reach d, i.e. FPV(u, p, c) can be derived.

**Provider updates:** AS u received a HLP route advertisement in the form of a fragmented path vector update FPV(w, p1, (c-1)) from its provider w, then u can conclude that it would be able to reach d with path (u, p1) with cost c.

Based on this definitions, we can then verify HLP is loop-free by proving the following PVS theorems:

```
export_guideline: THEOREM
FPV(u, p, c) => NOT (exists (p1,w): FPV(w, p1, (c-1))
     AND p=cons(u, p1) and DirectCP(u, w))
```

which states that if u can derive a new route FPV(u, p, c) by receiving advertisement FPV(w, p1, (c-1)) from w, u cannot be w’s provider, i.e. w cannot advertise to its provider u. By unfolding HLP definitions in PVS, the proof process can be carried out easily within DRIVER. Note that this theorem shows HLP adheres to the following route export guideline: at any node u, route advertised by its peer or provider is not forward to another provider.

## 5 NDlog Program Generation and Experimental Validation

Reference [19] demonstrates a natural translation from NDlog programs to formal specification recognizable by PVS based on the proof-theoretic semantics of Datalog. Interestingly, by adopting a component-based approach, as we have done in Section 4 for the BGP system, there is a straightforward translation from PVS formalization to NDlog programs. This section briefly outlines the basic translation, which serves as the basis for generating equivalent NDlog programs from verified PVS formalization. We validate the translation based on an actual experimental validation of the generated NDlog programs in a local cluster testbed.

### 5.1 PVS to NDlog Translation

To illustrate the translation, we make use of an example component T shown in Figure 6 (a) with input i and output o. The PVS axiom is as follows:

![Fig. 5. Component with n inputs and m outputs](image)

![Fig. 6. PVS Components to NDlog](image)
The above definition specifies \( T \) as a relation over \( i, o \) in terms of constraint \( C \). Note that \( C \) itself may be a conjunction of predicates that denote sub-components.

The equivalent NDlog rule is as follows:
\[
T \left( o \right) \leftarrow T \left( i \right), \ Constraint \ C \left( o, i \right)
\]

The above rule specifies component \( T \) as a rule that take \( T \left( i \right) \) as input and output \( T \left( o \right) \) if constraint \( C \) is also satisfied. Obviously, this equivalent translation can be easily generalized to components with multiple inputs and outputs.

Next, consider a component consists of several sub-components, as depicted in Figure 6 (b). Example component \( T_c \) is built from sub-components \( T_1, T_2, T_3 \). The PVS representations are as follows:

\[
T_1(i,o) \text{: INDUCTIVE bool } = \ Constraint \ C_1(i,o)
\]
\[
T_2(i,o) \text{: INDUCTIVE bool } = \ Constraint \ C_2(i,o)
\]
\[
T_3(i,i',o) \text{: INDUCTIVE bool } = \ Constraint \ C_3(i,i',o)
\]
\[
T_c(i_1,i_2,o_3) \text{: INDUCTIVE bool } = \ EXISTS \ (o_1,o_2): \ T_1(i_1,o_1) \ AND \ T_2(i_2,o_2) \ AND \ T_3(o_1,o_2,o_3)
\]

The above PVS definition specifies component \( T_c \) as a relation \( T_c \) in terms of \( T_1, T_2, T_3 \), where \( C_1, C_2, C_3 \) are constraints imposed by the sub-components. This composition can be compiled into the following equivalent NDlog rules:

\[
t_1 \ T_1\_out(o_1) \leftarrow T_1\_in(i_1), C_1(i_1,o_1).
\]
\[
t_2 \ T_2\_out(o_2) \leftarrow T_2\_in(i_2), C_2(i_2,o_2).
\]
\[
t_3 \ T_3\_out(o_3) \leftarrow T_3\_in(o_1,o_2), C_3(o_1,o_2,o_3).
\]

The above rules \( t_1-t_3 \) specifies the sub-components as three NDlog rules. Note that the predicates \( C_1, C_2, \) and \( C_3 \) may themselves be a set of conjunctive predicates used to denote sub-components. And the notion of component \( T_c \) is implicitly implied in the three rules where \( T_1, T_2 \) takes input \( i_1, i_2 \), and \( T_3 \) outputs \( o_3 \).

To annotate the above NDlog program with the appropriate location specifiers and materialized lifetimes, additional predicate schema information is required as input for the translation process.

5.2 Experimental Evaluation

We perform an experimental evaluation of equivalent NDlog declarative networking programs that implement the BGP system verified in Sections 4.1-4.3. As noted earlier, our component-based approach enables a straightforward mapping from PVS specifications to NDlog rules for execution.

Our evaluation setup is based on execution of the NDlog program in a local cluster. It consists of 15 Pentium IV 2.8GHz PCs with 2GB RAM running Fedora 9 with kernel version 2.6.23, which are interconnected by high-speed Gigabit Ethernet. For our experiments, we emulate a 60-node network, where each node is an instance of the P2 declarative networking engine [1] executing the NDlog programs. Each one of these 60 nodes runs an instance of the P2 system, and is deployed on one of the 15 physical machines in the cluster (i.e. each physical machine executes 4 P2 instances).

The main goal of our evaluation is to validate that divergence due to route oscillation can happen in practice in the presence of policy conflicts, as we have verified
with DRIVER in Section 4.2. Our equivalent NDlog program mapped from the PVS specifications consists of 13 NDlog rules.

Our input network consists of a 60-node network with 90 random bi-directional links (i.e. the average degree of each node is 3). Given this topology, our experimental setup executes the NDlog program such that all paths are computed (i.e. protocol converges). To measure bandwidth utilization required for convergence, each NDlog program executed in a distributed fashion given the input topology until all routes have been computed.

We experiment with two policy configurations: 25%-conflict and 50%-conflict, meaning that 25% and 50% respectively of all best paths are a result of policy conflicts (when two neighboring nodes prefer each other to a common destination as opposed to selecting the shortest paths). Our baseline (best case convergence) for comparison is No-conflict, where all-pairs shortest paths are computed without any conflicting policies.

In our experiments, we measure the bandwidth utilization over time (sampled at 0.1s interval) when executing the NDlog program to compute all routes until the protocol converges (i.e. no more route changes in the network). We make the following observations. First, as the percentage of conflicts increases, the protocol takes longer time to converge. While No-conflict converges in 0.3 seconds, 25%-conflict and 50%-conflict require 3.2 seconds and 3.9 seconds respectively to converge. Moreover, the protocols that involve such conflicts utilize significantly more bandwidth. The aggregate bandwidth utilization for No-conflict, 25%-conflict and 50%-conflict is 6 MB, 38 MB and 55 MB, respectively. In the extreme case where all computed routes are a result of policy conflicts, the convergence time is even longer, at 5.5 seconds, requiring 122MB in bandwidth.

In general, our experimental evaluation demonstrates that the delayed convergence proved formally in Section 4.2 is indeed observed in the actual NDlog implementation. This matched behavior suggests the utility of the overall DRIVER system: formally verified BGP protocols can be easily synthesized into implementations that preserve the verified properties. Furthermore, one can measure the performance implications (e.g. bandwidth utilization and actual convergence time) with regard to the properties that have been formally verified.

6 Related Work

We briefly compare DRIVER with existing works on network protocol verification and recent development of declarative networks.

Model checking is a collection of algorithmic techniques for checking temporal properties of system instances based on exhaustive state space exploration. Recent advances in model checking network protocol implementations include MaceMC [11] and CMC [7]. Compared to DRIVER’s use of theorem proving, these approaches are sound as well, but not complete in the sense that the large state space persistent in network protocols often prevents complete exploration of the huge system states. They are typically inconclusive and restricted to small network instances and temporal properties.

Classical theorem proving has been used in the past few decades for verification of network protocols (e.g. [2, 5, 8]). Despite extensive work, this approach is generally restricted to protocol design and standards, and cannot be directly applied to protocol implementation. A high initial investment based on domain expert knowledge is often required to develop the system specifications acceptable by some theorem prover (up to
several man-months). Therefore, even after successful proofs in the theorem prover, the actual implementation is not guaranteed to be error-free. DRIVER is hence a significant improvement over existing usage of theorem proving which typically require several man-months to develop the system specifications, a step that is reduced to a few hours through the use of declarative networking.

Runtime verification techniques provide a mechanism for checking at runtime that a system does not violate expected properties. Since declarative networks utilize a distributed query engine to execute its protocols, these checks can be expressed as monitoring queries in NDlog. However, any runtime verification scheme will incur additional runtime overheads, and subtle bugs may require a long time to be encountered. Moreover, the properties that can be checked in this case are restricted to those that can be expressed in NDlog.

The recently proposed cardinality abstraction technique [15] introduces an abstraction based on the operational semantics of the P2 declarative networking implementation. It is used to perform the checking of counting-based invariants of a Byzantine fault tolerant protocol written as P2 declarative network. Our DRIVER system has several advantages over cardinality abstraction. First, by adopting proof-theoretic semantics of Datalog, we have a natural translation to formal specifications that does not rely on any particular interpretation of the operational semantics of P2. Second, by utilizing theorem proving and higher-order logic, DRIVER is more expressive and flexible, while verification based on cardinality abstractions are limited to counting-based invariants. Finally, given the flexibility and generality of DRIVER, to our best knowledge, DRIVER is the first verification framework that enables complex policy based routing protocol deployed among autonomous ISPs to be formally analyzed, and in addition, verified specifications can be directly translated into declarative networking programs for execution.

In summary, compared with existing tools, by adopting a theorem-proving based approach that can be integrated with component-based declarative protocol development, DRIVER provides a unifying framework that bridges specification, verification, and implementation.

7 Conclusions

In this paper, we present the DRIVER system for designing, analyzing and implementing network protocols. DRIVER utilizes theorem proving, a well established verification technique where formal specifications are automatically generated to capture network semantics, and a user-driven proof process is used to establish network correctness properties. DRIVER takes as input declarative networking specifications written in the NDlog query language, and maps that automatically into formal specifications that can be directly used in existing theorem provers to verify desired protocol properties. Moreover, the verified formal specifications can be compiled into NDlog programs for execution, hence enabling the synthesis of implementation from verified protocol.

Our initial experiences suggest that DRIVER is a promising approach towards a unified framework that integrates specification, implementation, and verification. Moving forward, we have identified a few areas of future work.

First, we are working towards integrating the use of model checking techniques to specify general protocol invariants such as reachability and solvability [18] in temporal logic. Second, we are exploring more automatic proof support to make DRIVER
more approachable for non theorem proving expert. Most general-purpose theorem provers utilize an interactive proof process that requires experience of the proof system of these provers. To ease the user-directed proof construction, we plan to introduce into DRIVER network-specific proof strategies by leveraging the PVS built-in proof strategy language [2], hence lowering the barrier for adoption by network designers.

Finally, recent work on boolean satisfiability (SAT) solving and satisfiability modulo theories (SMT) [4], as well as the development in automatic first-order theorem provers have enable fully automated verification of various software and hardware problems. This provides an alternative proof automation support to PVS network-specific proof strategies developments that we plan to incorporate into DRIVER.

References