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Proving Memory Management Invariants for a Language Based on Linear Logic

Abstract

We develop a tool for the rigorous formulation and proof of properties of runtime memory management for a sample programming language based on a linear type system. Two semantics are described, one at a level of observable results of computations and one that describes linear connectives in terms of memory-management primitives. The two semantics are proven equivalent and the memory-management model is proven to satisfy fundamental correctness criteria for reference counts.

Comments

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Proving Memory Management Invariants for a Language Based on Linear Logic

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April 9, 1992

Abstract: We develop tools for the rigorous formulation and proof of properties of runtime memory management for a sample programming language based on a linear type system. Two semantics are described, one at a level of observable results of computations and one describing linear connectives in terms of memory-management primitives. The two semantics are proven equivalent and the memory-management model is proven to satisfy fundamental correctness criteria for reference counts.

1 Introduction

Although much literature on optimization of programs discusses the interaction of program execution and memory management, one does not often find formal statements and proofs regarding this interaction. For instance, a compiler for a functional language may generate code that updates an array in-place instead of copying the entire array, even though this optimization is not always safe. The compiler writer probably does not *prove* the optimization is safe: the size and complexity of real compilers is one serious impediment, but another problem is the lack of an appropriate level of abstraction at which to carry out the proofs. Most practical interpreters and compilers are written for specific machines and hence are too low-level and specialized to serve as tractable models, whereas the

usual abstract operational semantics (*e.g.*, natural semantics or structural operational semantics [21, 24]) do not provide any information about memory management.

In this paper we investigate the interaction of memory management and interpretation, defining two operational semantics of a functional language: one at a high level ordinarily used for specifying the formal operational semantics of a language, and one at a lower level that explicitly manages memory. We give proof techniques for showing that the two interpreters yield equivalent results on programs, and for showing that the desired invariants (*e.g.*, correctness of the reference counts) hold for the low-level specification.

Our main interest in developing such lower-level operational semantics is to test theses about type systems and memory management. Many new type systems based on *linear logic* [15, 16] have been proposed that hypothesize simpler garbage collection or in-place updating. (Others have proposed related type systems [10, 26].) The particular language we study is essentially the linear logic-based language of [1, 2], enhanced with the operations of PCF [9, 23]. Types are formulas in a fragment of linear logic, and terms encode proofs in the sequent calculus for this fragment (*cf.* [8, 12]).

We define two operational semantics for this language. The first is a high-level natural semantics resembling that of Abramsky [1, 2], which delays evaluation for terms of $!$ -type. This semantics describes the expected observational semantics of type-correct programs, but it does not explain the computational significance of some of the linear connectives: the weakening rule of linear logic, which discards one of its two arguments, is essentially interpreted as a “no-op”. The second operational semantics, one that mirrors current implementation technology, interprets linear connectives as memory-management primitives which carry

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out pointer manipulations and reference-counting. For instance, the weakening rule is interpreted as a *dispose* command which deallocates memory (or, more precisely, decrements reference counts).

There are three principal technical contributions of the paper. The first shows that our reference-counting interpreter implements the more abstract semantics. The second result is a proof that the reference-counting interpreter maintains a correct reference count for memory cells. The third and final result uses the model to clarify some hypotheses regarding memory management in a language with a linear logic-based type system. In particular, we find that simplification of garbage collection, along the lines suggested in [1, 15, 16], is *not* possible.

The paper is organized as follows. In Section 2 we abstractly describe the reference-counting operational semantics for λ -calculus-based functional languages. The memory model is familiar, using closures to implement λ -abstractions with environments that point to locations in memory; this implementation strategy permits some obvious optimizations through the sharing of data structures. Section 3 defines the syntax and type-checking rules of the linear logic-based language. In Section 4 we prove the correctness of the reference-counting operational semantics, and in Section 5 discuss the role of the linear type system with respect to memory management. Finally, Sections 6 and 7 conclude with a discussion of related work and possible extensions.

2 A Reference-Counting Operational Model

We first describe the low-level model in some generality, leaving vague the functional language to be interpreted.

Memory in the model is structured using environments and closures in much the same way as the SECD machine [17, 22]. Fix an infinite set of locations Loc , with the letter l denoting elements of this set. Then

- An **environment** is a finite function from variables to locations; ρ denotes an environment, and Env denotes the set of all environments. The notation $\rho(x)$ returns the location associated with variable x in ρ , and

$$(\rho[x \mapsto l])(y) = \begin{cases} l & \text{if } x = y \\ \rho(y) & \text{otherwise} \end{cases}$$

The symbol $\emptyset$ denotes the empty environment.

- A **value** is either a numeral k ; a boolean b ; a closure tuple $\langle x, M, \rho \rangle$, where ρ is an environment; or

a suspended object $\text{susp}(l)$ —a boxed value—where l is a location. The letter V denotes a value, and Value denotes the set of values.

- A **storable object** is either a value or has the form $\text{thunk}(M, \rho)$. We use S to denote a storable object, and Storable to denote the set of storable values.
- A **store** is a function $\sigma : \text{Loc} \rightarrow (\mathbb{N} \times \text{Storable})$; the left part of the returned pair denotes a reference count. Moreover, a store must contain no loops of references—*e.g.*, a function which mapped location l_1 to $\langle x, P, \rho \rangle$ where l_2 is in the range of ρ , and mapped location l_2 to $\text{susp}(l_1)$, would be illegal. Finally, the **domain** of a store σ must be finite, *viz.*, the set

$$\text{dom}(\sigma) = \{l \in \text{Loc} : \text{refcount}(l, \sigma) \geq 1\}$$

is finite. Our operational rules will always maintain these invariants. The symbol σ denotes a store, $\emptyset$ denotes the empty store, and Store denotes the set of stores. Abusing notation, we use $\sigma(l)$ to denote the storable object associated with location l , and $\text{refcount}(l, \sigma)$ to denote the reference count stored at l .

A few operations are needed for manipulating reference counts, environments, and stores. The operation $\text{update}(l, S, \sigma)$ updates the store σ and binds l to the storable object S but leaves its reference count unchanged; $\text{incr}(l, \sigma)$ increments the reference count of location l in σ and returns the resultant store; and $\text{dec}(l, \sigma)$ decrements the reference count of l and returns the resultant store. Other operations include $\text{incenv}(\rho, \sigma)$, which for every variable $x \in \text{dom}(\rho)$, increments the reference count associated with location $\rho(x)$; and $\text{dec-ptrs}(l, \sigma)$, which decrements the reference count associated with location l , then recursively decrements the reference counts of all locations stored in location l if the reference count of l falls to zero. For instance, $\text{dec-ptrs}(l, \sigma)$ has the following action:

Note that $\text{dec-ptrs}(l, \sigma)$ is easier to define if there is no loop within σ starting from location l ; this explains our earlier restriction on stores. Complete definitions of $\text{incenv}(\rho, \sigma)$ and $\text{dec-ptrs}(l, \sigma)$ may be found in Appendix A.

The last operation we need is a relation for allocating memory cells. A subset R of the product $(\text{Storable} \times \text{Store}) \times (\text{Loc} \times \text{Store})$ is an **allocation relation** if for any store σ and storable value S , there is an l' and σ' where $(S, \sigma) R (l', \sigma')$ and

- $l' \notin \text{dom}(\sigma)$;
- $\text{dom}(\sigma') = \text{dom}(\sigma) \cup \{l'\}$;
- $\sigma'(l') = S$ and $\text{refcount}(l', \sigma') = 1$; and
- For all locations $l \in \text{dom}(\sigma)$, $\sigma(l) = \sigma'(l)$ and $\text{refcount}(l, \sigma) = \text{refcount}(l, \sigma')$.

In particular, an allocation relation can reuse cells immediately when their reference count falls to zero—the usual reference-counting garbage collection scheme—or may occasionally invoke a *stop-and-copy* or *mark-and-sweep* collection [4] to determine those cells with reference count zero for use as the next cells. For specificity, we choose some specific allocation relation new , and by abuse of notation write $\text{new}(S, \sigma)$ for some pair (l', σ') such that $(S, \sigma) \text{new} (l', \sigma')$. Of course, our operational rules should be independent of the choice of allocation relation, a point formalized later.

The operational rules for a reference-counting interpreter are written in *natural* style [21]. The rules derive conclusions of the form

$$(M, \rho, \sigma) \Downarrow_{rc} (l, \sigma')$$

where the domain of ρ is exactly the free variables of M , and l is a location, in the domain of σ' , that holds the result of evaluation; [13] gives a *denotational* semantics in essentially the same form. As an example of an operational rule, suppose the language has a construct $(\text{succ } M)$ for computing the successor of a number. Then the operational rule is

$$((\text{succ } P), \rho, \sigma) \Downarrow_{rc} (l', \sigma') \quad \text{if} \quad \begin{cases} \text{suppose } (P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = n \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \text{in } (l', \sigma') = \text{new}(n + 1, \sigma_1) \end{cases}$$

Note that the reference count of location l_0 is decremented; this maintains a correct reference count on l_0 in a way described more precisely in Section 4.

The reference-counting model and corresponding evaluation of terms should seem quite familiar, but like the SECD machine we probably would not implement the model *directly*: for example, storable values in both models may occupy more than one word of storage [11, 17, 22]. The SECD machine is, in some ways, easier to implement: our rules implicitly involve stack operations, *e.g.*, the evaluation of the body of succ in

the rule above, which the SECD machine would make explicit. On the other hand, implementation details lacking from the SECD model have been made explicit in our model, in particular the management of memory.

3 A Functional Language Based on Linear Logic

Syntax: The syntax of the language is based on PCF [23] and Abramsky's linear functional language [1, 2]. Types are built over the grammar

$$s ::= \text{Nat} \mid \text{Bool} \mid (s \multimap s) \mid !s$$

where $\multimap$ denotes linear implication or linear function space, and $!$ denotes the possibility of using a value zero or more times. (A complete description of linear logic may be found in [2, 7, 8].) We use the letters s, t, u , and v to denote types. Types without leading $!$'s, *e.g.*, Nat and $(\text{Nat} \multimap \text{Bool})$, are called *linear*; a variable of linear type must be used exactly once in a term, whereas variables of $!$ -type may be used zero or more times.

The set of raw terms in the language is given by

$$\begin{aligned} M ::= & x \mid (\lambda x : s. M) \mid (M M) \mid n \mid \text{true} \mid \text{false} \mid \\ & (\text{succ } M) \mid (\text{pred } M) \mid (\text{zero? } M) \mid \\ & (\text{if } M \text{ then } M \text{ else } M) \mid (\mu x : s. M) \mid \\ & (\text{delay } M) \mid \\ & (\text{fetch } x \text{ from } M \text{ in } M) \mid \\ & (\text{share } x, y \text{ as } M \text{ in } M) \mid \\ & (\text{dispose } M \text{ before } M) \end{aligned}$$

where the letter x denotes any variable, and n denotes a numeral in $\{0, 1, 2, \dots\}$. The last four operations correspond to the special rules of linear logic, which we will explicate shortly; the other operations are the operations of PCF. The usual definitions of free and bound variables apply here, where $(\text{fetch } x \text{ from } M \text{ in } N)$ and $(\text{share } x, y \text{ as } M \text{ in } N)$ bind the variables x and x, y in N respectively. Terms are identified up to renaming of bound variables, and syntactic substitution is written $M[x := N]$ [3].

The type-checking rules of the language appear in Table 1, and are essentially those given by Abramsky in [1, 2]. The symbols Γ and Δ denote **type contexts**, which are lists of pairs $x_1 : s_1, \dots, x_n : s_n$, where each x_i is a distinct variable and each s_i is a type. In the rules, $!\Gamma$ denotes any context of the form $(x_1 : !s_1), \dots, (x_n : !s_n)$, *i.e.*, where the primary type constructor of all the types mentioned in $!\Gamma$ is $!$. The two constructs dispose and share require their arguments be of $!$ -type; these are the only constructs available for using a value zero or more times. The rule

$x : s \vdash x : s$	$\frac{\Gamma, x : s, y : t, \Delta \vdash M : t}{\Gamma, y : t, x : s, \Delta \vdash M : t}$
$\frac{\Gamma \vdash N : u \quad \Delta, x : s \vdash M : t}{\Gamma, \Delta \vdash M[x := N] : u}$	
$\frac{\Gamma, x : s \vdash M : t}{\Gamma \vdash (\lambda x : s. M) : (s \multimap t)}$	$\frac{\Gamma \vdash N : s \quad \Delta, x : t \vdash M : u}{\Gamma, \Delta, f : (s \multimap t) \vdash M[x := (f N)] : u}$
$\frac{! \Gamma \vdash M : s}{! \Gamma \vdash (\text{delay } M) : !s}$	$\frac{\Gamma, x : s \vdash M : t}{\Gamma, z : !s \vdash (\text{fetch } x \text{ from } z \text{ in } M) : t}$
$\frac{\Gamma \vdash M : t}{\Gamma, x : !s \vdash (\text{dispose } x \text{ before } M) : t}$	$\frac{\Gamma, x : !s, y : !s \vdash N : t}{\Gamma, z : !s \vdash (\text{share } x, y \text{ as } z \text{ in } N) : t}$
$\vdash n : \text{Nat}$	$\vdash \text{true}, \text{false} : \text{Bool}$
$\frac{\Gamma \vdash M : \text{Nat}}{\Gamma \vdash (\text{succ } M) : \text{Nat}}$	$\frac{\Gamma \vdash M : \text{Nat}}{\Gamma \vdash (\text{pred } M) : \text{Nat}}$
$\frac{\Gamma \vdash M : \text{Nat}}{\Gamma \vdash (\text{zero? } M) : \text{Bool}}$	$\frac{\Gamma \vdash L : \text{Bool} \quad \Delta \vdash M : s \quad \Delta \vdash N : s}{\Gamma, \Delta \vdash (\text{if } L \text{ then } M \text{ else } N) : s}$
	$\frac{! \Gamma, x : !s \vdash M : s}{! \Gamma \vdash (\mu x : !s. M) : s}$

Table 1: The type-checking rules for the linear logic-based language.

for checking recursions was suggested to us by Samson Abramsky, and will be justified in the next section when we consider the operational semantics of the language.

The rules in Table 1 have a rather different form than most type systems: our system corresponds to a *sequent-style* formulation of linear logic, whereas most type systems correspond to a *natural deduction-style* formulation [8]. One main difference between the two styles arises in the form of the rules. In natural deduction systems, one uses introduction and elimination rules, *e.g.*, the rule

$$\frac{\Gamma \vdash M : (s \multimap t) \quad \Delta \vdash N : s}{\Gamma, \Delta \vdash (M N) : t},$$

is an elimination operation for $\multimap$. Natural deduction type systems must also satisfy another criterion, the *substitutivity property* [8]: if both $\Gamma \vdash M : s$ and $\Delta, x : s \vdash N : t$ are provable, then the *proofs* may be combined by concatenation to yield a proof of $\Gamma, \Delta \vdash N[x := M] : t$. It is unclear whether

there is a true natural deduction formulation of the $\{\multimap, !\}$ -fragment for our language, although a melding of natural deduction and sequent-style typing rules is discussed in [18]. In the interest of retaining the elegant proof-theoretic properties of linear logic, we use the sequent-style rules.

Examples of Programs: To get a feel for how one writes programs, it is useful to compare the language to Standard ML [20]. For instance, consider the following program written in Standard ML:

```
let fun add x y =
  if (x = 0) then y
    else (add (x-1) (y+1))
in add 2 1
end;
```

There are two reasons that make this program *not* immediately translatable to our language. First, in the body of the recursive `add` function, the variable `x` is used more than once: in the test for 0 and in the

recursive call to **add**. Multiple uses of variables are not allowed in well-typed terms; instead, the variable names of the two uses must be made distinct through the operation **share**. On the other hand, even though the variable **y** is *mentioned* twice, it is not *used* twice since at most one branch of the **if** is executed. Second, the variable **x** appears in the **else** branch but not in the **then** branch. Again, this is not allowed; the programmer must explicitly annotate the fact that the variable **x** will not be used in the **then** branch.

The addition program in our language is

```
[μ Add : !(Nat → Nat → Nat). λx : !Nat. λy : Nat.
  share w, z as x in
  if zero? (fetch n from w in n)
  then dispose z before (dispose Add before y)
  else (fetch a from Add in a)
        (delay (pred (fetch x from z in x)))
        (succ y))
(delay 2) 1
```

There are some minor syntactic differences between this program and the above Standard ML code, *e.g.*, the recursive function is explicitly declared using μ - and λ -notation. But there are also four new constructs in this program: **delay**, **fetch**, **share**, and **dispose**. The operations **delay** and **fetch** create and destroy *objects* of $!$ -type, which are the only objects that may be shared; the operations **share** and **dispose**, on the other hand, create and destroy *references* to objects of $!$ -type.

4 Two Operational Semantics

A high-level interpreter for our language, written in natural style, appears in Table 2. The notation $M \Downarrow c$ is read “ M halts at canonical term c ”; the canonical terms have the form n , **true**, **false**, $(\lambda x. M)$, and $(\text{delay } M)$. We write $M \Downarrow$ when there exists a c such that $M \Downarrow c$. The rules maintain the invariant that only canonical forms are substituted for variables. To preserve this invariant, $(\mu x. M)$ is reduced by substituting $(\text{delay } (\mu x. M))$ in the body M and reducing the resultant term; this operational rule justifies our choice of type-checking rule for recursions.

The rules for the lower-level semantics of our language appear in Appendix A; the rules derive conclusions of the form $(M, \rho, \sigma) \Downarrow_{rc} (l', \sigma')$, where $\text{dom}(\rho)$ is *exactly* the free variables of M . The operations of our language take their names from the intuitive actions of the reference-counting interpreter. For instance, $(\text{share } x, y \text{ as } M \text{ in } N)$ first evaluates M , then binds the variables x and y to the location returned, and finally evaluates N in the new environment. This creates multiple references to cells. An-

other important collection of rules are the rules for reducing $(\text{fetch } x \text{ from } P \text{ in } Q)$; the interpreter evaluates P to a *susp’ended* object, and returns the result of evaluating the location under the *susp*, memoizing the result of computation if the *susp’ended* object points to a *thunk*. This memoization saves steps when a subsequent *fetch* of the *susp’ended* object is done, and hence resembles a *call-by-need* reduction strategy. (Accounts of operational semantics with call-by-need evaluation appear in [5, 25].)

The reference-counting interpreter must satisfy a number of invariants in order for it to be correct:

1. The evaluation of a term in a legal store must always return a store as one of its results, *i.e.*, the resultant store must contain no cycles of references and must have a finite domain.
2. A store σ is **thunk-correct** if for all l such that $\sigma(l) = \text{thunk}(M, \rho)$, $\text{refcount}(l, \sigma) = 0$ or 1 . We could reformulate the rules so that this invariant is not required, but maintaining it allows us to optimize the rules. We also say that a tuple $(l_1, \dots, l_k, \rho_1, \dots, \rho_n, \sigma)$ is **thunk-correct** if σ is.
3. An environment-store pair (ρ, σ) is called **count-correct** if for any location l' in $\text{dom}(\sigma)$, the number of references to l' in $\text{dom}(\sigma)$ plus the number of references to l' in ρ equals $\text{refcount}(l', \sigma)$. Similarly, a location-store pair (l, σ) is **count-correct** if for any l' in $\text{dom}(\sigma)$, the number of references to l' in $\text{dom}(\sigma)$ plus the number of references to l' in l (obviously either 0 or 1) equals $\text{refcount}(l', \sigma)$. We may also generalize the notion of count-correctness to tuples $(l_1, \dots, l_k, \rho_1, \dots, \rho_n, \sigma)$ in the obvious way. Intuitively, $(l_1, \dots, l_k, \rho_1, \dots, \rho_n, \sigma)$ is **count-correct** if we regard the locations mentioned by $(l_1, \dots, l_k, \rho_1, \dots, \rho_n)$ as a “root set” of pointers, and the reference counts of cells take into account these root set pointers.
4. Finally, all environments should hold pointers to *values*. First, we say that σ is **well-formed** if for every location l' , $\sigma(l') = \langle y, P, \rho' \rangle$ or $\text{thunk}(P, \rho')$ implies that for each $x \in \text{dom}(\rho')$, $\sigma(\rho'(x))$ is a value, *i.e.*, not a *thunk*. Again, we may extend this in the straightforward way to tuples: a tuple $(l_1, \dots, l_k, \rho_1, \dots, \rho_n, \sigma)$ is **well-formed** if σ is well-formed, each $\sigma(l_i)$ is a value, and for every $x \in \text{dom}(\rho_j)$, $\sigma(\rho_j(x))$ is a value.

The fact that the interpreter maintains these invariants can be verified formally. For instance,

Theorem 1 *If σ is a store and $(M, \rho, \sigma) \Downarrow_{rc} (l', \sigma')$, then σ' is a store.*

$\frac{}{n \Downarrow n}$	$\frac{}{\text{true} \Downarrow \text{true}}$	$\frac{}{\text{false} \Downarrow \text{false}}$
$\frac{M \Downarrow n}{(\text{succ } M) \Downarrow (n+1)}$	$\frac{M \Downarrow (n+1)}{(\text{pred } M) \Downarrow n}$	$\frac{M \Downarrow 0}{(\text{pred } M) \Downarrow 0}$
$\frac{M \Downarrow 0}{(\text{zero? } M) \Downarrow \text{true}}$	$\frac{M \Downarrow (n+1)}{(\text{zero? } M) \Downarrow \text{false}}$	
$\frac{L \Downarrow \text{true} \quad M \Downarrow c}{(\text{if } L \text{ then } M \text{ else } N) \Downarrow c}$	$\frac{L \Downarrow \text{false} \quad N \Downarrow c}{(\text{if } L \text{ then } M \text{ else } N) \Downarrow c}$	
$\frac{M[x := (\text{delay } \mu x. M)] \Downarrow c}{\mu x. M \Downarrow c}$		
$\frac{}{\lambda x. M \Downarrow \lambda x. M}$	$\frac{M \Downarrow \lambda x. P \quad N \Downarrow d \quad P[x := d] \Downarrow c}{(M N) \Downarrow c}$	
$\frac{}{(\text{delay } M) \Downarrow (\text{delay } M)}$	$\frac{M \Downarrow (\text{delay } P) \quad P \Downarrow c' \quad N[x := c'] \Downarrow c}{(\text{fetch } x \text{ from } M \text{ in } N) \Downarrow c}$	
$\frac{M \Downarrow d \quad N \Downarrow c}{(\text{dispose } M \text{ before } N) \Downarrow c}$	$\frac{M \Downarrow d \quad P[x, y := d] \Downarrow c}{(\text{share } x, y \text{ as } M \text{ in } P) \Downarrow c}$	

Table 2: The high-level operational semantics of the language.

The proof proceeds by a simple induction on the number of steps in $(M, \rho, \sigma) \Downarrow_{rc} (l', \sigma')$. One may also prove that the interpreter maintains correct reference counts.

Theorem 2 *If (ρ, σ) is count-correct, thunk-correct, and well-formed, and $(M, \rho, \sigma) \Downarrow_{rc} (l', \sigma')$, then (l', σ') is count-correct, thunk-correct, and well-formed.*

Proof: (Hint) By induction on the height of $\Downarrow_{rc}$, where we prove the following generalization:

Suppose the tuple $(l_1, \dots, l_k, \rho, \rho_1, \dots, \rho_n, \sigma)$ is well-formed, count-correct, and thunk-correct. If $(M, \rho, \sigma) \Downarrow_{rc} (l', \sigma')$, then the tuple $(l', l_1, \dots, l_k, \rho_1, \dots, \rho_n, \sigma')$ is well-formed, count-correct, and thunk-correct.

It is worth considering part of one of the cases of this induction. Recall the rule for reducing successors:

$$((\text{succ } P), \rho, \sigma) \Downarrow_{rc} (l', \sigma') \quad \text{if} \quad \begin{cases} \text{suppose } (P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = n \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \text{in } (l', \sigma') = \text{new}(n+1, \sigma_1) \end{cases}$$

The rule first reduces the operand of `succ` using the reduction $(P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0)$. By the induction hypothesis, we assume that if $(l_1, \dots, l_k, \rho, \rho_1, \dots, \rho_n, \sigma)$ is count-correct, thunk-correct, and well-formed, then so is $(l_0, l_1, \dots, l_k, \rho_1, \dots, \rho_n, \sigma_0)$. The result is then tested to make sure it is a numeral n . Next, the reference count of location l_0 is decremented yielding a new store σ_1 ; thus, $(l_1, \dots, l_k, \rho_1, \dots, \rho_n, \sigma_1)$ is count-correct. Finally, a new cell l' is allocated to store the result; thus, the result $(l', l_1, \dots, l_k, \rho_1, \dots, \rho_n, \sigma')$ is count-correct. Proving that the result is well-formed and thunk-correct is easier, and the other cases of the induction are similar to this one. ■

Finally, one may prove that alternative choices of the allocation relation “new” do not result in different results. For specificity, if f is an allocation relation, let $\Downarrow_{rc,f}$ be the relation defined (as in the Appendix) by using f in the place of `new`. Then

Theorem 3 *Suppose f and g are allocation relations, and $(M, \rho, \sigma) \Downarrow_{rc,f} (l', \sigma')$ and $\sigma'(l') = n, \text{true}, \text{or false}$. Then $(M, \rho, \sigma) \Downarrow_{rc,g} (l'', \sigma'')$ and $\sigma''(l'') = \sigma'(l')$.*

The proof again follows by an induction on the proof of $(M, \rho, \sigma) \Downarrow_{rc,f} (l', \sigma')$, using a strengthened induction hypothesis describing the equivalence of two stores.

It is harder to prove that the reference-counting interpreter correctly implements the high-level interpreter.

Theorem 4 *For any closed term M , $M \Downarrow c$ iff $(M, \emptyset, \emptyset) \Downarrow_{rc} (l', \sigma')$. Moreover, $c = n$ iff $\sigma'(l') = n$, and similarly for true, false.*

Proof: (Hint) The main difficulty in proving this theorem lies in the memoization done under `susp`'ended objects in the reference-counting interpreter. This memoization causes the reference-counting interpreter to return results that are “more evaluated” than results returned by the high-level interpreter. To overcome these difficulties, we first define a relation $M \leq N$, read “ M is more evaluated than N ,” in which closed subterms of N may be reduced via $\Downarrow$ to obtain M . We also define the functions $\text{valof}(M, \rho, \sigma)$ and $\text{valofcell}(l, \sigma)$ for extracting closed terms out of an environment-store pair or out of a cell-store pair. The definition of $\leq$ can be extended to stores in the obvious way. We then prove the following by induction on the height of $\Downarrow_{rc}$ and $\Downarrow$:

Suppose the tuples $(l_1, \dots, l_k, \rho, \rho_1, \dots, \rho_n, \sigma)$ and $(l_1, \dots, l_k, \rho, \rho_1, \dots, \rho_n, \sigma')$ are well-formed, thunk-correct, and count-correct, and $\sigma' \leq \sigma$. Then $\text{valof}(M, \rho, \sigma) \Downarrow c$ iff $(M, \rho, \sigma') \Downarrow_{rc} (l'', \sigma'')$ where $\sigma'' \leq \sigma'$ and $\text{valofcell}(l'', \sigma'') \leq c$.

Since the only terms $\leq$ basic constants are the constants themselves, the theorem follows directly. ■

5 The Linear Type System and Memory Management

It would seem that only `susp`'ended objects may have more than one pointer to them. This would lead to many potential optimizations: `succ` and `pred` could, for instance, be implemented using in-place updating. Unfortunately, this is not the case. Consider the closed typable term

```
share y, z as (delay 2)
  in (fetch g from y in (fetch h from z in M))
```

for some term M . During evaluation, the value 2 is placed in some cell l_1 , and y and z are bound to l_0 which holds `susp`(l_1). When the evaluation continues, the variable g is also bound to l_1 , so l_1 has more than one pointer to it at some intermediate stage. We could, of course, explicitly copy the cell containing 2 and bind g to a new location holding 2, and thus avoid having more than one pointer to a non-`susp`'ended cell.

But if 2 were instead some closure, we would have to copy all cells referenced by the environment of the closure, and then recursively copy those cells referenced by those cells copied. It is clear that this would be a very expensive operation. There may well be other general ways to guarantee that the reference counts of non-`susp`'ended objects are always 1, but we have not found any honest, efficient means yet.

Linear constructs do provide information, though, about *when* certain data structures may be garbage collected. Consider, for example, the terms

```
N1 = ((λx. if B then (dispose x before P)
                else (dispose x before Q)) M)
N2 = ((λx. dispose x before if B then P else Q) M)
```

N_2 `dispose`'s the cell bound to M as early as possible; this may potentially free enough space so that the computation of B does not run into problems. This gives the programmer fine control over memory management with some degree of safety: type-correct programs do not `dispose` pointers unless it is safe to do so, insuring the absence of dangling pointers which may happen in languages like C with memory-management primitives.

6 Relation to Previous Work

Our study has focused primarily on abstract models of memory management for the purposes of detecting certain optimizations. Jones and Muchnick [14], Hudak [13], and Deutsch [6] use abstract interpretation to deduce the correctness of certain compiler optimizations, *e.g.*, in-place updating of arrays. Only Hudak's work explicitly incorporates memory management (through reference-counting), although it probably could be added to the other models. The main difference between these papers and ours is that we prove more properties of our low-level operational semantics: first, that it matches the higher-level description, and second, that it satisfies the necessary invariants. Such principles may be harder to state and prove in the models of these papers.

Others have discovered that objects with linear type do not necessarily have one pointer to them. For instance, Wadler [26] noticed (although informally) in the context of graph reduction, subgraphs corresponding to terms with linear type do not necessarily have one pointer to them. Wadler's language, though, is slightly different than ours—he considers terms without the linear constructs of `delay`, `fetch`, `dispose`, and `share`, and attempts to infer the position of these connectives in raw λ -terms.

Lincoln and Mitchell [19], on the other hand, use almost exactly the same language as ours with slightly

different typing rules. They also define an abstract machine with two heaps, one for objects with only one pointer to them, and one for objects with possibly more than one pointer to them. As with our work, Lincoln and Mitchell find that objects with linear type cannot be guaranteed to have exactly one pointer to them; they may need to be placed in the second heap. It seems that further work is needed to prove that their abstract machine exploits the linear type system in any significant way.

7 Conclusion

The goal of our work here is to develop tools for formally *establishing* applications for linear logic, as well as proving memory-related optimizations correct. What we have done should also be relevant to other investigations that involve rigorous demonstrations of properties at several levels of semantic abstraction, such as work on efficient implementations of call-by-name evaluation.

The linear language may have uses beyond guaranteeing a safe “dispose” operation. For instance, programs in functional languages, *e.g.*, PCF, can be translated into the syntax of our language in a relatively straightforward manner [7]. Our language may therefore provide a suitable intermediate representation for finding memory management optimizations, just as continuation-passing style is used to find control optimizations. The language may also be useful as a tool for profiling memory usage in standard functional languages.

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A Reference-Counting Interpreter

$$\begin{aligned}
\text{incenv}(\rho, \sigma) &= \begin{cases} \sigma_n, & \text{where the domain of } \rho \text{ is } \{x_1, \dots, x_n\} \\ & \sigma_1 = \text{incr}(\rho(x_1), \sigma) \\ & \vdots \\ & \sigma_n = \text{incr}(\rho(x_n), \sigma_{n-1}) \end{cases} \\
\text{decenv-ptrs}(\rho, \sigma) &= \begin{cases} \sigma_n, & \text{where the domain of } \rho \text{ is } \{x_1, \dots, x_n\} \\ & \sigma_1 = \text{dec-ptrs}(\rho(x_1), \sigma) \\ & \vdots \\ & \sigma_n = \text{dec-ptrs}(\rho(x_n), \sigma_{n-1}) \end{cases} \\
\text{dec-ptrs}(l, \sigma) &= \begin{cases} \text{dec}(l, \sigma) & \text{if } \sigma(l) = n, \text{ true, or false} \\ \text{dec}(l, \sigma) & \text{if } \text{refcount}(l, \sigma) > 1 \\ \text{dec-ptrs}(l', \sigma') & \text{if } \text{refcount}(l, \sigma) = 1, \sigma' = \text{dec}(l, \text{update}(l, 0, \sigma)), \text{ and } \\ & \sigma(l) = \text{susp}(l') \\ \text{decenv-ptrs}(\rho, \sigma') & \text{if } \text{refcount}(l, \sigma) = 1, \sigma' = \text{dec}(l, \text{update}(l, 0, \sigma)), \text{ and } \\ & \sigma(l) = \langle x, M, \rho \rangle \\ \text{decenv-ptrs}(\rho, \sigma') & \text{if } \text{refcount}(l, \sigma) = 1, \sigma' = \text{dec}(l, \text{update}(l, 0, \sigma)), \text{ and } \\ & \sigma(l) = \text{thunk}(M, \rho) \end{cases}
\end{aligned}$$

- (1) $(x, \rho, \sigma) \Downarrow_{rc} (\rho(x), \sigma)$
- (2) $(n, \rho, \sigma) \Downarrow_{rc} \text{new}(n, \sigma)$
- (3) $(\text{true}, \rho, \sigma) \Downarrow_{rc} \text{new}(\text{true}, \sigma)$
- (4) $(\text{false}, \rho, \sigma) \Downarrow_{rc} \text{new}(\text{false}, \sigma)$
- (5) $((\lambda x. P), \rho, \sigma) \Downarrow_{rc} \text{new}(\langle x, P, \rho \rangle, \sigma)$
- (6) $((\text{delay } P), \rho, \sigma) \Downarrow_{rc} \text{new}(\text{susp}(l_0), \sigma_0), \text{ where } (l_0, \sigma_0) = \text{new}(\text{thunk}(P, \rho), \sigma)$

$$(7) \quad ((\text{succ } P), \rho, \sigma) \Downarrow_{rc} (l', \sigma') \text{ if } \begin{cases} \text{suppose } (P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = n \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \text{in } (l', \sigma') = \text{new}(n+1, \sigma_1) \end{cases}$$

$$(8) \quad ((\text{pred } P), \rho, \sigma) \Downarrow_{rc} (l', \sigma') \text{ if } (P, \rho, \sigma) \Downarrow_{rc} (l', \sigma') \text{ and } \sigma'(l') = 0$$

$$(9) \quad ((\text{pred } P), \rho, \sigma) \Downarrow_{rc} (l', \sigma') \text{ if } \begin{cases} \text{suppose } (P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = n+1 \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \text{in } (l', \sigma') = \text{new}(n, \sigma_1) \end{cases}$$

$$(10) \quad ((\text{zero? } P), \rho, \sigma) \Downarrow_{rc} (l', \sigma') \text{ if } \begin{cases} \text{suppose } (P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = 0 \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \text{in } (l', \sigma') = \text{new}(\text{true}, \sigma_1) \end{cases}$$

$$(11) \quad ((\text{zero? } P), \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_1) \text{ if } \begin{cases} \text{suppose } (P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = n+1 \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \text{in } (l', \sigma') = \text{new}(\text{false}, \sigma_1) \end{cases}$$

- (12) $(\text{if } N \text{ then } P \text{ else } Q, \rho_1 \cup \rho_2, \sigma) \Downarrow_{rc} (l', \sigma')$ if $\left\{ \begin{array}{l} \text{suppose } (N, \rho_1, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = \text{true} \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \text{in } (P, \rho_2, \sigma_1) \Downarrow_{rc} (l', \sigma') \end{array} \right.$
- (13) $(\text{if } N \text{ then } P \text{ else } Q, \rho_1 \cup \rho_2, \sigma) \Downarrow_{rc} (l', \sigma')$ if $\left\{ \begin{array}{l} \text{suppose } (N, \rho_1, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = \text{false} \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \text{in } (Q, \rho_2, \sigma_1) \Downarrow_{rc} (l', \sigma') \end{array} \right.$
- (14) $((P \ Q), \rho_1 \cup \rho_2, \sigma) \Downarrow_{rc} (l', \sigma')$ if $\left\{ \begin{array}{l} \text{suppose } (P, \rho_1, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \text{refcount}(l_0, \sigma_0) = 1 \\ \sigma_0(l_0) = \langle x, N, \rho' \rangle \\ \text{let } \sigma_2 = \text{dec}(l_0, \sigma_0) \\ (Q, \rho_2, \sigma_2) \Downarrow_{rc} (l_3, \sigma_3) \\ \text{in } (N, \rho'[x \mapsto l_3], \sigma_3) \Downarrow_{rc} (l', \sigma') \end{array} \right.$
- (15) $((P \ Q), \rho_1 \cup \rho_2, \sigma) \Downarrow_{rc} (l', \sigma')$ if $\left\{ \begin{array}{l} \text{suppose } (P, \rho_1, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \text{refcount}(l_0, \sigma_0) > 1 \\ \sigma_0(l_0) = \langle x, N, \rho' \rangle \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \sigma_2 = \text{increnv}(\rho', \sigma_1) \\ (Q, \rho_2, \sigma_2) \Downarrow_{rc} (l_3, \sigma_3) \\ \text{in } (N, \rho'[x \mapsto l_3], \sigma_3) \Downarrow_{rc} (l', \sigma') \end{array} \right.$
- (16) $((\mu x. P), \rho, \sigma) \Downarrow_{rc} (l', \sigma')$ if $\left\{ \begin{array}{l} \text{let } (l_0, \sigma_0) = \text{new}(\text{thunk}((\mu x. P), \rho), \sigma) \\ (l_1, \sigma_1) = \text{new}(\text{susp}(l_0), \sigma_0) \\ \sigma_2 = \text{increnv}(\rho, \sigma_1) \\ \text{in } (P, \rho[x \mapsto l_1], \sigma_2) \Downarrow_{rc} (l', \sigma') \end{array} \right.$
- (17) $((\text{dispose } P \text{ before } Q), \rho_1 \cup \rho_2, \sigma) \Downarrow_{rc} (l', \sigma')$ if $\left\{ \begin{array}{l} \text{suppose } (P, \rho_1, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \text{let } \sigma_1 = \text{dec-ptrs}(l_0, \sigma_0) \\ \text{in } (Q, \rho_2, \sigma_1) \Downarrow_{rc} (l', \sigma') \end{array} \right.$
- (18) $((\text{share } x, y \text{ as } P \text{ in } Q), \rho_1 \cup \rho_2, \sigma) \Downarrow_{rc} (l', \sigma')$ if $\left\{ \begin{array}{l} \text{suppose } (P, \rho_1, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \text{let } \sigma_1 = \text{incr}(l_0, \sigma_0) \\ \text{in } (Q, \rho_2[x, y \mapsto l_0], \sigma_1) \Downarrow_{rc} (l', \sigma') \end{array} \right.$
- (19) $((\text{fetch } x \text{ from } P \text{ in } Q), \rho, \sigma) \Downarrow_{rc} (l', \sigma')$ if $\left\{ \begin{array}{l} \text{suppose } (P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = \text{susp}(l_1) \\ \text{refcount}(l_0, \sigma_0) = 1 \\ \sigma_0(l_1) = V \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \text{in } (Q, \rho[x \mapsto l_1], \sigma_1) \Downarrow_{rc} (l', \sigma') \end{array} \right.$

$$(20) \quad ((\text{fetch } x \text{ from } P \text{ in } Q), \rho, \sigma) \Downarrow_{rc} (l', \sigma') \quad \text{if} \quad \left\{ \begin{array}{l} \text{suppose } (P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = \text{susp}(l_1) \\ \text{refcount}(l_0, \sigma_0) > 1 \\ \sigma_0(l_1) = V \\ \text{let } \sigma_1 = \text{incr}(l_1, \sigma_0) \\ \sigma_2 = \text{dec}(l_0, \sigma_1) \\ \text{in } (Q, \rho[x \mapsto l_1], \sigma_2) \Downarrow_{rc} (l', \sigma') \end{array} \right.$$

$$(21) \quad ((\text{fetch } x \text{ from } P \text{ in } Q), \rho, \sigma) \Downarrow_{rc} (l', \sigma') \quad \text{if} \quad \left\{ \begin{array}{l} \text{suppose } (P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = \text{susp}(l_1) \\ \text{refcount}(l_0, \sigma_0) = 1 \\ \sigma_0(l_1) = \text{thunk}(R, \rho') \\ \text{let } \sigma_1 = \text{dec}(l_0, \sigma_0) \\ \sigma_2 = \text{dec}(l_1, \sigma_1) \\ (R, \rho', \sigma_2) \Downarrow_{rc} (l_3, \sigma_3) \\ \text{in } (Q, \rho[x \mapsto l_3], \sigma_3) \Downarrow_{rc} (l', \sigma') \end{array} \right.$$

$$(22) \quad ((\text{fetch } x \text{ from } P \text{ in } Q), \rho, \sigma) \Downarrow_{rc} (l', \sigma') \quad \text{if} \quad \left\{ \begin{array}{l} \text{suppose } (P, \rho, \sigma) \Downarrow_{rc} (l_0, \sigma_0) \\ \sigma_0(l_0) = \text{susp}(l_1) \\ \text{refcount}(l_0, \sigma_0) > 1 \\ \sigma_0(l_1) = \text{thunk}(R, \rho') \\ \text{let } \sigma_1 = \text{incrcnv}(\rho', \sigma_0) \\ (R, \rho', \sigma_1) \Downarrow_{rc} (l_2, \sigma_2) \\ \sigma_3 = \text{update}(l_0, \text{susp}(l_2), \sigma_2) \\ \sigma_4 = \text{dec-ptrs}(l_1, \sigma_3) \\ \sigma_5 = \text{dec}(l_0, \text{incr}(l_2, \sigma_4)) \\ \text{in } (Q, \rho[x \mapsto l_2], \sigma_5) \Downarrow_{rc} (l', \sigma') \end{array} \right.$$