Effect of Annealing on the Contact Resistance of Aluminum on a p-type Substrate

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Effect of Annealing on the Contact Resistance of Aluminum on a p-type Substrate

Abstract
Aluminum contacts are widely used to form both ohmic and rectifying contacts. The process to form these contacts involves annealing, thus it is important to study the effect of annealing on the electrical properties of the contacts. Here, we present a way to measure the contact resistance of aluminum contacts formed on a p-type silicon substrate. It was found the contact resistivity decreased by an average of 18%. It was thus found that annealing at 400°C in a forming gas environment improves the electrical properties of aluminum contacts.

Keywords
Metallization, contact resistance, aluminum contacts, contact resistivity

Disciplines
Effect of annealing on the contact resistance of Aluminum on a p-type substrate

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Aluminum contacts are widely used to form both ohmic and rectifying contacts. The process to form these contacts involves annealing, thus it is important to study the effect of annealing on the electrical properties of the contacts. Here, we present a way to measure the contact resistance of aluminum contacts formed on a p-type silicon substrate. It was found the contact resistivity decreased by an average of 18%. It was thus found that annealing at 400 °C in a forming gas environment improves the electrical properties of aluminum contacts.

Key Words: Metallization, Contact Resistance, Aluminum contacts, Contact resistivity

I. Introduction

Metal-semiconductor contacts have been widely investigated in the recent years due to the fact that these contacts affect the performance of electronic devices. Among the metal-semiconductor contacts, Aluminum contacts are considered to be at the heart of metallization in silicon integrated circuits, as they are used both as ohmic contacts and as rectifying contacts. It has also been reported that aluminum is uniquely suited as a single-element metallization system. Aluminum is preferred because of its ease of processing, ability to reduce native silicon dioxide chemically and its low resistivity. Usually the metallization scheme for aluminum includes depositing aluminum followed by annealing at 400-450°C. This annealing is done to promote adhesion and eliminate electrical damage associated with the metal deposition process. In this paper we investigate the effect of annealing on the contact resistance and resistivity of Aluminum metal contacts on p-type Silicon substrate.

II. Theory

Contact resistance refers to the resistance associated with the metal-semiconductor barrier at the interface between the semiconductor and the metal contact. The easiest technique to measure the contact resistance is the two-terminal contact resistance method. For both the contacts on the top surface, the two terminal contact resistance method can be implemented as shown in Fig. 1. The equation to calculate the total resistance \( R_t \) using this method is given by Eq. (1)

\[
R_t = 2R_c + 2R_{sp} + R_s + 2R_p
\]  

where \( R_c, R_{sp}, R_s \) and \( R_p \) are the contact resistance, spreading resistance, semiconductor resistance and probe resistance, respectively. The semiconductor resistance \( R_s \) is usually negligible. The spreading resistance for the method we used and for small contacts can be approximated using Eq. (2)

\[
R_{sp} = Cx(\rho/(4r))
\]  

where \( \rho, C \) and \( r \) are semiconductor resistivity, correction factor and contact radius, respectively. For widely separated contacts and semi-infinite substrate the correction factor \( C \) has a value of 1.

III. Experiment

Fig. 2 shows the process flow for making the device used for measuring contact resistance before and after annealing. For making the device, we started with a p-type silicon wafer on which silicon dioxide of approximately 280nm was grown thermally on both the sides. The silicon dioxide was stripped off from the back of the wafer using 1% hydrofluoric acid and a layer of aluminum of approximately 115 nm is sputtered at the back of the wafer for making a back contact. The fist level lithography is
performed using Heidelberg direct-write and then silicon dioxide was also etched from the exposed area as shown in Fig. 2. Later, aluminum of approximately 115nm is sputtered on the top of the wafer. The 2nd level lithography and stripping of Aluminum leads to the formation of the contacts for the device. The electrical measurements are taken before and after annealing at 400°C in forming gas (a mixture of hydrogen and nitrogen gas) for 15 minutes. Since it is important for measuring the contact resistance accurately, we made small radius contacts ranging from 1µm to 9µm. The CAD file and the final device can be seen in Fig. 3 and Fig. 4, respectively.

IV. Results and Discussion

The bulk resistivity (\(\rho\)) of the device was measured using the four point probe method. The value of resistivity before and after annealing were 0.00314 \(\Omega\)m and 0.00216 \(\Omega\)m, respectively. It can be thus be seen that there is a 31% decrease in the resistivity upon annealing. The probe resistance was calculated by shorting the probes. The probe resistance \(R_p\) was found to be 1.714\(\Omega\). The total resistance \(R_t\) was measured using the scheme shown in Fig. 1 and the Voltage versus Current graphs from which the total resistance for each contact was measured are shown in Fig. 5. The total resistance for the contacts before and after annealing calculated from the Voltage versus Current graphs are shown in Table I.

After finding bulk resistivity (\(\rho\)), the spread resistance (\(R_{sp}\)), and probe resistance (\(R_p\)), the contact resistance was calculated using Eq. 1. Table I shows the contact resistance before and after annealing for contacts of different sizes. After annealing, the contact resistance decreases for all devices. The average percentage decrease in the contact resistance is around 14.8%. Table II shows the change in contact resistivity before and after annealing. The average contact resistivity decreased by 18%. The value of contact resistivity before and after annealing of 6.5x10^{-6} \(\Omega\)cm^2 and 5.4x10^{-6} \(\Omega\)cm^2 respectively are similar to that measured by Proctor et al. and Berger et al.\(^7\)\(^8\). The graph in Fig. 6 shows the difference in contact resistance upon annealing. The decrease in contact resistance is likely to be due to the aluminum intermixing with a native oxide on the surface of the silicon layer which leads to the formation of a more conductive interface. It may also be due to the fact that the annealing process helps promote adhesion and reduces any electrical damage caused during deposition. The apparent reduction in the bulk resistivity is not understood at this time. Some of the raw data and calculations can be found in Appendix A.

### Table I. Total resistance (\(R_t\)) before and after annealing

<table>
<thead>
<tr>
<th>Radius (µm)</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>6</th>
</tr>
</thead>
<tbody>
<tr>
<td>(R_t) before (Ω)</td>
<td>3091.7</td>
<td>2890.8</td>
<td>2839.1</td>
<td>2737</td>
</tr>
<tr>
<td>(R_t) (After) (Ω)</td>
<td>2408.9</td>
<td>2389.1</td>
<td>2274.9</td>
<td>2234.5</td>
</tr>
<tr>
<td>(R_r) (Before) (Ω)</td>
<td>1518.1</td>
<td>2102.3</td>
<td>2312.3</td>
<td>2471.9</td>
</tr>
<tr>
<td>(R_r) (After) (Ω)</td>
<td>1202.3</td>
<td>1784.1</td>
<td>1870.4</td>
<td>2030.5</td>
</tr>
</tbody>
</table>

### Table II. Contact resistivity before and after annealing

<table>
<thead>
<tr>
<th>Radius (µm)</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>6</th>
</tr>
</thead>
<tbody>
<tr>
<td>(\rho_{c}) (Before) (Ωcm^2)</td>
<td>4.8E-05</td>
<td>6.6E-05</td>
<td>7.3E-05</td>
<td>7.8E-05</td>
</tr>
<tr>
<td>(\rho_{c}) (After) (Ωcm^2)</td>
<td>3.8E-05</td>
<td>5.6E-05</td>
<td>5.9E-05</td>
<td>6.4E-05</td>
</tr>
</tbody>
</table>
FIG. 5. Voltage versus current characteristics for contacts of radius 1 µm, 2 µm, 3 µm, and 6 µm.

FIG. 6. Change of contact resistance after annealing in contacts with varied radius.

V. Conclusion

This experiment found that after annealing at 400°C in forming gas for 15 min, which is the normally used annealing temperature and time for Aluminum contacts on Silicon, the contact resistance decreased by an average of 14.8%. This decrease in contact resistance leads to the formation of better Ohmic contacts and thus device performance. It was also found that the resistivity decreased by 31%. The average contact resistivity decreased by 18%. In conclusion, annealing leads to a reduction in both resistivity and contact resistance, thus leading to the formation of better electrical contacts.

VI. Acknowledgements

This work was performed at the Singh Center for Nanotechnology at the University of Pennsylvania, a member of the National Nanotechnology Coordinated Infrastructure (NNCI) network, which is supported by the National Science Foundation (Grant NNCI-1542153).

A. Appendix A

The raw data of all the devices can be seen in Table III and IV. From these raw data, the contact resistance was calculated. The raw data used to calculate the probe resistance is shown in Table V.

The contact resistance was calculated using Eq. 1. The total resistance was found by plotting a linear trend-line for the data in Tables III and IV. For calculating the spreading resistance, the bulk resistivity was calculated using the four-point probe method. Finally, the probe resistance was calculated by plotting a trend-line for the data in Table V. By knowing the spread resistance, the total resistance and the probe resistance the contact resistance can be calculated.
FIG. 7. Voltage vs Current curve for measuring probe resistance. The slope is the probe resistance.

**TABLE III. Raw data before annealing**

<table>
<thead>
<tr>
<th>Voltage(V)</th>
<th>Current(A)-1µm</th>
<th>Current(A)-2µm</th>
<th>Current(A)-2µm</th>
<th>Current(A)-6µm</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0.01</td>
<td>0.000003</td>
<td>3.3E-06</td>
<td>0.00003798</td>
<td>4.52E-06</td>
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<tr>
<td>0.02</td>
<td>0.00000599</td>
<td>6.78E-06</td>
<td>0.00007266</td>
<td>8.03E-06</td>
</tr>
<tr>
<td>0.03</td>
<td>0.00000898</td>
<td>1.03E-05</td>
<td>0.0001074</td>
<td>1.15E-05</td>
</tr>
<tr>
<td>0.04</td>
<td>0.000012973</td>
<td>1.38E-05</td>
<td>0.00014225</td>
<td>1.5E-05</td>
</tr>
<tr>
<td>0.05</td>
<td>0.000016215</td>
<td>1.72E-05</td>
<td>0.00017701</td>
<td>1.86E-05</td>
</tr>
<tr>
<td>0.06</td>
<td>0.000019458</td>
<td>2.07E-05</td>
<td>0.00021178</td>
<td>2.21E-05</td>
</tr>
<tr>
<td>0.07</td>
<td>0.0000227</td>
<td>2.42E-05</td>
<td>0.00024656</td>
<td>2.56E-05</td>
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<tr>
<td>0.08</td>
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<td>2.77E-05</td>
<td>0.00028132</td>
<td>2.91E-05</td>
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<tr>
<td>0.09</td>
<td>0.000029186</td>
<td>3.12E-05</td>
<td>0.0003161</td>
<td>3.26E-05</td>
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<tr>
<td>0.1</td>
<td>0.000032427</td>
<td>3.47E-05</td>
<td>0.00035086</td>
<td>3.61E-05</td>
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</tbody>
</table>

**TABLE IV. Raw data after annealing**

<table>
<thead>
<tr>
<th>Voltage(V)</th>
<th>Current(A)-1µm</th>
<th>Current(A)-2µm</th>
<th>Current(A)-2µm</th>
<th>Current(A)-6µm</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0.01</td>
<td>4.33E-06</td>
<td>0.000004427</td>
<td>4.41E-06</td>
<td>4.74E-06</td>
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<tr>
<td>0.02</td>
<td>8.7E-06</td>
<td>8.7352E-06</td>
<td>8.8E-06</td>
<td>9.46E-06</td>
</tr>
<tr>
<td>0.03</td>
<td>1.29E-05</td>
<td>0.00012947</td>
<td>1.32E-05</td>
<td>1.36E-05</td>
</tr>
<tr>
<td>0.04</td>
<td>1.71E-05</td>
<td>0.0001726</td>
<td>1.76E-05</td>
<td>1.8E-05</td>
</tr>
<tr>
<td>0.05</td>
<td>2.11E-05</td>
<td>0.00021632</td>
<td>2.2E-05</td>
<td>2.24E-05</td>
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<tr>
<td>0.06</td>
<td>2.52E-05</td>
<td>0.00025365</td>
<td>2.64E-05</td>
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<tr>
<td>0.07</td>
<td>2.91E-05</td>
<td>0.00029375</td>
<td>3.08E-05</td>
<td>3.15E-05</td>
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<tr>
<td>0.08</td>
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<td>0.00033358</td>
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<td>3.55E-05</td>
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<tr>
<td>0.09</td>
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<td>0.00037284</td>
<td>3.96E-05</td>
<td>4E-05</td>
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<tr>
<td>0.1</td>
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<td>0.00041294</td>
<td>4.4E-05</td>
<td>4.48E-05</td>
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</tbody>
</table>

**TABLE V. Voltage and current measurements for calculating probe resistance**

<table>
<thead>
<tr>
<th>Voltage(V)</th>
<th>Current(A)</th>
</tr>
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<tr>
<td>0.001</td>
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<tr>
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<tr>
<td>0.006</td>
<td>0.003502</td>
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