January 1974

Research on Automatic Program Generation

Jesus A. Ramirez  
*University of Pennsylvania*

N. Adam Rin  
*University of Pennsylvania*

Maxine Brown  
*University of Pennsylvania*

Noah S. Prywes  
*University of Pennsylvania*

Follow this and additional works at: [http://repository.upenn.edu/cis_reports](http://repository.upenn.edu/cis_reports)

Recommended Citation


This paper is posted at ScholarlyCommons. [http://repository.upenn.edu/cis_reports/682](http://repository.upenn.edu/cis_reports/682)

For more information, please contact libraryrepository@pobox.upenn.edu.
Research on Automatic Program Generation

Abstract

Automatic Program Generation Research has been conducted under Contract N00014-67-A-0216-0014, since 1971. The objective of the research has been to provide software generation directly from user specifications.

Initially, the research concentrated on a specific application, of generating file conversion programs. A first report on this subject was authored by Diane Pirog Smith, in December 1971, titled, "An Approach to Data Description and Conversion". Subsequently, a software system for automating this function was implemented by Jesus A. Ramirez and described by him in a report titled, "Automatic Generation of data conversion-programs Using A Data Description Language (DIL)". Currently, the objective of the research has been broadened to develop a user language and a software system for automatic generation of business oriented programs.

This technical report contains a collection of three papers intended to summarize the results of past research and the direction of current research. The first paper, by Ramirez, Rin and Prywes is a summary of Dr. Ramirez's report and dissertation cited above. The second paper, titled, "An Overview of a System for Automatic Generation of File Conversion Programs " by N. Adam Rin and Maxine Brown is intended to provide a more user oriented view based on their experience in utilization of the system developed by Ramirez.

There have been many research activities and a large number of papers in this area. The third paper, "Automatic Generation of Software Systems: A Survey," by N. Prywes serves to relate the research underway at the University of Pennsylvania, to the many recent and current activities in this field. It also aims to clearly define short and long term objectives and methodologies.

Comments


This technical report is available at ScholarlyCommons: http://repository.upenn.edu/cis_reports/682
UNIVERSITY OF PENNSYLVANIA
The Moore School of Electrical Engineering
Department of Computer and Information Science

TECHNICAL REPORT

RESEARCH ON

AUTOMATIC PROGRAM GENERATION

Project Supervisor
Noah S. Prywes

January 1974

Prepared for the
Office of Naval Research
Information Systems
Arlington, Va. 22217

under

Contract N00014-67-A-0216-0014
Project No. NR 049-153

DISTRIBUTION STATEMENT

Reproduction in whole or in part is permitted for any purpose
of the United States Government

Moore School Report # 74-05
Automatic Program Generation Research has been conducted under contract N00014-67-A-0216-0014, since 1971. The objective of the research has been to provide software generation directly from user specifications.

Initially, the research concentrated on a specific application, of generating file conversion programs. A first report on this subject was authored by Diane Pirog Smith, in December 1971, titled, "An Approach to Data Description and Conversion." Subsequently a software system for automating this function was implemented by Jesus A. Ramirez and described by him in a report titled, "Automatic Generation of Data Conversion-Programs Using A Data Description Language (DDL)". Currently, the objective of the research has been broadened to develop a user language and a software system for automatic generation of business oriented programs.

This technical report contains a collection of three papers intended to summarize the results of past research and the direction of current research. The first paper, by Ramirez, Rin and Prywes is a summary of Dr. Ramirez's report and dissertation cited above. The second paper, titled, "An Overview Of A System For Automatic Generation of File Conversion Programs," by N. Adam Rin and Maxine Brown is intended to provide a more user oriented view based on their experience in utilization of the system developed by Ramirez.

(Continued on next page)
There have been many research activities and a large number of papers in this area. The third paper, "Automatic Generation of Software Systems - A Survey," by N. Prywes serves to relate the research underway at the University of Pennsylvania, to the many recent and current activities in this field. It also aims to clearly define short and long term objectives and methodologies.
Automatic Program Generation Research has been conducted under contract N00014-67-A-0216-0014, since 1971. The objective of the research has been to provide software generation directly from user specifications.

Initially, the research concentrated on a specific application, of generating file conversion programs. A first report on this subject was authored by Diane Pirog Smith, in December 1971, titled, "An Approach to Data Description and Conversion." Subsequently a software system for automating this function was implemented by Jesus A. Ramirez and described by him in a report, titled, "Automatic Generation of Data Conversion Programs Using A Data Description Language (DDL)." Currently, the objective of the research has been broadened to develop a user language and a software system for automatic generation of business oriented programs.

This technical report contains a collection of three papers intended to summarize the results of past research and the direction of current research. The first paper, by Ramirez, Rin and Prywes is a summary of Dr. Ramirez's report and dissertation cited above. The second paper, titled, "An Overview Of A System For Automatic Generation Of File Conversion Programs," by N. Adam Rin and Maxine Brown is intended to provide a more user oriented view based on their experience in utilization of the system developed by Ramirez.

There have been many research activities and a large number of papers in this area. The third paper, "Automatic Generation of Software Systems - A Survey," by N. Prywes serves to relate the research underway at the University of Pennsylvania, to the many recent and current activities in this field. It also aims to clearly define short and long term objectives and methodologies.
# TABLE OF CONTENTS

I. AUTOMATIC GENERATION OF DATA CONVERSION PROGRAMS USING A DATA DESCRIPTION LANGUAGE, J.A. RAMIREZ, N.A. RIN AND N.S. PRINSES

1. Introduction 2

2. Summary of Language Processing Facilities Of The DDL/DML Processor 4

3. Information Flow In The DDL/DML Processor 7
   3.1 Introduction 7
   3.2 Overview Of The DDL/DML Processor 7
   3.3 The Syntax Analysis Program Generator (SAPG) 10
   3.4 The DDL Compiler 13
   3.5 Data Conversion Processor 18

4. Conclusion 21

II. AN OVERVIEW OF A SYSTEM FOR AUTOMATIC GENERATION OF FILE CONVERSION PROGRAMS, N. Adam Rin and Maxine Brown

1. An Overview Of The DDL Processor System 1
   A. Introduction 1
   B. The DDL Language as a Data Description Language 3
   C. Capabilities and Applications of DDL and Its Processor 5

2. Usage and Capabilities Of The DDL Processor With An Illustrative Example 7
   A. Usage and function of the DDL Processor 7
   B. Statement of Problem 9
   C. Features and Capabilities of the DDL Language and Processor 36
   D. Evaluation 39

3. Current Research 41
### TABLE OF CONTENTS

II. AN OVERVIEW OF A SYSTEM FOR AUTOMATIC GENERATION OF FILE CONVERSION PROGRAMS, N. Adam Rin and Maxine Brown

<table>
<thead>
<tr>
<th>Section</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>4. Other Possible Future Work</td>
<td>41</td>
</tr>
</tbody>
</table>

III, AUTOMATIC GENERATION OF SOFTWARE SYSTEMS - A SURVEY, N.S. Prywes

<table>
<thead>
<tr>
<th>Section</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Introduction</td>
<td>1</td>
</tr>
<tr>
<td>Part I Current Problems in Software Development</td>
<td>2</td>
</tr>
<tr>
<td>2. Overview of Historic and Economic Considerations</td>
<td>2</td>
</tr>
<tr>
<td>3. Analysis of the Software Development Process</td>
<td>7</td>
</tr>
<tr>
<td>Part II Future Advances in Automation of Software Development</td>
<td>14</td>
</tr>
<tr>
<td>4. Automatic Design and Implementation of Programs</td>
<td>14</td>
</tr>
<tr>
<td>5. Automatic Generation of Non-Procedural Specifications</td>
<td>25</td>
</tr>
<tr>
<td>6. Conclusions</td>
<td>31</td>
</tr>
</tbody>
</table>
UNIVERSITY OF PENNSYLVANIA
The Moore School of Electrical Engineering
Department of Computer and Information Science

TECHNICAL REPORT
RESEARCH ON
AUTOMATIC PROGRAM GENERATION

Project Supervisor
Noah S. Prywes

January 1974

Prepared for the
Office of Naval Research
Information Systems
Arlington, Va. 22217

under
Contract N00014-67-A-0216-0014
Project No. NR 049-153

DISTRIBUTION STATEMENT
Reproduction in whole or in part is permitted for any purpose
of the United States Government

Moore School Report #74-05
"AUTOMATIC GENERATION OF DATA CONVERSION PROGRAMS USING A DATA DESCRIPTION LANGUAGE"

by:

J.A. Ramirez, N.A. Rin and N.S. Prywes

The Moore School of Electrical Engineering, University of Pennsylvania, Philadelphia, Pennsylvania 19174

ABSTRACT:

Costs and development times involved in computer software have been exceedingly large. Programming and testing constitute the major component of total software cost and account for most of the slippages that have been widely experienced. It has also been found exceedingly difficult for management to control the progress of programming and testing of software. Therefore the research program described here aims initially at reducing and controlling these components of the costs, particularly in the Business Application Programming Area.

Historically it has been necessary to employ the computer itself to reduce programming costs through use of high level programming languages, such as COBOL or PL/I, or use of Data Base Management Systems. The increasing costs indicate the need to employ even a higher level of automation than employed so far, and automate the programming in high level languages; namely automatically produce ad hoc programs in languages such as COBOL or PL/I.

The paper describes a methodology to automatically generate programs and the application of the methodology in a Processor for generating file conversion programs. The Processor accepts as input descriptions of source and target files in a Data Description Language (DDL) and data manipulation specifications in a Data Manipulation Language (DML). It produces as an output a conversion program in PL/I capable of converting the source file and producing the target file.
1. INTRODUCTION:

The ultimate goal of the research reported in this paper is the automatic generation of programs to perform data processing. The design objectives have been as follows:

1. Input to the automatic programming system should be largely (but not completely) derived directly from the functional specifications for the program to be developed. (A number of languages for functional specifications are in use [1]).

2. Control, input/output and data definition code is to be generated automatically.

3. Data manipulation not specified in the input to the system, can be added in a modular manner without requiring concern with input/output, or control or data definition code.

4. A methodology for debugging, test and proofing of program components that is extensible to the entire end product program.

The paper describes a step toward this goal, a Processor which has been developed to generate programs for the limited application of data conversion. The Processor accepts as an input functional descriptions of source and target files in a Data Description Language (DDL). To specify validation criteria, data security, summaries and reports, the Processor accepts inputs in a Data Manipulation Language (DML). It produces as an output a conversion program in PL/1 capable of converting the source file and producing the target file. The advantage of using the Processor over programming directly in PL/1 are, briefly, the much simpler data definition in DDL, especially for variable length fields and records, and the automatic generation of JCL statements and ad-hoc PL/1 code for data declaration, input/output control logic and for calls on the DML procedures when appropriate.
The Processor generates the top levels of the program, performing the program design and producing documentation and cross referencing.

The emphasis in this paper is on the methodology used in constructing the DDL/DML Processor. The methodology is directed to implementing processors for automatic generation of programs for specific areas of data processing applications. It is also directed toward maximum ease in modification of the functional specification input language, as such languages are considered experimental until considerable experience has been gained in their use. This direction of the work, reported in the paper, is reflected in the discussion of languages used and information flow in the DDL/DML Processor in Sections 2 and 3 below, respectively.

The DDL/DML Processor automatically produces conversion programs in PL/1; i.e., PL/1 is used as an intermediate object/source language in the compilation process of DDL statements into machine code. The main reason for the use of PL/1 code is easier debugging and documenting of the compiler and the respective applications.

The Processor itself has also been programmed in PL/1, to facilitate ease of programming and to allow for better documentation. The processor is programmed for IBM 360/370 computers. It has been used in several applications to determine its computer usage, reliability and dependability.

A report on the Processor design and a User Guide to its applications and use are available as part of the Ph.D. Dissertation of the first Author [2].
The application of DDL/DML and the Processor consist of the following: (1) A Language for communication between humans about data structures: For example, for an analyst to describe a data base to an applications programmer. (2) Restructuring and conversion of files: As a utility, the Processor enables the user to convert files. (3) Interfacing files with different programs and programming languages: The Processor, can convert files into a structure which can be processed by another program. (4) Validating and editing of a file: By defining a file in DDL, one can validate it according to user-provided criteria written in DML. (5) Report generation: By defining the source file to be a data base and defining the target file to be a report, the DDL/DML Processor facilitates report generation. (6) Generally, single file to single file processing with intermediate manipulation of data.

2. SUMMARY OF LANGUAGE PROCESSING FACILITIES OF THE DDL/DML PROCESSOR

The processor has external as well as internal languages. The external user-oriented language used for input is DDL/DML. The internal languages are used to specify the syntax of DDL and some of the semantics. Based on these latter specifications various portions of the processor are automatically generated. The main aspects of both types of languages are summarized below.
DDL has extensive facilities for describing data files, covering both logical and physical structures of the data. There are facilities to describe such concepts as repeating fields, mandatory or optional fields, fixed or variable length fields. There are built-in functions to produce summaries, giving information on length of fields or the number of repeating fields. Code for editing and reformatting is generated automatically. The syntax of DDL is similar to the data descriptive facilities of COBOL, PL/1 or the DDL designed by the CODASYL Data Base Task Group [3]. The DDL used in the Processor is based on a subset of the DDL design by Smith [4]. In the course of the implementation and use it was found advantageous to make modifications to make DDL easier to implement and use. A more detailed description of the facilities of DDL accompanied by many illustrative examples of its use can be found in the User Guide Appendix of [2].

DML is used for specifying validation criteria for the data, for specifying complex conversions not built-in the Processor, for producing summaries, and for producing reports. DML is, in fact, a subset of PL/1, and therefore, has general manipulative capability. The Processor may be also regarded as an adjunct to a PL/1 Compiler, adding, in fact, the facilities of DDL to PL/1.

The syntax of DDL is easy to change. The specification of the syntax of DDL itself and its internal encoding is used as an input to a portion of the Processor, from which the Syntactic Analysis Program (SAP) for DDL is generated. The language used to specify the syntax of DDL is an Extended BNF (EBNF).
To convey some of the semantics, the specifying language was extended further by allowing the specific calling for subroutines. The Extended BNF With Subroutine Calls is referred to in the following as EBNF/WSC. It was used for specifying processing and encoding of the DDL Statements into internal tables, and thus also to reduce the work in programming the code generation.

The specification of DDL is used as an input to a program called Syntactic Analysis Program Generator (SAPG). This program then generates SAP, which is used to syntactically analyze and encode the input DDL/DML statements. This aspect of the processor was considered most important, as it was realized that the DDL being used, as well as other functional specification languages, are experimental and extensive changes may be necessary before even a near optimal language is obtained. This facility was indeed used to change the language. For instance, a change from the DDL designed by Smith [4] to a modified DDL was accomplished in less than a week. Another advantage was the ease in incorporating input error analysis, as experience was gained with use of the DDL Processor. It was also found that automatic generation of the SAP, via SAPG, greatly speeded and reduced the work required to implement the processor.

Transition Matrices were used in Lexical Analysis of the DDL as well as the EBNF/WSC statements following the ideas in [5], and [6].

It is, however, noteworthy that techniques for automatic generation of the Processor's code generation routines were not used. This was because the DDL processor is specially designed to generate
conversion programs and the research to date [7] on automatic generation of code for general purpose language compilers is not applicable. Therefore, the code generation of the DDL/DML processor was hand coded in PL/1. Thereby, better efficiency was also achieved in the generation of the conversion programs. All the hand codes, as well as automatically generated parts of the system, including the SAPG, were written in PL/1 for reasons of facilitating programming, maintainability and limited machine independence.

3. INFORMATION FLOW IN THE DDL/DML PROCESSOR

3.1 INTRODUCTION

The information flow in the DDL/DML Processor is illustrated in Figures 1 through 6. Figures 1 and 2 give a progressively somewhat more detailed overview of the entire processor. The remaining four Figures, give a further detailed information on the three major components, the Syntax Analysis Program Generator, the DDL Compiler and the generated Conversion Program, respectively.

3.2 OVERVIEW OF THE DDL/DML PROCESSOR

The DDL/DML Processor is actually a set of three processors. These are shown in Figure 1a as follows: The first is the Syntactic Analysis Program Generator (SAPG). It accepts as an input the EBNF/WSC syntax specifications of the DDL and produces the Syntax Analysis Program (SAP) for the DDL. This product is then joined with inputs consisting of routines providing additional semantics, code generation logic, etc. to make the second processor, the DDL Compiler. The second processor accepts the DDL/DML statements and its product is the third processor,
SYNTACTIC ANALYSIS
PROGRAM GENERATOR (SAPG)

SYNTAX DEFINITIONS OF DDL IN EBNF/WSC
LEXICAL & SYNTAX ANAL. ROUTINES FOR EBNF/WSC

SYNTAX DEFINITION
SEMANTICS AND CODE GENERATION RULES

CODE GENERATION ROUTINE FOR DDL

DATA DEFINITIONS (DDL & DML STMTS)

SYNTAX DEFINITION
SEMANTICS AND CODE GENERATION RULES

USERS SOURCE STATEMENTS

INPUT DATA

OUTPUT DATA

1b COMPILER-COMPILER SYSTEM

TARGET DATA

1a THE DDL/DML PROCESSOR

FIGURE 1
COMPARISON OF DDL/DML PROCESSOR AND COMPILER-COMPILER SYSTEM DESIGNS
the Data Conversion Program, which is used to convert the source data into the target data.

An analogy with existing computer programming language systems is illustrated by comparing Figure 1a with Figure 1b. The SAPG (in 1a) is analogous to a compiler-compiler's syntax analysis generation module (in 1b) which is used to produce syntax analysis programs for the specific language. The DDL Compiler (in 1a) is analogous to a programming language processor, (in 1b) such as COBOL, FORTRAN or PL/1 compilers. The Data Conversion Processor (in 1a) is analogous to an executable user program (in 1b). The relationship between the use of the three processors in the DDL system is readily seen from the analogy.

The SAPG is a program used to create the syntax analysis program which in turn, becomes part of the DDL Compiler. The "generator" is a very valuable tool in the development of the DDL Compiler. Furthermore, it could be a "stand-alone" valuable tool in writing any language syntax analysis program.

The DDL Compiler and the Data Conversion Processor are the only components of the DDL Processor that most users will need. To produce a Data Conversion Processor, a user writes a data definition, a series of statements in DDL, for each of the source and target files. These statements are basically descriptions of the formats of the source and target files and of transformations from the former to the latter. If data manipulation is needed, a series of routines are written in DML. These statements are used as inputs to the DDL Compiler. The compiler in
turn produces a data conversion program. Just as it is not necessary to compile a conventional program each time it is used, it is not necessary, also, to create a new data conversion program, each time it is used. The same object module could be re-used (either as PL/1 source or in object code form).

A somewhat more detailed view of the three major components of the DDL Processor is given in Figure 2. In Figure 2-6, the three processors are identified by being surrounded by broken lines. A rectangle with the missing top represents an input, and with a missing bottom an output. Outputs which are programs, are shown in trapezoids. When the output is a program, double lines are used to show where it is later used. Figure 2 shows an overview of the information flow with some additional detail. In particular, syntax and lexical analysis programs make up the DDL Compiler.

The discussion in the following three sub-sections (and in Figures 3 through 6) provides a more detailed view of the components, in the order of top to bottom of Figure 2.

3.3 **THE SYNTAX ANALYSIS PROGRAM GENERATOR (SAPG)**

The top left of Figure 2 is expanded in Figure 3. The SAPG is a compiler in its own. It consists of Lexical Analysis (a) Syntax Analysis (b) and Code Generation (c) modules, all hand coded in PL/1. They are compiled into IBM/370 machine code using the PL/1 compiler, and form the SAPG.

The input to SAPG is the DDL syntax specification, in EBNF/WSC (d). The output (f) is a Syntax Analysis Program (SAP) - in PL/1, which
FIGURE 2
MAJOR COMPONENTS OF THE
DDL-PROCESSOR
FIGURE 3
SYNTAX ANALYSIS PROGRAM GENERATOR
(SAPG)
can perform the syntax analysis on statements written in DDL.

The SAPG has all the components of a compiler. The lexical analysis consists of processing the EBNF/WSC statements to form tokens. The syntax analysis further processes the EBNF/WSC strings of tokens to perform various checks, generate error comments and organize the information in accordance with the internal tables.

Finally this information is analyzed and the SAP PL/1 code is generated for processing subsequently (in Figure 4) the DDL Statements.

3.4 THE DDL COMPILER

Figure 4 shows the components that are used to make up the DDL compiler. They are as follows:

(1) The Lexical Analysis Subroutine for DDL - hand coded in PL/1.

(2) The Syntax Analysis Program (SAP) - produced by the SAPG in PL/1.

(3) Supporting subroutines - written in PL/1, perform services required by the SAP. Services include recognition of syntactic elements, diagnostics of syntax errors, creation of Internal Tables (symbol and data tables) and, if specified, a cross-reference table of the identifiers used in the DDL program. These subroutines can be called in the EBNF/WSC statements.

(4) Code Generation Program - written in PL/1, forming the basis of the code generation phase of the DDL compiler. In it is contained the logic required to interpret the information stored in the internal tables and to generate PL/1 statements which will perform the data conversion indicated by the analysis of DDL statements.
FIGURE 4
DDL COMPILER INPUT AND OUTPUT CONTENTS
(5) & (6) The PL/1 Compiler (5) produces the DDL Compiler (6) in IBM/370 machine code.

The remainder of the system is also summarized in Figure 4, as follows:

(7) The DDL statements given by the user describe the structure of his source and target files and mappings from the former to the latter. The DML statements describe the file manipulations to be performed on the source file, such as editing, complex specialized conversion formulas, security testing, report generation and statistical gathering of data.

(8) & (9) The Data Conversion Program, in PL/1 is the input to the PL/1 compiler (9) this produces the Data Conversion Program (10) in machine code for the IBM/370.

(10), (11) and (12) Finally the Data Conversion Program (10) accepts as input the source file (11) and outputs the target file (12).

The compiling process is further expanded in Figure 5. It is performed in two phases, as described below.

PHASE 1 OF THE DDL COMPILER

In phase 1, DDL statements are read by the Lexical Analysis Program (LEX) that forms tokens which are in turn the input to the SAP. The SAP examines the string of tokens to determine whether or not the string obeys certain structural conventions explicit in the syntactic definition of the language. Should an error be discovered, the error-diagnostic routines will be called to output a message informing the user of the location and nature of the misconstruction.

Concurrent with this error detection is the internal table generation. At this time, routines are called whose functions are the capturing of
FIGURE 5
DDL COMPILER PHASES
information contained in the DDL source statement and the building of
tables to preserve this data in coded form for use during code generation,
as well as in the detection of global syntax errors. The internal tables
that are formed are the Symbol and Data Tables. If no errors were detected
and if a "XREF" option was specified to the DDL Compiler, then the
cross-reference table is generated and output for the user's reference.

PHASE 2 OF THE DDL COMPILER

Phase 2 is code generation. The first part of Phase 2 is the execution
of a series of programs which steps through the Data Table and generates
PL/1 declare statements. When compiled, these will produce a Description
Table that contains the following for each field of the source record:
(1) data descriptor information, and (2) space for placing pointers to the
field at execution time.

After the compiler has produced the Descriptor Table, a call is made
to the Data Parsing Code generation routines. These routines generate the
code which will set the field pointers in the Descriptor Table entry to point
to the fields in the input buffer of the source file at run time.

The second part of Phase 2 is a program which uses the Data Table
entries for the target file and generates the data movement code, which moves
the source data into the target file. This completes Phase 2 of the
DDL compiler. At this point the DML statements are read by the DDL compiler
and they are merged with the code produced during the code generation phase.

The creation of the data conversion program in IBM/370 machine language,
is performed by the PL/1 compiler. The input to the PL/1 compiler is the
PL/1 text produced in Phase 2.
3.5 **DATA CONVERSION PROCESSOR**

The Data Conversion Processor is composed of the set of programs and data which was produced by the DDL compiler and the DML routines supplied by the user. This is illustrated in Figure 6, it is composed of:

(a) a Data Conversion Program

(b) a Data Structure for the source file, and

(c) a set of DML subroutines

The Data Structure for the Source File, which was produced by the DDL Compiler from the DDL statements, is used by the generated Data Conversion Program to aid in parsing the source data. The other component of the Data Conversion Processor, the user-supplied DML subroutines, perform functions such as character set conversion, extension or truncation of fields, data type conversion, criteria testing, security testing, report generators and gathering of statistical data.

The information flow in the Data Conversion Processor, shown in Figure 6, in fact is a representation of a generalized strategy for data conversion. In terms of a program this strategy is represented by two parts. The first consists of the data definition and control code which is required in a program to allocate buffers, input/output of blocks and records, identify the location of data elements in accordance with a specified data structure and finally perform standard conversion functions. The "flow chart" for the generated code is illustrated in Fig. 7. In a "top-down" [8] concept of a program, the first part constitutes the "top." The second part, which consists of data manipulation routines in DML constitutes the "bottom" level of a program. This general strategy has
FIGURE 6
DATA CONVERSION PROCESSOR
DDL GENERATED PROGRAM PROTOTYPE

Declare Internal "Housekeeping" Variables

Declare Input buffers for source file

Declare structure of pointers and lengths for every field and group in the source file

Declare Output buffers for target file

READ a user-defined record

call WRAPUP procedure if any

Set all pointers of source record initially to null

For each field in source record:

1) Call criterion procedure, if any, to test existence
2) Determine starting position of field and set pointer to it
3) Determine length of field as
   (a) a given constant
   (b) by scanning for a delimiter
   (c) by calling a routine that computes length
   (d) by referring to the value of another field
   or (e) by using the length or count of another field

Y Any more source fields?

N

For each target field:

1) Find field in source record or constant to be moved to target
2) Call the appropriate conversion routine, if specified
3) Concatenate source field to the output buffer being built

Y Anymore target fields?

WRITE Output Record

FIG. 3 ILLUSTRATION OF THE "FLOW CHART" OF DDL/DML PROCESSOR GENERATED CONVERSION PROGRAM
been adopted from the beginning in the supporting and code generation routines. It is this type of strategy that is envisaged to change from one class of potential applications to another.

4. CONCLUSION

The Version 1.0 DDL/DML Processor has been found to be an effective and efficient tool, by the users, in several applications, although it has limited functions and capabilities. Additions of functions and use to gain additional experience are progressing. The authors invite those interested to communicate with them regarding receipt of the system for wider experimentation. Presently, the subjective impressions are that effectiveness in program development, efficiency in processing and reliability are gratifying. However, there have not been a systematic approach to measuring these aspects so far.

The authors however feel that in this study of development, an equally important outcome is the availability of the system as a research tool and backbone for expansion to accept other functional specification languages and to perform for wider classes of data processing applications.

ACKNOWLEDGEMENT

The authors acknowledge with thanks the participation in design and programming by Messrs. H. Solow, P. Gross and A. French, and the preparation of the documentation and this manuscript by Miss B. Weber.
REFERENCES:


AN OVERVIEW OF A SYSTEM
FOR AUTOMATIC GENERATION OF FILE CONVERSION PROGRAMS

N. Adam Rin and Maxine Brown

ABSTRACT

This paper describes a processor which automatically produces file conversion programs based on non-procedural user specifications. The Processor accepts, as input, descriptions of a source file and a desired target file with some auxiliary descriptions of associations between the two. This is specified by a user in a Data Description Language (DDL).

To specify validation criteria, complex, conversions not built-in to the system, security criteria, or summary processes, the system also accepts specifications in a Data Manipulation Language (DML). It produces, as output, a conversion program in PL/1 capable of converting the described source file into the desired target file. The paper describes the structure, system design, capabilities, and applications of the DDL/DML language and processor, including an illustrative example.
AN OVERVIEW OF A SYSTEM FOR
AUTOMATIC GENERATION OF DATA CONVERSION PROGRAMS

N. Adam Rin and Maxine Brown

I. AN OVERVIEW OF THE DDL PROCESSOR SYSTEM

A. Introduction

This paper is concerned with research done in the field of automatic
generation of data conversion programs; it provides an overview of an existing
software system which produces file conversion programs from a user's
specification in a data definition language and it discusses possible future
work in this area. The system was designed and implemented with support from
the Office of Naval Research by a group of graduate students under the
supervision of Dr. N.S. Prywes at the Moore School of Electrical Engineering
at the University of Pennsylvania.

The system developed is a step towards the automatic generation of data
processing programs. The class of data processing programs which the existing
processor can automatically generate is file conversion programs; i.e., the
system is able to produce a procedural program based on non-procedural user
oriented specifications to translate an existing file of known structure into
a new desired format. Non-procedural specifications as used here are descriptive
statements defining the source and target files and what transformations are
to be performed between the two, but do not relate how these transformations will
be constructed. The procedural program produced by the processor is

1 For detailed documentation of the system, its implementation and use, see
J.A. Ramirez, "Automatic Generation of Data Conversion Programs Using a
Data Description Language (DDL)," Ph.D. Dissertation, Moore School of
Electrical Engineering, University of Pennsylvania, Philadelphia, Pennsylvania,
19174; 1973.
FIGURE 1  THE DDL PROCESSOR BLOCK DIAGRAM
a PL/1 conversion program complete with input/output related instructions, logical sequencing and control, and invocation of special manipulative routines peculiar to the problem.

Figure 1 shows a block diagram of the processor which accepts, as input, descriptions of a source file and a desired target file with some auxiliary associations between the two specified in a Data Description Language (DDL). To specify validation criteria, complex conversions not built into the system, security criteria, and summary processes, the system also accepts as input prescriptive statements in a Data Manipulation Language (DML), a subset of PL/1 with general manipulative capabilities. The processor produces, as output, a procedural PL/1 program capable of converting the source file into the desired target file. The generated program is then compiled into machine language, yielding a tailored-to-need conversion program. The processor can therefore be regarded as an adjunct and enhancement to the PL/1 compiler, adding the facilities of DDL to it.

B. The DDL Language as a Data Description Language

The DDL that has been developed contains all the data definition facilities of COBOL and PL/1 plus other unique capabilities for describing data structures and organization not found in these languages. For example, the DDL language and its processor have the ability to compute the length of a data item at execution time and to accept a variable number of occurrences of variable length data items.

The actual syntax of the DDL developed is not extremely crucial to pinpoint; the way the processor was implemented makes it easy to change the DDL syntax. Namely, the specification of the syntax of DDL is itself an
Figure 2: Block Diagram of SAPG and the DDL Processor System.
input to a meta-processor, developed by this project, which generates a syntax analysis program for DDL (or any other language specified to it). As seen in the more refined block diagram of the DDL processor in Figure 2, this meta-processor, called a Syntax Analysis Program Generator (SAPG), accepts descriptions of a formal language such as DDL in a meta-language which is essentially an extension of BNF. It generates a Syntax Analysis Program (SAP) for that language (which, in our case, is DDL) and invokes manually-written processing routines. The SAP for DDL together with manually written code generation modules form the bulk of the DDL processor which, in turn, accepts the DDL/DML statements and generates a PL/1 program.

The implementation of the SAPG and its use to implement the DDL System allows changes to the syntax of DDL to be made relatively easily and in a short period of time. Thus, this processor could accommodate other Data Description Languages with relatively little effort. This is a crucial fact, for such languages are considered experimental until experience is gained on their use and effectiveness.

C. Capabilities and Applications of DDL and Its Processor

Actually, any application involving one input sequential file and one output sequential file is a candidate for use in the present DDL System. Some examples of the uses of the DDL language and its processor are the following:

(1) **A Language for Communication Between Humans About Data Structure**

One important application of DDL is as a means of communication between humans about the structure of data. For example, using a DDL, a designer of a data base can describe precisely to an application programmer the exact structure of the data the programmer is to use. Furthermore, even non-technical personnel could learn a DDL-like language to describe data.
(2) Restructuring and Conversion of Files

As a utility, the DDL processor in conjunction with DML enables the user to re-define the structure of his file, reformat it and convert it accordingly. Furthermore, conversion of a file can be done selectively; i.e., those portions of a file that meet a user's criteria can be selectively converted or copied to a new file.

(3) Interfacing Files with Different Programs and Programming Languages

Frequently files created by one program cannot be processed by another program or by another program in a different programming language or computer facility. With the DDL processor, files can be converted into a structure or format which can be processed by another program or machine. In this manner files can be interfaced across programming languages and computing facilities.

(4) Validation of a File

By defining a file in DDL, one can validate it according to user-provided criteria written in DML.
II. USAGE AND CAPABILITIES OF THE DDL PROCESSOR WITH AN ILLUSTRATIVE EXAMPLE

A. Usage and function of the DDL processor

Figure 2A shows a block diagram of the usage and function of the DDL processor. A file conversion problem using the DDL system passes through two states; the user is responsible for formulating and describing the data definitions, mappings, and manipulations in the DDL/DML language, and the DDL processor compiles and executes these descriptions to produce the desired file. Enumerating this process, the user is responsible for producing the following:

(1) a DDL description of the source file,

(2) a DDL description of the target file with the associated mappings from the source file, and

(3) the DML routines for specifying criteria, special computations, and complex conversions not built-in to the system.

The DDL processor, in turn, will generate the following programs and listings:

(1) the desired generated PL/1 program that performs the conversions (this program declares appropriate variables, allocates and opens buffers for the source and target files, issues input commands for reading the source file, issues output commands for writing the target file, and invokes the user provided DML routines at the appropriate locations);

(2) a listing of the user's DDL/DML statements;

(3) diagnostics for user errors;

(4) a cross-reference table which has an alphabetical listing of all the names which appear in the user's DDL description along with its attribute (field, group, record, etc.) and the line numbers of all the statements in which it appears;

(5) a listing of the required Job Control Language (JCL) cards which describes the input and output files and are needed to run the generated program under the OS 360/370 operating system.
Figure 2A: Usage and function of the DDL language and processor
The generated program, integrated with the user-provided DML routines, is input into the PL/1 compiler, compiled into machine language, and executed to produce the desired target file.

The above listings will be illustrated later in Figures 5a - 5e.

B. Statement of Problem

In order to present a brief discourse on how to use the DDL system, a sample problem was devised, implemented, and reproduced in this section for illustrative purposes.

It is desired to convert a given tape consisting of text of news articles into one whose records are of a more legible format. The format of the source records have the hierarchical structure as shown in Figure 3a.

The NUM field contains a 5 digit number which is the number of bytes contained in the field IN_TEXT.

DOCUMENT is a 6-digit field which contains a serial number assigned to the news article found in IN_TEXT. If the article is more than 1 record in length, the same document number appears on all related records.

DATE is a 8-character field containing the date the news event took place.

TIME is a 5-character field containing the time of the event.

TITLE is a variable-length field whose length is to be computed by a user-provided routine called TEXTLEN.

IN_TEXT is a field which contains the actual news article. It has a variable length of up to 4088 bytes and contains either the entire article or a portion thereof. In the latter case, the article is continued in the next record, but in no case is there ever more than one article per record.

The desired program is to perform the following conversions:

(1) the beginning of each article is prefixed by a collection of fields,
FIGURE 3a  SOURCE RECORD STRUCTURE

FIGURE 3b  TARGET RECORD STRUCTURE
denoted as INFORMATION, from which we wish to extract DATE, TIME, and TITLE.

(2) The body of the news article contains special sequences of characters, which shall be referred to as break characters, interspersed throughout the news article. These are to be scanned for and eliminated.

(3) The desired target tape is to contain records with fields DATE, TIME, and TITLE, if apropos, and an OUT_TEXT field, as depicted in Figure 3b, which consists of the preserved text without the special break characters and introduction. The target textual records are to output with a maximum of 75 characters per line for the purpose of legibility when the tape is printed at a later time. For efficiency considerations, however, the 75-byte logical records are blocked to 7500 characters on the output tape.

Figure 4 shows the dump of the first few records of the original tape, while Figures 5a through 5e show the listing produced by the DDL compilation for this sample problem. A dump of the first few records of the desired target tape is given in Figure 6.
SPY PATROL

FIGURE 4: SOURCE FILE LIST
STEPPED UP ITS INTERVENTION ON THE EVE OF THE CEASE-FIRE IN THE ==
SUMMER OF 1970. "== QUESTION: WHAT ABOUT OUR RECENT ATTEMPTS TO BREAK
THE RING OF== ARAB HOSTILITY BY MAKING OVERTURES TO ARAB STATES LESS==
FANATICAL THAT OUR NEIGHBORS??? ANSWER: NOT ONLY TO THE LESS

FANATICAL STATES, BUT EVEN TO== OUR NEIGHBORS, WE NEVER MISS OPPORTUNIT
IES TO EXPRESS OUR HOPE FOR== PEACE AND OUR OUTLOOK, WITHOUT GOING
INTO DETAILS, I TELL== SAY THAT NOT ONLY IN THE CASE OF REMOTE ARAB COUN
TRIES BUT EVEN== CLOSE ARAB COUNTRIES, WE HAVE MADE USE OF THE GOOD SE
RVICES OF== TO HELP THE PEOPLE WHO GO FROM CAIRO TO JERUSALEM, OR FROM
JERUSALEM== TO AMMAN, TO PRESENT A CORRECT PICTURE OF OUR POLICY TO THE
== OTHER SIDE. THIS ALSO APPLIES TO COUNTRIES WHICH ARE LESS INVOLVED==
== IN THIS DISPUTE, AND COUNTRIES FURTHER AWAY FROM THE AREA. OUR==
EFFORTS ARE CONSTANT== QUESTION: WHAT ABOUT THE DEMAND FOR OUR
MIRAGES, WHICH ARE STILL== IN FRANCE??? ANSWER: THE DEMAND IS STIL
S STILL VALUE AND THE MIRAGES ARE STILL IN== FRANCE.== QUESTION
N: MR. CHAH, DO YOU THINK THAT THE POLITICAL OPTIONS== AT OUR DISPO
SAL LIMIT US FROM THE OUTSET? THE EGYPTIANS SMILE== SOMETIMES ON MOSCO
W AND SOMETIMES IN WASHINGTON, WE GET RESPONSES== FROM BOTH SIDES, WHIL
E WE HAVE TO WORK HARD EVEN WITH OUR FRIENDS== THE UNITED STATES.==
ANSWER: NATURALLY, THIS DIFFERENCE EXISTS. BUT IF I WERE THE== EGYPTIAN FOREIGN MINISTER I WOULD NOT BE GREATLY COMFORTED BY IT,==
AFTER ALL THE PRIORITY IS TO EXPLOIT ALL THE POSSIBILITIES, WHAT HAS== THE
RESULT BEEN? THE RESULT IS THAT WE REMAIN EXACTLY WHERE== WE WERE ON
THE DAY AFTER THE WAR AND NO ONE HAS SUCCEEDED IN MOVING== A SINGLE ISR
AEL SOLIDER FROM A SINGLE POSITION== QUESTION: HAVE WE EVER TRIED TO
OPEN A DIALOG WITH THE USSR== OR OTHER EAST EUROPEAN COUNTRIES==
ANSWER: YES, WE HAVE TRIED TO MAKE THE USSR REALIZE THAT== ISRAEL
DOES NOT REGARD THIS CONTRIBUTION AS NECESSARY, NOR DO WE== BELIEVE THAT THE
EXISTENCE OF DIFFERENCES Necessitates BREAKING OFF== RELATIONS. WE
HAVE BEEN USING MANY CHANNELS RECENTLY TO CONVEY TO THE== USSR THE KNOW
LEDGE THAT IF IT WANTS A DIALOG WITH US, == SEPARATELY FROM AND PRIOR TO
O RESUMPTION OF RELATIONS, WE ARE OPEN TO== THIS.== (MORE)==

17 APR 1980 1 1 1 1

FIGURE 4: CONTINUED
CONVERT(SFILE INTO TFILE);

/****** DESCRIPTION OF SOURCE FILE */

SFILE IS FILE(INDEXED, STORAGE=MAG_TAPE);

... IN_RECORD IS RECORD(NUM /* THE LENGTH OF 'IN_TEXT' */
, DOCUMENT /* SERIAL NO. ASSIGNED TO ARTICLE */
, INFORMATION(01), DOC_TYPE='INTRO'
/* INTRODUCTION TO TEXT */
, IN_TEXT /* ACTUAL TEXT */
, SIZE=VARIABLE(4096));

NUM IS FIELD(NUM_PICTURE=999999);

DOCUMENT IS FIELD(CHAP(6));

... INFORMATION IS GROUP (JUNK01
, MSGID
, JUNK02
, DATE
, JUNK03
, TIME
, JUNK04
, PARS
, ZEM
, TITLE);

JUNK01 IS FIELD(CHAP(6));

MSGID IS FIELD(CHAP(11));

JUNK02 IS FIELD(CHAP(23));

DATE IS FIELD(CHAP(8));

JUNK03 IS FIELD(CHAP(3));

TIME IS FIELD(CHAP(5));

JUNK04 IS FIELD(CHAP(24));

PARS IS FIELD(CHAP(26));

ZEM IS FIELD(CHAP(27));

TITLE IS FIELD(CHAP('TXTLEN'));

IN_TEXT IS FIELD(CHAP('PPRC31'));

MAG_TAPE IS TAP(E VARIABLE(4096), VOL_NAME=X00273);

/****** DESCRIPTION OF TARGET FILE */

TFILE IS FILE(OUT_INDEX, STORAGE=MAG_TAPE);

... OUT_RECORD IS RECORD(OUT_INFO ( ));

OUT_RECORD
, SIZE=VARIABLE(40951);

/* EACH 'OUT_RECORD' IS AT MOST 4050 CHAR LENGTH. THE ACTUAL LENGTH IS DETERMINED AFTER AN ASSIGNMENT HAS BEEN MADE TO THE FIELDS 'OUT_INFO' AND 'OUT_TEXT'. */

OUT_INFO IS GROUP(OUT_DATE_LABEL
, OUT_DATE
, OUT_Time_LABEL
, OUT_TIME
, OUT_TITLE_LABEL
, OUT_TITLE);

OUT_DATE_LABEL IS FIELD(CHAP(10)<= 'DATE');

FIGURE 5A: DDL/DML LISTING
FIGURE 5A: CONTINUED

23 OUT_DATE IS FIELD(CHAR(65)<>'DATE');
24 OUT_TIME Label IS FIELD(CHAR(10)<>'TIME');
25 OUT TITLE Label IS FIELD(CHAR(7)<>'TITLE');
26 OUT TITLE IS FIELD(CHAR(*)<>'TITLE', CONV='PROC4');
27 OUT TEXT IS FIELD(CHAR(*)<>'IN-TEXT', CONV='PROC4');
28 MAGAZINE IS TAPE(FIXED(750), 75), FILENAME=553548,
29 DENSITY=160,
30 END;
**FIGURE 5A - CONTINUED**
PROCEDURE
/*
This is a FILE LENGTH ROUTINE which calculates the length of the
source field 'IN-TEXT'.
*/
LCL_UPN_PIC(i9999) BASED(WORD_PRT)
/* If 'INFORMATION' exists, the length of 'IN-TEXT' is equal to the
contents of 'INFO'; otherwise, the
length of 'IN-TEXT' is simply the contents of 'NUM'. */
IF CRIT_RES THEN COUNT_RESULT=NUMER-(LEN+FRIS+LEN+ZEN+LEN+TITLE+LEN)
ELSE COUNT_RESULT=NUMER;
RETURN;
END PROC;

HEADER:PROC
/*
This is a PRE-CRITERIA ROUTINE which determines whether or not the
target group 'OUT_INFO' exists for a particular record.
*/
/* If the source group 'INFORMATION' exists, then the target field
'OUT_INFO' exists */
CRIT_RESULT=CRIT_RES;
RETURN;
END HEADER;

Figure 5a - Continued
PROCEDURE "LC(1);"

/*
This is a complex procedure that searches for special blank characters
in the input text before it is mapped to the target.
*/

TEXT := TEXT ("\001") ;
/* ASSYM: THE CHARACTERS OF TEXT ARE THE LOCAL VARIABLE TEXT */

/* DELIMITED CHARACTERS, THEY ARE OF THE FORM:
*/

LOOP: J=I; TEXT(="*");
FIL: IF J<> THEN

LINE=TEXT;
CONV_FLD_CHAR=CONV_FLD_CHARNIL;
RETURN;
END;

TEXT := TEXT ("\001") ;

IF (SUBSTR(TEXT,1,J+1)="*"1)
(SUBSTR(TEXT,1,J+1)="*"1
(SUBSTR(TEXT,1,J+1)="*"1)
THEN
LINE=SUBSTR(TEXT,1,J-1);
CONV_FLD_CHAR=CONV_FLD_CHARNIL;
IF (LINE=SUBSTR(TEXT,J+1)) THEN TEXT=SUBSTR(TEXT,J+11)
ELSE RETURN;
END;

ELSE IF J=1 THEN TEXT;
IF J=1 THEN J:=J;
GO TO FIL;
END;
ELSE J:=J+J;
GO TO INSERT_LOOP;
END;

IF J=1 THEN TEXT=SUBSTR(TEXT,J+1)
END;
END;

END TO LC;
END TO LC;

Figure 5A - Continued
FIGURE 5b: DDL COMPILER DIAGNOSTICS

NO ERRORS OR WARNINGS DETECTED
FIGURE 5c: CROSS-REFERENCE TABLE
<table>
<thead>
<tr>
<th>Stmt No</th>
<th>Identifier</th>
<th>Attribute and References</th>
</tr>
</thead>
<tbody>
<tr>
<td>25</td>
<td>OUT_TIME</td>
<td>FIELD 20,</td>
</tr>
<tr>
<td>24</td>
<td>OUT_TIME_LABEL</td>
<td>FIELD 21,</td>
</tr>
<tr>
<td>27</td>
<td>OUT_TITLE</td>
<td>FIELD 21,</td>
</tr>
<tr>
<td>26</td>
<td>OUT_TITLE_LABEL</td>
<td>FIELD 21,</td>
</tr>
<tr>
<td>2</td>
<td>SFILE</td>
<td>FIELD 1,</td>
</tr>
<tr>
<td>19</td>
<td>TFILE</td>
<td>FIELD 1,</td>
</tr>
<tr>
<td>12</td>
<td>TIME</td>
<td>FIELD 6, 25,</td>
</tr>
<tr>
<td>16</td>
<td>TITLE</td>
<td>FIELD 6, 27,</td>
</tr>
<tr>
<td>15</td>
<td>ZEM</td>
<td>FIELD 6,</td>
</tr>
</tbody>
</table>

**Figure 5c: Continued**
REQUIRED DD-CARDS NEEDED TO RUN THE OBJECT (PL/1) PROGRAM:

//DDLSRC DD DSN=DUMMY, VOL=SER=X00279, DISP=OLD,
// UNIT=NINETRK, LABEL=(1, NL),
// DCB=(RECFM=V, LRECL=0, BLKSIZE=4096, DEN=2)
//NDLTAR DD DSN=SS3548, VOL=SER=SS3548, DISP=OLD,
// UNIT=NINETRK, LABEL=(1, SL),
// DCB=(RECFM=F8, LRECL=75, BLKSIZE=7500, DEN=3)

Figure 5d. Required JCL Needed to Run the Generated Program.
FIGURE 5E: PL/I OBJECT PROGRAM LISTING
FIGURE 5E: CONTINUED
FIGURE 5E: CONTINUED
FIGURE 5E: CONTINUED
FIGURE SE: CONTINUED
\* THIS IS A LRE_CRITERIA ROUTINE WHICH DETERMINES WHETHER THE SOURCE
1 GROUP "INFORMATION" APPEARS 0 OR 1 TIMES IN THE RECORD. \* 1

| 140  | 2 | END INTRODUCTION CHAR[11] BASED(NEXT_FLG_PTR); |
| 141  | 2 | IF L_INTRODUCTION=*(! FSGN0* THEN CRIT_RESULT=TRUE; |
| 142  | 2 | ELSE CRIT_RESULT=FALSE; |
| 143  | 2 | END IF; |
| 144  | 2 | END INTRODUCTION; |
| 145  | 2 | END INTRODUCTION CHAR[11] BASED(NEXT_FLG_PTR); |
| 146  | 2 | IF L_INTRODUCTION=*(! FSGN0* THEN CRIT_RESULT=TRUE; |
| 147  | 2 | ELSE CRIT_RESULT=FALSE; |
| 148  | 2 | END IF; |
| 149  | 2 | END INTRODUCTION; |
| 150  | 2 | END INTRODUCTION CHAR[11] BASED(NEXT_FLG_PTR); |
| 151  | 2 | IF L_INTRODUCTION=*(! FSGN0* THEN CRIT_RESULT=TRUE; |
| 152  | 2 | ELSE CRIT_RESULT=FALSE; |
| 153  | 2 | END IF; |
| 154  | 2 | END INTRODUCTION; |

**Figure 5E: Continued**
/* THIS IS A CONVERSION ROUTINE WHICH DELETES SPECIAL\n  BREAK CHARACTERS FROM THE SOURCE FIELD 'TITLE' BEFORE IT IS MAPPED INTO THE\n  TARGET FIELD 'OUTITLE'. */

/* ASSIGN THE CONTENTS OF 'TITLE' TO THE LOCAL VARIABLE 'TTI'. */
TTI = SUBSTR(TITLE, 1, TITLE_LEN);

/* DELETE LNK CHARACTERS. THEY ARE OF THE FORM: */

LCP(i,j) = LCP(i,j) \times (TTI \neq ')';

TTI' = IF J = 0 THEN
  "",
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHAR = Conv_FLU_CHAR || TTI;
    Conv_FLU_CHARS = TTI;
    IF (Conv_FLU_CHAR[1:7] = "TXTT_") THEN
      Conv_FLU_CHAR = SUBSTR(Conv_FLU_CHAR, 3);
    END IF;
    ELSE IF CHAR = Conv_FLU_CHAR THEN
      Conv_FLU_CHARS = TTI;
    END IF;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_CHAR THEN
    Conv_FLU_CHARS = TTI;
  END IF;
  ELSE IF CHAR = Conv_FLU_Char
FIGURE 5E: CONTINUED
Figure 3C: Continued
(DATE) 06/09/71

(TITLE) SECOND AND 38 (ABBA FRAN INTERVIEW)

XXX CONSIDERATIONS OF SYMPATHY.

(EXCERPTS) QUESTION: WHAT IS THE SIGNIFICANCE OF THE INTENSIVE
SHIPMENTS OF SOVIET ARMS TO EGYPT?

ANSWER: FIRST OF ALL, THIS IS NOTHING NEW. SOVIET POLICY HAS
BEEN CONSISTENT IN THIS MATTER SINCE 1967. I ASSUME THIS
IS ALSO A REACTION TO THE WEL-KNOWN FACT THAT ISRAEL HAS BECOME
STRONGER. THE USSR FINDS ITS INFLUENCE IN EGYPT MIGHT BE WEAKENED;
IT HAS NOT WON GREAT ACHIEVEMENTS FOR ITS PRETENTS OR ALLIES. THE
EGYPTIANS COULD ASK: WHAT Sort OF ALLY ARE YOU HERE WE ARE
APPROACHING THE FIFTH YR. AND NOT A SINGLE SOLDIER HAS MOVED.
EITHER YOU ARE NOT TRYING TO HELP US, OR YOUR HELP IS NOT
PARTICULARLY EFFECTIVE. TENSIONS ARE PROBABLY RISING ABOUT THE
LARGE SOVIET PRESENCE; THEREFORE THE USSR MUST ASK ITSELF HOW IT CAN
GUARANTEE THAT ITS INFLUENCE IN EGYPT WILL CONTINUE TO BE CONSIDERABLE.
THE BEST WAY TO DO THIS IS TO STRENGTHEN ITS STATUS AND PRESTIGE IN THE
EGYPTIAN ARMY BY SHIPPING ARMS. I BELIEVE THERE IS ALSO
A DESIRE TO THREATEN AND PRESSURE ISRAEL AND THE UNITED STATES
BY SAYING: "IF YOU DO NOT TAKE US MORE INTO ACCOUNT, WE MAY BREAK
OUT." I BELIEVE THERE IS A VERY POWERFUL, PERSISTENT
WAR OF NERVES HERE.

QUESTION: AND IT IS UNDER THESE CONDITIONS THAT YOU HAVE
RECEIVED REPORTS THAT 915-23'S ARE BEING SENT TO THE EGYPTIANS?

ANSWER: OF COURSE, ALTHOUGH I AM NOT CAPABLE OF DENYING--AS
I AM NOT CAPABLE OF CONFIRMIN THE FACT--THAT PLANES OF THIS
TYPE HAVE ARRIVED.

QUESTION: YOU ARE NOT CAPABLE, OR YOU DO NOT WANT TO?

ANSWER: I MEAN THE INFORMATION I POSSESS DOES NOT ENABLE ME TO
SAY WITH CERTAINTY WHETHER OR NOT SUCH PLANES HAVE ARRIVED. WHAT
IS CLEAR IS THAT MANY HIGH-QUALITY WEAPONS HAVE ARRIVED AND THAT
THE RATE OF SHIPMENT IS HIGH. THUS, WHETHER A REPORT ABOUT THIS
OR ANY OTHER TYPE OF PLANE IS FOUND TO BE TRUE DOES NOT ADD OR
DETACT ANYTHING. THE FACT IS THAT THERE IS A CONTINUOUS REINFORCEMENT
OF THE EGYPTIAN MILITARY DEPLOYMENT BY THE USSR. AS FOR THE REPORT,
I AM SURE THAT THE PROVINCENCE GIVEN TO IT IS LINKED TO THE EFFORT BOTH
TO CALM EGYPT ABOUT SOVIET INTENTIONS AND TO PRESSURE ISRAEL
AND THE UNITED STATES. HERE I MUST SAY THAT THE IMMEDIATE REACTION
TO THESE REPORTS BY U.S. DEFENSE SECRETARY LINTER WAS VERY BENEFICIAL.
HE SAID THESE REPORTS ARE A SOURCE OF WORRY AND ARE WELL-FOUNDED AND
THAT THE UNITED STATES WILL NOT BUDGE FROM ITS POLICY OF PREVENTING
AN UPRISING OF ISRAEL'S ARMS BALANCE.

QUESTION: THE RUSSIANS HAVE PROVED MORE THAN ONCE THAT THEY OFTEN
COUNT MORE ON THEIR ENEMIES' WEAK POINTS THAN ON HAVING A GENUINE
DESIRE TO PUT THEIR THREATS INTO PRACTICE. WHEN THE ENEMY WITHSTANDS
THE PRESSURE, THE RUSSIAN STRAP SOMETIMES TURNS OUT TO BE A PAPER
BEAR. IF I WERE TO ASK YOU, SIG, HOW FAR CAN WE GO IN THIS MATTER,
WHAT WOULD YOU REPLY?

ANSWER: I DO NOT THINK HISTORIANS WILL SAY OF ISRAEL THAT ITS
NERVES HAVE BEEN WEAK DURING THE LAST FEW YEARS. AT ANY RATE,
WE HAVE NEVER HEARD SUCH A COMPLAINT FROM EITHER OUR ENEMIES OR
OUR FRIENDS. AS FOR THE CHANCES OF SOVIET INTERVENTION, I DO
NOT RECOMMEND THAT WE THROW OURSELVES IN A PANIC TO THE EXTENT
THAT WE ARE PARALYZED AND IN EFFECT FORCED TO WITHDRAW. THERE
ARE, AFTER ALL, SEVERAL OBSTACLES IN THE PATH OF SOVIET INTERVENTION.
FIRST OF ALL, OUR POWER IN THE AREA IS NOT INCONSIDERABLE AND THE
USSR'S STRENGTH IS NOT UNLIMITED. SECOND, THE USSR ATTACHES WEIGHT

FIGURE 6: TARGET FILE LISTING
TO THE POSSIBILITY OF U.S. INTERVENTION, I THEREFORE DO NOT BELIEVE WE ARE COMPLETELY ABANDONED. IT WOULD NOT BE REALISTIC, HOWEVER, TO ASSUME THAT THE USSR WILL CHANGE THE POLICY IT ADOPTED WHEN IT STEPPED UP ITS INTERVENTION ON THE EVE OF THE CRUSADES IN THE SUMMER OF 1970.

QUESTION: WHAT ABOUT OUR RECENT ATTEMPTS TO BREAK THE RING OF ARAB HOSTILITY BY MAKING ONE ARAB STATE LESS FANATICAL THAN OUR NEIGHBORS?

ANSWER: NOT ONLY TO THE LESS FANATICAL STATES, BUT EVEN TO OUR NEIGHBORS. WE NEVER MISS OPPORTUNITIES TO EXPRESS OUR DESIRE FOR PEACE AND OUR OUTLOOK. WITHOUT GIVING INTO DETAILS, I WILL SAY THAT NOT ONLY IN THE CASE OF REMOTE ARAB COUNTRIES BUT EVEN CLOSE ARAB COUNTRIES, WE MAY MAKE USE OF THE GOOD SERVICES OF IMPORTANT PERSONS WHO GO FROM CAIRO TO JERUSALEM, OR FROM JERUSALEM TO AMMAN, TO PRESENT A CORRECT PICTURE OF OUR POLICY TO THE OTHER SIDE. THIS ALSO APPLIES TO COUNTRIES WHICH ARE LESS INVOLVED IN THIS DISPUTE—ARAB COUNTRIES FURTHER AWAY FROM THE AREA. OUR EFFORTS ARE CONSTANT.

QUESTION: WHAT ABOUT THE DEMAND FOR OUR MIRAGES, WHICH ARE STILL IN FRANCE?

ANSWER: THE DEMAND IS STILL VALID AND THE MIRAGES ARE STILL IN FRANCE.

QUESTION: MR. EBAN, DO YOU EVER FEAR THAT THE POLITICAL OPTIONS AT OUR DISPOSAL LIMIT US FROM THE OUTSET? THE EGYPTIANS SMILE SOMETIMES ON MOSCOW AND SOMETIMES ON WASHINGTON AND GET RESPONSES FROM BOTH SIDES, WHILE WE HAVE TO WORK HARD EVEN WITH OUR FRIEND, THE UNITED STATES.

ANSWER: NATURALLY, THIS DIFFERENCE EXISTS, BUT IF I WERE THE EGYPTIAN FOREIGN MINISTER I WOULD NOT BE GREATLY COMFORTED BY IT, FOR AFTER ALL THE PROFIT AND ENJOYMENT OF THE GREAT OPTIONS, WHAT HAS THE RESULT BEEN? THE RESULT IS THAT WE REMAIN EXACTLY WHERE WE WERE ON THE DAY AFTER THE WAR AND NO ONE HAS SUCCEEDED IN MOVING A SINGLE ISRAELI SOLDIER FROM A SINGLE POSITION.

QUESTION: HAVE WE EVER TRIED TO OPEN A DIALOG WITH THE USSR OR OTHER EAST EUROPEAN COUNTRIES?

ANSWER: YES, WE HAVE TRIED TO MAKE THE USSR REALIZE THAT ISRAEL DOES NOT REGARD THIS RUPTURE AS NECESSARY. NOR DO WE BELIEVE THAT THE EXISTENCE OF DIFFERENCES NECESSITATES BREAKING OFF RELATIONS. WE HAVE BEEN USING MANY CHANNELS RECENTLY TO CONVEY TO THE USSR THE KNOWLEDGE THAT IF IT WANTS A DIALOG WITH US, SEPARATELY FROM AND PRIOR TO RESUMPTION OF RELATIONS, WE ARE OPEN TO THIS.

(MORE)

17 APR 1970 44
NNNN
NNNN

FIGURE 6: CONTINUED
C. Features and Capabilities of the DDL Language and Processor

(1) DDL language – data definition

A descriptive statement is of the form

```
name IS attribute (description);
```

where `name` is an identifier given to a particular attribute,

`attribute` can be a FILE, RECORD, GROUP, FIELD, TAPE, DISK, or CARD, and

`description` is a statement of the characteristics peculiar to that

particular attribute name.

To categorize and illustrate these points, examine the following table:

<table>
<thead>
<tr>
<th>ATTRIBUTE</th>
<th>DESCRIPTION</th>
<th>ILLUSTRATION (see Fig. 5a)</th>
</tr>
</thead>
<tbody>
<tr>
<td>FILE</td>
<td>STATES THE NAME OF THE STORAGE MEDIUM ON WHICH THE FILE IS FOUND</td>
<td>SFFILE (LINE 2) IS THE</td>
</tr>
<tr>
<td></td>
<td>STATES THE NAME OF THE RECORDS WHICH COMprise THE FILE.</td>
<td>SOURCE FILE. IT IS STORED</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ON MAG_TAPE AND IS COMPRISED</td>
</tr>
<tr>
<td></td>
<td></td>
<td>OF RECORDS IN_RECORD.</td>
</tr>
<tr>
<td>RECORD</td>
<td>LISTS THE NAMES OF THE COMPONENT MEMBERS OF THE RECORD.</td>
<td>IN_RECORD (LINE 3) IS COM-</td>
</tr>
<tr>
<td></td>
<td></td>
<td>POSED OF FIELDS CALLED</td>
</tr>
<tr>
<td></td>
<td></td>
<td>NUM, DOCUMENT, AND IN_TEXT,</td>
</tr>
<tr>
<td></td>
<td></td>
<td>AND OF THE GROUP CALLED</td>
</tr>
<tr>
<td></td>
<td></td>
<td>INFORMATION, IT HAS A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>LOGICAL RECORD LENGTH OF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4088 BYTES.</td>
</tr>
<tr>
<td>GROUP</td>
<td>LISTS ALL THE SUB-GROUPS AND FIELDS WHICH COMprise IT.</td>
<td>INFORMATION (LINE 6) IS A</td>
</tr>
<tr>
<td>FIELD</td>
<td>DESCRIBES THE ATTRIBUTES OF THE FIELD.</td>
<td>GROUP COMPOSED OF 10 FIELDS.</td>
</tr>
<tr>
<td>STORAGE MEDIA</td>
<td>THESE STATEMENTS GIVE A DESCRIPTION OF THE STORAGE MEDIUM AND THE PHYSICAL</td>
<td></td>
</tr>
<tr>
<td></td>
<td>SUCH AS BLOCK SIZE, RECORD LENGTH, LABELS, EXTERNAL NAMES, ETC.</td>
<td>MAG_TAPE (LINE 18) IS THE</td>
</tr>
<tr>
<td></td>
<td></td>
<td>NAME OF A DESCRIPTION OF A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>TAPE WITH VARIABLE BLOCK</td>
</tr>
<tr>
<td></td>
<td></td>
<td>LENGTH OF 4096 BYTES AND EXTERNAL</td>
</tr>
<tr>
<td></td>
<td></td>
<td>NAME OF X00279. ALL OTHER</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ATTRIBUTES FOR THIS TAPE ARE</td>
</tr>
<tr>
<td></td>
<td></td>
<td>DEFAULT TO THE SYSTEM.</td>
</tr>
</tbody>
</table>


## DDL language - data mappings

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>MEANING</th>
<th>ILLUSTRATION</th>
</tr>
</thead>
<tbody>
<tr>
<td>&lt;-</td>
<td>MAPS A CONSTANT INTO A TARGET FIELD</td>
<td>OUT_DATE_LABEL (LINE 22) IS A TARGET FIELD INTO WHICH IS MAPPED THE CONSTANT 'DATE'</td>
</tr>
<tr>
<td>&lt;=</td>
<td>MAPS A SOURCE FIELD INTO A TARGET FIELD</td>
<td>OUT_DATE (LINE 23) IS A TARGET FIELD INTO WHICH IS MAPPED THE CONTENTS OF THE SOURCE FIELD DATE</td>
</tr>
</tbody>
</table>

**NOTE:** THE DDL PROCESSOR MAKES IT VERY EASY TO ELIMINATE FIELDS FOUND IN THE SOURCE FILES WHEN CONSTRUCTING THE TARGET FILE. SEVERAL OF THE SOURCE FIELDS, SUCH AS ZEM AND FIBS, HAVE BEEN EXTRACTED BUT ARE NOT COPIED ONTO THE TARGET FILE, AS SEEN IN THE TARGET FILE DESCRIPTION IN FIGURE 5A.
(3) DML routines - data manipulation

<table>
<thead>
<tr>
<th>TYPE OF DML ROUTINE</th>
<th>WHEN THE USER NEEDS TO PROVIDE A DML PROCEDURE</th>
<th>ILLUSTRATION</th>
</tr>
</thead>
<tbody>
<tr>
<td>LOCK</td>
<td>THIS TYPE PROCEDURE DETERMINES WHETHER OR NOT A PARTICULAR RECORD IS TO BE PROCESSED</td>
<td>(SEE FIGURE 5A)</td>
</tr>
<tr>
<td>LENGTH</td>
<td>THIS TYPE ROUTINE PERMITS VARYING FIELD LENGTHS FROM ONE RECORD TO THE NEXT BY COMPUTING THE LENGTHS AT RUN-TIME</td>
<td>THE LENGTH OF FBIS (LINE 14) IS COMPUTED DYNAMICALLY BY THE DML ROUTINES FBISLEN.</td>
</tr>
<tr>
<td>CRITERIA</td>
<td>THIS TYPE ROUTINE ALLOWS A GROUP OR FIELD TO HAVE A VARIABLE NUMBER OF MEMBERS BY CHECKING THE EXISTENCE OF EACH SUCCEEDING MEMBER.</td>
<td>INFORMATION (LINE 3) IS DECLARED TO APPEAR 0 TO 1 TIMES IN THE SOURCE RECORD DEPENDING ON CERTAIN CRITERIA CONSIDERED IN THE USER PROVIDED DML ROUTINE INTRO. THE TARGET GROUP OUT_INFO (LINE 23) APPEARS IF AND ONLY IF INFORMATION DOES.</td>
</tr>
<tr>
<td>CONVERSION</td>
<td>SPECIAL ROUTINES WHICH WILL MODIFY THE CONTENTS OF A FIELD</td>
<td>PROC 4 (LINE 28) SCANS FOR AND ELIMINATES SPECIAL BREAK CHARACTERS IN IN_TEXT BEFORE THE SOURCE FIELD IS STORED IN OUT_TEXT.</td>
</tr>
</tbody>
</table>

(4) The example shows the specification of a tape to tape copying and conversion of a file (MAG_TAPE (line 18) MAGAZINE (line 29)); conversion between files on other storage media can be handled as well.
D. Evaluation

Some interesting statistics regarding the lines of code, core requirements, and time requirements during the various phases of processing for this sample run are given in Figure 7. The statistics regarding the number of records processed and the time it took to transfer and manipulate the data reveal that the efficiency of the generated program is comparable to one written by the conventional manual manner. At the same time, however, it took a person a lot less time and lines of DDL statements than required in order to write the program by hand.
**Storage and Time Requirements:**

<table>
<thead>
<tr>
<th></th>
<th>Core</th>
<th>CPU Seconds</th>
<th>I/O Seconds</th>
<th>Number of Statements of Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>DDL Compilation</td>
<td>190K</td>
<td>2.82</td>
<td>14</td>
<td>30 DDL</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>125 DML</td>
</tr>
<tr>
<td>PL/I Compilation</td>
<td>IRRELEVANT SINCE PL/I COMPILER USES AS MUCH CORE AS AVAILABLE FOR EFFICIENCY</td>
<td>2.15</td>
<td>4</td>
<td>272</td>
</tr>
<tr>
<td>Execution</td>
<td>104K</td>
<td>33.9</td>
<td>27</td>
<td>3222 BYTES OF MACHINE LANGUAGE INSTRUCTION</td>
</tr>
</tbody>
</table>

**Other Execution-Time Statistics:**

<table>
<thead>
<tr>
<th></th>
<th>Number of Blocks Transferred</th>
<th>Number of Logical Records Per Block</th>
<th>Block Length in Bytes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input File</td>
<td>143</td>
<td>1</td>
<td>4096</td>
</tr>
<tr>
<td>Output File</td>
<td>427</td>
<td>100</td>
<td>7500</td>
</tr>
</tbody>
</table>

**Miscellaneous Figures:**

- CPU seconds to produce each logical output record — .0011
- Channel seconds to produce each logical output record — .0006
- Minutes for trained human to write DDL/DML statements — 40
- Number of runs for trained humans to get a bug-free program — 3

**Note:** All the above figures are based on a run of the example made on an IBM/168 Computing Facility.

Figure 7 QUANTITATIVE STATISTICS
III. CURRENT RESEARCH

Current research is concentrated on developing a Processor whose principle is similar to DDL's, but which will be able to produce programs of a much wider class. The current processor is limited to generating programs which accept a single sequential file and produce a single target sequential file. The processor is currently being extended to enable it to accept descriptions of multiple files whose structure is not necessarily sequential. Such a processor would then generate programs which would accept and process any number of files and produce as output any number of desired files.

The processor would involve new techniques and procedures not present in the current processor. These techniques would have to be capable of producing programs which would input the files and sequence their reading in the most efficient manner and transform them into a set of desired output files, all this from non-procedural user specifications describing his source and target files. This would involve construction of networks of all involved data elements interacting with routines that process them, followed by designing and structuring the object program that will process the data most efficiently. The generated program would consist of all the I/O related instructions, the logical sequencing and control, and invocations of DML routines. Thus, generated modules would again be the top-level of the desired program with the low level data manipulation to be provided by the user's DML routines. Such a processor would be another step towards the automatic generation of data processing programs and would obviously cover a much wider class of applications.

IV. OTHER POSSIBLE FUTURE WORK

Other possible future extensions to the system might include the following areas in order to make the processor of wider use:

(a) If automatic generation of COBOL rather than PL/1 programs is desired, this could be effected by rewriting the output modules of our processor.

(b) As we have mentioned above, we could accept another specification language other than our DDL with the aid of our SAPG.

(c) Research could be carried out to develop modules to generate reports logically. That is, a processor which accepts non-procedural and logical description of desired reports could be developed to design and produce programs for report writing much in the same way that the DDL Processor generates files.
Complete automation of generating programs is, of course, a long way off, but the above-described system is a small step in that direction. For a more complete analysis of the role of automatic program generation in software development, see [2].

ACKNOWLEDGEMENTS

The author would like to thank Dr. N.S. Prywes, the project supervisor, for his direction and supervision of the project. Acknowledgement should be made to Dr. J.A. Ramirez who, as a graduate student, was system designer and original project leader of the team that implemented the first version of the DDL processor.
AUTOMATIC GENERATION OF SOFTWARE SYSTEMS - A SURVEY

N. PRWES

Department of Computer and Information Sciences, University of Pennsylvania

ABSTRACT

Research and development on automatic programming has been underway for nearly twenty-five years, and will continue for many years. An ultimate objective is the development of methods and facilities to produce software automatically, on demand, for the businessman, industrialist or scientist. This paper considers this proposition and examines a number of related questions: what is a plausible sequence of developments?, what is the social-economic need for such automatic programming advances?, what is the extent of the potential impact of such advances?, what are the envisaged methodologies? how will they be employed? and what are the open research questions? The paper is intended to provide motivation for, and critique of, research toward the above ultimate goal of automatic programming.
1. INTRODUCTION:

Research and development on automatic programming has been underway since early applications of digital computers, for nearly twenty-five years, and will continue for many years. It has started with the use of the computer to relieve the programmer of miscellaneous detail. Its ultimate objective is envisaged as a situation where software would be automatically generated for the businessman, industrialist or scientist, on demand. The computer would have to access the combined knowledge of the respective field of the application and of computer system design and programming in order to generate automatically the needed software, ready for use. This ultimate objective of automatic programming is the main subject of this paper.

The paper is organized in two parts. The first part discusses the widespread concern that cost and effectiveness of software development threaten the continued growth of use of computers. This is offered as a motivation for acceleration of the research on automatic programming to achieve the above cited ultimate objective. The second part of the paper attempts an assessment of the methods of achieving the ultimate objective of all automatic software generation.

The first part of this paper is divided into two sections. Historic and economic contexts of advances in automatic programming are discussed briefly in the next section, as a basis for evaluation and projection of progress.

1. For instance, early development of mathematical routines at Harvard University, University of Pennsylvania and Cambridge University.

*Work supported by ONR Information Systems Program, Contract #N00014-67-A-0216-0014
From midway vintage point between the beginning and ultimate goal, it is possible to see that progress on automatic programming has followed a natural sequence where earlier work established foundations for later advances. Also, that rate of progress has been a function of the economic pressures of demand and costs for programming in employment of computers in society.

Then, in the section that follows, the software development cycle is analyzed, to identify the components that need to be automated and to assess the benefits from their automation.

Subsequent two sections constitute the second part of the paper, which discusses future advances in automatic generation of software. The advances described in the first of the two sections are currently underway, and are believed to be capable of replacing the human programmers who are engaged in translating non-procedural specifications into computer programs for processing data. This is referred to in the following as automatic design and implementation of program modules. It is required that a computer engaged in performing this function, have access to knowledge on computer system design and programming, but that it may be independent of knowledge in the field of a specific application.

The second part, automatic generation of overall problem requirements, is based on computer access also to knowledge in respective specific application fields.

PART I CURRENT PROBLEMS IN SOFTWARE DEVELOPMENT

2. OVERVIEW OF HISTORIC AND ECONOMIC CONSIDERATIONS

The total process of development and use of data processing systems has been pictured as consisting of many layers. Figure 1 illustrates such a multi-layer structure by showing thirteen such layers. The examination of this structure is interesting in that it provides information on future areas of progress of automatic programming. The layers in Figure 1 are named to indicate their general function. A reader desirous of more specific definitions
AUTOMATION DEMAND
EVALUATION OF ALTERNATIVE AUTOMATION PLANS
FUNCTIONAL SPECIFICATIONS OF COMPUTER SYSTEM

EVALUATION OF FILE STRUCTURE ALTERNATIVES
PROGRAM MODULE IDENTIFICATION
PROGRAM MODULE INPUT/OUTPUT LOGICAL SPECIFICATIONS
PROGRAM DESIGN
HIGH LEVEL (COMPILER, JCL&DM) LANGUAGE PROGRAMMING

HIGH LEVEL TO ASSEMBLY LANGUAGE
ASSEMBLY LANGUAGE TO MACHINE CODE

PROBLEM AND SUPERVISOR MACROS
PROBLEM INSTRUCTION SEQUENCES
MICRO INSTRUCTION PROCESSING OF DATA

OVERALL
- PROBLEM REQUIREMENTS

PROGRAM DESIGN AND IMPLEMENTATION

PROGRAM TRANSLATION AND OPTIMIZATION

- EXECUTION

FIG. 1 TRANSLATION/DEVELOPMENT LAYERS IN DATA PROCESSING SYSTEMS AND IN THEIR APPLICATIONS
for the layers may have to add additional layers. The top three layers in Figure 1 are associated with the process of determining automation requirements, which sometimes requires definition of the problem. Note also that typically, a problem environment includes several other components beside the computer. The next five layers concern the design and implementation of software, in a language convenient for the problem. The two layers below, are concerned with the translation and optimization of the programs. Finally, the three lowest levels are concerned with the execution of the programs to perform the desired automation functions.

One way of regarding these layers is as a bottom-up sequence, in the sense that the automation of a bottom layer is utilized in building the layer above it. Historically, a bottom-up approach has been taken in automating these layers. The first layers to be automated were those associated with program execution. This was followed by the layers associated with language translation and program optimization. To date, a great emphasis has also been placed on the efficiency and utility of programming languages. Improved facilities were quickly put to a test in a variety of applications. Relatively, little automation has been applied to the top seven layers in Figure 1 and the translations from one layer to the one below it have been done manually by appropriate software specialists.

Another, top-down view of Figure 1, pictures the layers as a series of transducers—translators, where the flow is downward so that output of a top layer is used as an input to the layer below it. (A feedback flow is used for corrections and modifications)

While improvements in hardware, system software and programming languages

continue to make programming more effective, they are not sufficient to compensate for the dramatic projected increases in demand for software. One approach to projection of demand of software is as follows: The ratio of cost of software to hardware, which in 1970 was 2:1, is projected to increase by 1985 to 10:1. The U.S. Bureau of Labor Statistics 1970 estimates show a work force of 360,000 engaged in software development in the United States. Of these 137,000 were classified as business programmers, 97,000 as business systems analysts and the remainder were employed in systems and scientific software. These estimates are on the low side as they exclude some segments of the U.S. economy and were registered at a low economic point of 1970. In spite of the decreasing costs of computer equipment, total U.S. revenues from computing hardware have been increasing since 1970 at a real rate that may be averaged at 10% annually. Though costs of computers have been decreasing, considering the large numbers of potential users and new areas of industrial applications, this rate of increase is projected to continue for the period of 1970 to 1985. It will amount to quadrupling the real annual cost of new computer equipment by 1985. When this is coupled with the 1985 projection of 10:1 ratio of cost of programming to the cost of hardware, this would amount to 20 fold increase in programming manpower by 1985 (a 22% annual growth rate). Obviously such a requirement would constitute not only a financial obstacle to advances in use of computers, but also such a labor force cannot be recruited, trained, or controlled effectively. In order to keep software manpower


increases at a pace with increases of total hardware costs (10% annual increase), the manpower requirements will have to be reduced by 80% by 1985, as compared with present (mostly manual) methods. 

Continuing the bottom-up sequence of development, there are considerable efforts underway on the complete automation of the five layers in Figure 1 that constitute program design and implementation. The bottom two of the five layers, will provide automatic generation of programs, based on program module logical specifications; namely, the automatic production of Ad Hoc programs in compiler languages such as COBOL or PL/1. This will reduce, particularly, the requirements for business programmers. As indicated, these programmers represent 60% of those employed in business programming. Subsequently, work of business system analysts, who are engaged in the top three of the five layers mentioned, will be reduced as well.

The manpower associated with the top four layers in Figure 1, of determination of overall problems requirements, represents currently a relatively small amount of manpower, which is primarily trained in the respective applications rather than in computer systems. Most probably the bottom-up sequence of developments will continue and will gradually effect the top layers in Figure 1. However, the automation of determining overall problems requirements may be delayed until automatic program design and generation methodology (in lower layers) has been well established. The methods under development are described in Part II below.
3. ANALYSIS OF THE SOFTWARE DEVELOPMENT PROCESS

Advances in automation of programming are needed in large scale software development projects, particularly in the business data processing area which employs the majority of programmers and where there is likely to be the most growth in demand for such services. Examples of such systems are a new financial system required by top management to provide new reporting for improved financial control and planning, or systems to support new products such as reservations services, medical services or a new insurance plan. Large scale projects are not the creation of a single or a small group of managers. Such systems have an impact on top management, business specialists in areas such as accounting or finance, operational staff of the organization, and finally the public that interact with the organization such as customers, vendors, etc. This is one of the reasons why large scale software development projects span several years of development time and involve activities of various groups within an organization. The objective below is to put programming automation in the context of such projects, to determine the requirements of research in greater detail. Figure 2 gives an overview of the software development process, indicating the respective phases, by whom they are performed, the end products and the languages used in the documentation.

Data on distribution of labor and costs in software development phases is difficult to obtain for two reasons. First, phase classification in accounting of development costs is rarely adhered to systematically\(^5\). Second, costs of software development projects vary greatly, depending on the quality of planning and execution. Costs frequently vary in the ratio of 2:1 for similar project and in some instances the ratio is much higher (20:1). Therefore, the estimates that are made below indicate widely accepted typical values.

SOFTWARE SYSTEM END PRODUCTS LANGUAGES
DEVELOPMENT PHASES
PERFORMED
BY
OVERALL PROBLEM
SYSTEM REQUIREMENTS TOP MANAGEMENT 1. OVERALL DESCRIPTION AND ANALYSIS STAFF OF SYSTEM. AD HOC
SYSTEM 2. COST/BENEFIT ANALYSIS
FUNCTIONAL SPECIFICATIONS BUSINESS (ACCOUNTING,
SYSTEM FINANCE, ETC.) 1. INFORMATION FLOW,
IMPLEMENTATION SPECIALISTS ROUTING AND SEQUENCING
COMPUTER SPECIALISTS: SYSTEM 2. DESCRIPTION OF COMPUTER FUNCTIONAL
CHIEF PROGRAMMER INPUTS AND OUTPUTS SPECIFICATION
SYSTEM DESIGN AND 3. DESCRIPTION OF DATA MANIPULATION IMPLEMENTATION
IDENTIFICATION OF PROGRAM SPECIALISTS:
REQUIRED PROGRAM COMPUTER SPECIFICATION
MODULES & FILE STRUCTURE CHIEF PROGRAMMER
DESIGN, CODE, PROGRAMMERS/
DEBUG AND ANALYSTS DOCUMENT
PROGRAM MODULE
SYSTEM INTEGRATION & INSTALLATION COMPUTER/BUSINESS
TEAM
MAINTENANCE PROGRAMMERS/
MODIFICATIONS AND BUSINESS SPECIALISTS
ADDITIONS
FIG. 2 OVERVIEW OF SOFTWARE SYSTEMS DEVELOPMENT PROJECTS
-8-
These estimates that are based on published reports and discussions with colleagues. The estimates are made to illustrate an approach to the projection of the possible impacts of automating software development. A reader whose experience indicates different estimates can substitute his own estimates and verify the validity of the conclusions. The estimates below are to be considered as averages for all business data processing projects. Similar analysis can be performed for other areas of programming applications.

In the discussion below, reference is made to the phases in Figure 2.

**Overall Problem Requirements** - This is a project kick-off phase concerned primarily with determining what information management needs and indications of the resources needed for the development. The initiation of large scale software development projects is usually prompted by important needs determined by top management. The phase includes preparing preliminary estimates supported by cost/benefits analysis. This activity requires expertise in the application area, as well as sizing computer requirements. The written description of such activity together with the indication of required resources and the decision to proceed are then communicated to individuals charged with carrying out the decision. To give a relative weight, this phase is estimated at 8% of total project cost.

**System Functional Specifications** - These specifications describe the information flow involving humans, communications and computers. They define the transactions that would be involved as well as management reports necessary for control of the activities in the system.


The computer is envisaged as a black box where the functional specifications describe all the transactions that are entered into the computer system, and the management reports and user information produced by the system. The group that prepares the specifications need not include computer specialists. However, as will be related further in Part II, it has been found useful to employ a formal functional specification language in documenting the system, which can be processed automatically to indicate incompleteness or inconsistency. Ineffectiveness and waste result where the functional specifications are incomplete or ambiguous thus causing redesign and reprogramming. The discipline imposed by the use of such specification languages has been considered as beneficial to reducing total development costs, especially in controlling the progress and flow of work during a development project. 22% of the total costs of software development projects is estimated for this phase.

The remaining 70% of the cost of a software development project is estimated to be for development of program modules that perform in accordance with the functional specification. If the functional specifications are complete, the work in this phase requires only computer oriented skills. Once the total environment and hardware requirements have been preliminarily determined the software development in this phase may be subdivided into three steps.

**File Structure and Module Design** - The first step in this phase is the specification of file structures and program modules. The functional specifications written by the business specialists in the previous step are now broken down into distinct logical specifications for each program module. Furthermore, the data structures on which the modules act can now be specified.
A program module is typically associated with a transaction, an updating or a reporting function. In a typical $1,000,000 project there would be 50 to 100 program modules each consisting of 1000 to 2000 lines of high level language code. The objective of this step is to obtain a sequence of input-output operations and data organization that minimize the cost of data processing. The product of the design activity is the generation of file structure specifications and program module logical specifications which incorporate the system functional specification on a per-module basis. 15% of project cost is estimated to go into creating this specification. (In many projects this step is integrated with program design, described below, and it is not possible then to estimate costs separately).

Program Design, Code and Debug - The next step is the design of each program module, its coding, debugging and documentation. The difficulties encountered currently in this step are due to having to supervise large numbers of programmers, and the low management visibility of progress or lack thereof. Therefore, problems are discovered late during the debugging of individual program modules, even later or during installation, resulting in delays and costs beyond original estimates. This step is estimated to require currently 25% of the total project cost.

System Integration and Installation - The last step in the programming phase consists of integration of individual program modules into the total system, testing and sometimes parallel operation of the system. It is during this phase that problems that have not been previously visible to management come to light. They may appear as malfunctions that must be corrected. If difficulties are discovered in user operation and control of the system, the indicated changes must be communicated to modify the functional specification of either the entire system or specific program modules and only then can modifications be
### TABLE I  SUMMARY OF ESTIMATES OF PERCENTAGES OF SOFTWARE DEVELOPMENT COSTS ATTRIBUTABLE TO PHASES, AND ENVISAGED REDUCTIONS DUE TO AUTOMATION

<table>
<thead>
<tr>
<th>Phase Description</th>
<th>Estimated % of Total Cost with Traditional Programming Approach</th>
<th>Estimate of Potential Reductions (in % of Cost) Due to:</th>
</tr>
</thead>
<tbody>
<tr>
<td>Determination of Problem Requirements</td>
<td>8</td>
<td>-6</td>
</tr>
<tr>
<td>Generation of System Functional Specs</td>
<td>22</td>
<td>-6 2</td>
</tr>
<tr>
<td>Programming:</td>
<td>70</td>
<td>2</td>
</tr>
<tr>
<td>File and Module Design</td>
<td>15</td>
<td>-10 -2</td>
</tr>
<tr>
<td>Code, Test, Debug and Document</td>
<td>25</td>
<td>-17 -5</td>
</tr>
<tr>
<td>Integration &amp; Installation</td>
<td>30</td>
<td>-20</td>
</tr>
<tr>
<td>Total</td>
<td>100</td>
<td>-32 -15 -22 -11 20</td>
</tr>
</tbody>
</table>
made. Depending on the quality of testing prior to installation this last sub-phase may account for 30% of the total costs of the project.

Maintenance - After the system has been in operation, it will require maintenance to correct errors not determined during the installation process. But even more so it will require maintenance to modify and add facilities. This is a post-software-development phase which must be facilitated during the development itself. The software development should provide a method by which modifications can be orderly entered in the specifications, the program modules, the documentations and the operational system itself.

The automation of any one phase imposes a discipline on the entire process which brings savings in the adjacent phases of the process as well. For instance, the discipline provided by a requirement to specify program modules in a formal functional specification language and an automatic generation of programs would assure that relatively bug free programs be provided to the system integration phase.

Table 1 summarizes the estimates of distribution of costs over the phases of the software development process as indicated previously. These estimates are necessarily subjective as indicated previously. A bottom up sequence of development is assumed in Table 1, with automatic program design and generation accomplished first* followed by automatic file and module design, automatic functional specification and finally automation of the determination

*The estimates of savings due to automation of program generation are inclusive of savings due to improved operating systems and language facilities shown in the five bottom layers of Figure 1.
problem requirements. The objective of such a succession of developments is to reduce the cost of software development by 80% over a period lasting to 1985, as illustrated in Fig. 3. The estimates of savings of cost due to the four envisaged increments in automation of programming, represent a guess of the author, after extensive discussions with co-workers in this field. The estimates of potential contributions of automation of software development phases are also based on considering the potential methodologies as described below. Therefore, more will be said on these estimates, at the end of Part II.

PART II FUTURE ADVANCES IN AUTOMATION OF SOFTWARE DEVELOPMENT

4 AUTOMATIC DESIGN AND IMPLEMENTATION OF PROGRAMS

This section examines the automation of the five layers in Figure 1 shown as constituting program design and implementation. This activity requires only computer related knowledge. The automation activities of the higher layers in Figure 1 that require knowledge of specific applications are discussed in a subsequent section. The "pure" computer art dependent structures are on a higher abstraction level than the business functions of a specific type of industry. The automating of the generation of these structures can serve as a foundation, and can be used in a variety of businesses and industry applications.

The discussions in this section involve only logical-abstract descriptions of the automation. The translations from the logical-abstract models to physical devices are performed in the control programs in lower five layers of Figure 1, such as Data and Teleprocessing Access Methods, etc.

However, it's important to note at this point that approaches to automating software development, where application and computer related knowledge
CURRENT STATE OF THE ART SOFTWARE MANPOWER NEEDS 22% ANNUAL INCREASE

TARGET SOFTWARE COST REDUCTIONS

HARDWARE REVENUES (1970 $)
10% ANNUAL INCREASE

1970 HARDWARE (U.S. DOMESTIC REVENUES)* $5 BILLION
1370 SOFTWARE BUSINESS PROGRAMMERS 137000
BUSINESS ANALYSTS 92000
SYSTEM & SCIENTIFIC 66000
TOTAL 360,000


FIG. 3. SUMMARY OF SOFTWARE/HARDWARE COSTS AND POTENTIAL CONTRIBUTIONS TO COST REDUCTIONS

-15-
were integrated, are being pursued as well. One such approach consists of using prefabricated program components oriented to a specific type of business or industry. The components are appropriately parametrized so that they can be adapted to use by many organizations. Typically a potential user selects functional components that are required in his operation. Questionnaires and check-lists are provided to facilitate gathering the user requirements. Also an Editor and a Report Generator are provided to enter user parameters and report formats. Such systems are used by service organizations and computer manufacturers to install small business systems for wholesale and distribution industries.\(^8\) There are shortcomings with this approach on two levels. First there is a continuing requirement for programmer skills for selecting components and determining the values of the parameters. Also frequently, necessary functions cannot be performed by previously prefabricated components and additional special purpose programs are required. On another level, use of such packages requires molding the needs for automation of a business into the structure of the prefabricated software packages. No provisions are made to determine management's critical decisions regarding efficient operation and whether data for decision making is indeed made available to management. This approach can be based on a more general and powerful program generation method described below instead on prefabricated components\(^9\) and will be discussed further in the next section.

Following the bottom-up sequence (Figure 1), automatic program module generation is discussed below first, and is followed by the automatic file

---

\(^8\) Examples are IBM Customizing Service for users of IBM System 3 and the proprietary services of Keydata and others for the distribution industry.

structure and module selection methodology.

**Automatic program module generation:** This methodology is based on the use of processors capable of generating broad classes of programs. Figure 4 illustrates this methodology. Box (1) at the bottom of Figure 4 illustrates a broad class of programs, characterized as intended for business applications where they have to process a number of input data [or message] files and to produce a number of output files [or reports]. Box (2) illustrates a processor which can generate the program module for (1). The Program Module Generator (2) consists of two parts, for processing the input formal non-procedural functional specification of the desired program module, and for performing design and code generation.

The design and code generation programs in box (2), embody a mathematical model of a program design process. They check the consistency and the completeness of input specifications by tracing each value in the output data to its sources. From these traces as well as from requirements imposed by file structures of the respective files [e.g. sequential or indexed] they determine the sequencing of the input commands, computation and output commands to attain program module efficiency.

The program module specifications used as input in this process are a subset of the overall problem non-procedural functional specifications. Several formal languages for stating non-procedural functional specifications have been developed and some have been in use\(^\text{10}\) (see also ref. 2, 18 and 19).

---

FIG. 3 INTERACTIONS IN AUTOMATIC GENERATION OF A PROGRAM MODULE
Because of the emphasis in these languages on facilities to describe data, they are very similar to languages used to describe data bases\textsuperscript{11,12}. It is important to be able to process program module specifications that are given in any combination of several forms, such as in a formal language, table format or in a question-answer format. Also, these languages are still in an experimental phase and little usage experience is available for their evaluation. In order to have a capability to accept several selected languages or formats, and to modify them, it is desirable to generate automatically the language analysis programs. This capability is indicated in Figure 4 by a higher level language analysis program generator processor box (3) which automatically generates the module specification analysis programs based on specifications of language syntax and semantics.

The specification of a desired program module that is input to the program module generator comes in two parts which can be related to top and bottom level parts of program code, in a top-down program structure.

\textsuperscript{11} COBASYL Data Base Task Group Report, Report to the COBASYL Programming Languages Committee, ACM, New York, 1971.

The quotation below from H. Mills' explains the top-down programming structure:

"We can begin a process which we can repeat over and over until we get the whole program defined. This process is to formulate a one page skeleton program which represents that hundred page program. We do this by selecting some of the most important lines of code in the original program and then filling in what lies between those lines by names. Each new name will refer to a new segment to be stored in a library and called by a macro facility. In this way, we produce a program segment with something under 50 lines, so that it will fit on one page. This program segment will be a mixture of control statements and macro calls with possibly a few initializing, file, or assignment statements as well."

In a similar approach, two levels are defined below. The top level of the program is defined to consist of all the data definitions, input/output commands and control logic statements. This part constitutes also macro level documentation, dispensing with the need for flow-chart documentation. In the bottom-level would be routines that provide the more detailed documentation of data manipulation. These two levels have been identified because they can be related to two parts of the module specification. The top level macro design activity is largely based on the module input-output data specification. The bottom level of the program is based entirely on the data manipulation specification (except where input and output data items, records or files are associated by common names and the direct relationship of input to output is implicit). This relationship is summarized in Table 2.

A system of the type described in Figure 4, with a restriction of only one input and one output file for a program module (n=m=1), has been developed at the University of Pennsylvania by Ramirez.14 Expansion of the


<table>
<thead>
<tr>
<th>NON PROCEDURAL INPUT LANGUAGE</th>
<th>PROCEDURAL CODE LEVEL</th>
<th>PROCEDURAL CODE STATEMENTS GENERATED AUTOMATICALLY</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>TOP</td>
<td>DEFINITIONS</td>
</tr>
<tr>
<td></td>
<td></td>
<td>CONTROL LOGIC</td>
</tr>
<tr>
<td>DDL</td>
<td>BOTTOM</td>
<td>I/O</td>
</tr>
<tr>
<td>DML</td>
<td></td>
<td>MANIPULATION-COMPUTATION</td>
</tr>
<tr>
<td></td>
<td></td>
<td>CONVERSION</td>
</tr>
<tr>
<td></td>
<td></td>
<td>EVALUATION</td>
</tr>
<tr>
<td></td>
<td></td>
<td>DDL/DML MODULES</td>
</tr>
</tbody>
</table>

**TABLE 2: SUMMARY OF RELATION OF DDL/DML TO CODE IN THE AUTOMATICALLY GENERATED PROGRAM**
system for handling multiple input and output files is currently underway. The Ramirez system includes automatic capability for generating language analysis programs based on an extended BNF syntax specification and sub-routine calls that express some of the semantics (other semantics are in hand coded code generation programs). A non-procedural specification language is used for input to the program generator. It is similar in structure to that developed by CODASYL. It is also a modified subset of a language developed by Smith. The data manipulation language (DML) used is a subset of PL/1. The generated program module code is in PL/1, requiring a subsequent compilation to produce a load module. The Ramirez system produces also the necessary JCL statements and a variety of documentation, in addition to the PL/1 program module listing.

The design process in the Program Module Generator [box 2 in Figure 4] required a painstaking analysis to specify an acceptable human program design process and to state it in terms of a mathematical model, which has been programmed to constitute a part of the Module Generator. While there exist extensive work on automatically generating language analysis programs there has been only very limited research on automatic production of code generation which is one of the most laborious tasks in constructing program generators. Artificial intelligence research in the area of automatic problem solving appears applicable to the automatic generation of programs to perform design in accordance with specified methods.


The methodology to generate automatically design and code generation programs
is the least developed part of the technology that is necessary to produce
program module generators efficiently. Until such technology is developed and
applied it is possible only to proceed slowly and laborously by manually
producing design and code generation programs. As will be further discussed
below, this inability to easily "teach" a computer how to employ a method,
that a human can easily learn is an extremely difficult obstacle to surmount
on the way toward all-automatic programming.

File Structure and Program Module Definitions: This activity is based
on functional specification of the total computer system, consisting of
a non-procedural functional specification of the total input and output data
and a preliminary determination of the system hardware and software that
is required. The outcome of this process are the non-procedural functional
specifications of the respective modules. This is a more global software
design activity then the module design. Refinements are presently carried out
iteratively, where a human designer relies on automatic simulation for
evaluation. Recent developments of automation of this are reviewed below.18

The first step of the process is to analyze the overall system non-
procedural functional specifications to determine completeness and consistency.
An example of this capability is an analyzer developed for functional
specifications expressed in the ADS language.19 Analysis reports include indices

---

18 J. Daniel Couger, "Evolution of Business System Analysis Techniques,"

19 J.F. Nummaker, et.al., "A Non Procedural High Level Language For Automated
Design of Application Systems," Computer Science Dept., Purdue University,
W. Lafayette, Indiana 47906.

See also, J.F. Nummaker, Jr., "A Methodology for the Design and Optimization of
of data elements, and cross referencing of data manipulation routines and
the respective source (input) and target (output) data elements. Groups
of data elements that are connected through hierarchical relationship are
identified. Next these groups are also cross referenced with the routines
that process the data elements in respective groups. A network can be
generated where groups of data elements that interact in computations would
be connected. Closely connected groups of data element, constitute candidate
files. The related processing functions represent candidate program modules.
Attempts are then made to consolidate or partition modules and files to
increase efficiency. For instance, if two processing functions occur in the
same processing cycle and if the preliminarily selected data files overlap
greatly in having common data elements, the respective processing functions
and files may be consolidated. To make such decisions it is necessary to
refer not only to the logical structure of the data and the data manipulation
rules but also to the frequencies and cycles of the processing functions.
Partitioning of the processing files, as well as use of intermediate files
sometimes improve efficiency. For instance, effect of pre or post sorting
to order the data may be an important consideration for efficiency. A third
type of consideration is to evaluate impact of alternative file organizations.
For example, whether the data is accessible sequentially or on a random access
basis has impact on efficiency of processing. Such alternatives may be
evaluated through simulation.\textsuperscript{20}

\textsuperscript{20} A.F. Cardenas, "Evaluation and Selection of File Organization-A Model
The analysis, synthesis and evaluations of alternatives can be performed piecewise with the aid of automatic simulation and evaluation. The integration of these functions requires human guidance. An all-automatic process will require a data base of global computer design knowledge accessible to a computer. It will be necessary to research first how to formalize such knowledge or how to input it to a computer in a natural language ad-hoc manner. Until such capability is developed, it will be very costly to enter such information through manual modelling and programming. Therefore, the achievement of an all-automatic process with no human participation will be necessarily delayed for some time.

5 AUTOMATIC GENERATION OF NON-PROCEDURAL SPECIFICATIONS

The top-level system design activity discussed in this section corresponds to the top three layers in Figure 1. The top layer, named in Figure 1 "automation demand", is concerned with determining what information would management need to evaluate economic and business alternatives and to make decisions for their overall organizational progress and effectiveness. The second and third layers are concerned with developing an automatic system that would collect and make the indicated information available to management. The second layer consists of generating candidate operational concepts and computer configurations and the evaluation of these computer configurations to establish cost/benefits of alternative proposed information systems. The third layer consists of specifying the selected system in a formal manner to be directly applicable in lower layers. Simulation technology for performing such evaluations is highly developed.21

21 J. Yeh and J. Minker, "Key Word In Context Index and Bibliography on Computer Systems Evaluation Techniques, University of Maryland, Computer Science Center, College Park, Maryland, TR-246, June 1973.
Performing the top layer activity in an automatic fashion presents the greatest difficulty. It is the most complex and imaginative part of the total process. It has little to do, if any, with computer methodology, which becomes important only in lower layers. Traditionally this process involves interacting with many participants, with expertise in different disciplines: with top management, staff specialists, operational staff and sometimes with outside the organization such as customers, vendors financial and government organizations. Presumably, a future computer system that could perform this activity automatically would need access to the combined knowledge of the present participants in this process. Current techniques for entering into a computer knowledge on how to evaluate complex economic or business situations consists of coding the knowledge in program form, which requires an enormous amount of manual analysis and structuring. This is the major problem in automating this activity. Two reported approaches to this problem, by Hax and Martin 9 and by Balzer 22 are summarized and reviewed below.

The Hax and Martin approach has already been briefly described previously, in connection with use of prefabricated program components for the operational side of a business. Other features of their approach are: 1) specialize in a relatively narrow application field, reportedly in Inventory Control. 2) Incorporate simulation models for evaluating economics and business questions or operational methods. 3) use artificial intelligence techniques for communicating with the computer interactively in English language for the entry of additionally needed information into the computer.

Two areas of concern in this approach are discussed below. First, whether due to the prefabricated programs feature, the structure may be too confining

and restrictive to be used in situations where technology, or business and economic conditions change rapidly. Second, since the area of application is highly specialized and relatively narrow, the cost of the software generation system development may not be justified by potential utilization opportunities. Both the usefulness and end value of such a generalized Inventory Control system could have been tested, for instance, during the situation of major shortages in essential materials that arose at the end of 1973. Could the system for instance, have generated an inventory control system for oil products that would be effective for conserving oil and for planning utilization of oil products to minimize impact of shortages on the economy? The almost instantaneous availability of such a system would have demonstrated its value as surely being sufficiently great to justify the costs of development.

As indicated previously, existing systems of prefabricated program components have editors and question-answer facilities to collect and enter user requirements. It is not clear how the use of cited artificial intelligence techniques\textsuperscript{17,23,24} would materially enhance such facilities.

Balzer\textsuperscript{22} proposes the construction of a computer system which will have a generalized learning capability that could be utilized to enter applications related knowledge into the computer. The quotation below summarizes the functions of the computer system in his proposed concept of its operation:


1. Problem statement in natural language in terms of the problem domain.
2. Knowledge about the domain acquired interactively in natural language in terms of the complete model of the problem domain.
3. Resulting programs which are optimized with respect to data representations, control structure, and code.

This approach requires significant advances in Artificial Intelligence techniques, in such areas as knowledge representation, inference systems, learning, and problem solving, and in the codification of programming knowledge in the areas of data representations, algorithm selection, and optimization techniques.

Item 1 and 2 in the above quotation refer to the top level design activities addressed in this section. Item 3 refers to program generation and optimization activities similar to those discussed in the previous section.

The reference to "domain" is similar to the use of the word "application" above. The starting knowledge in the computer is assumed to be confined to that of computer system analysis and design. As stated, in items 1 and 2, the description of the problem that needs to be solved, together with the knowledge that is needed to solve the problem, would be imparted to the computer system through interactive user-computer question-answer sessions conducted in natural English language. The computer would then learn from the user about his business to be able to determine the information needed for business decisions and system requirements. The dependence of this type of activity on artificial intelligence methodology is stated in the second paragraph in the above quotation.

Balzer admits that his proposed system concept is conjectural. There are questions in regard to both effectiveness of the process and feasibility. In regard to effectiveness, the approach implies, for instance, that a president of a company would find it beneficial to teach his problem and his business to a computer that is equipped with only computer oriented knowledge, so that
the computer could integrate the two types of knowledge to provide an effective solution to the president's problems. Additionally, some of the information would have to be obtained from the vice-presidents for operations, marketing and finance. Consider the problem of all these participants entering information in a consistent manner so that it all can be integrated. If the artificial intelligence techniques, cited by Balzer, are to be used, the humans interacting with the computer would be required to have knowledge about how the computer acquires knowledge.

A basic assumption of this approach is that interactive communication in English is an effective way to teach knowledge to a computer, although this is a relatively slow process in teaching humans. Note also, that when humans are taught by interactive communication, they already have a basic knowledge of word meanings and relationships, which would not be true for the computer system. This problem is discussed further below. It seems that it would have been less conjectural if it was proposed that the computer could accept textbooks as input materials and incorporate the respective knowledge into the system. The text material would then also constitute a base line of the knowledge in the computer on which further knowledge incorporation may be based. Also, such a system could be continuously tested as the information was entered. For instance, at the end of entry of a Chapter from an Introductory Economics textbook, the problems at the end of the Chapter would be submitted as well, and the computer would be required to use the knowledge in the Chapter to produce answers for the problems.

Another problem area is the need to enter into the computers large vocabularies and the inadequacy of present artificial intelligence techniques cited by Balzer for handling voluminous English language communications with the
computer. The systems cited by Balzer to demonstrate that the technology is available, have vocabularies of few hundred words. To communicate applications knowledge would require vocabularies of tens of thousands of words. The problem is not simply in the larger number of words needed for English language communication of application knowledge, but in the amount of work that it takes to enter multiple word meanings and relationships. Development of methodology to tabulate multiple meanings and relationships of words has been reported. It estimates that there are 8000 high usage words which have acquired many meanings and usages. These words, as well as many other words, of lesser usage, will have to be integrated within detailed models of particular applications. To enter all this information, word by word, in an interactive process would require a long time. Other techniques, by which a computer may acquire knowledge of words purely from the usage in text, must be developed to enable rapid entry of an application oriented knowledge base into a computer system. This would eliminate the need to tabulate meaning and relationships in vocabularies. Processes of this type have been considered and developed by workers in the areas of information storage and retrieval, content analysis and text processing.

All the questions that have been raised above seem to be open research questions that should be the subject of future research. It appears, therefore, that the adequacy of artificial intelligence techniques for implementing the


type of system envisaged by Balzer is far from proven and feasibility of such a system will still need to be demonstrated. Therefore, also an effective automation of this generalized high level analysis and design process cannot be forseen with confidence.

6. CONCLUSIONS

Predictions or projections of technological developments are always hazardous, especially when made for a prolonged period such as the period lasting to the year 1985. Therefore, it is in order to comment on reasons for confidence, or lack of, in the projections.

The projections of growth of software manpower requirements are not reliable. They are based on a study of future U.S. Airforce software requirements which have been applied to the total U.S. economy. New industrial uses of computers through 1985, that should figure prominently in such projections, have not been considered. The objective in making the growth projection was for estimating a target for manpower savings from advances in automatic programming. Even if the growth projections are 50% too high, there would still be need for the advances described in the paper.

One assumption made in the paper was that software development will continue in a bottom-up fashion indicated by the layers in Figure 1. The alternative would be some major breakthroughs which would upset the past order of progress. For instance, breakthroughs in the field of artificial intelligence, by which computers could quickly "learn" methods could have such an impact. But as indicated in Section 5, there are important research questions that need to be answered before such breakthroughs can be predicted.
Large and extensive research and development activities in the areas of automatic program generation and systems and files design and evaluation are underway. These efforts are indicated in the references in this paper, and especially in the survey paper and bibliography that have been cited. This, as well as examination of proposed methods, form the main basis for confidence that the automation of program design and implementation would be successfully developed and receive wide industrial and business use by 1985. This will take place in a number of increments as discussed in Section 4.

Finally, the present prospect is that determination of system requirements, leading to a system functional specification would be performed semi-automatically, with business specialists serving as system innovators and integrators, and that they will be aided by various evaluation and language processing systems. The automatic integration of these systems would have to await advances in lower layers of Figure 1 and probably also great advances in artificial intelligence methods.