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# A General Theory of Sharing Graphs

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# A General Theory of Sharing Graphs

## Abstract

Sharing graphs are the structures introduced by Lamping to implement optimal reductions of lambda calculus. Gonthier's reformulation of Lamping's technique inside Geometry of Interaction, and Asperti and Laneve's work on Interaction Systems have shown that sharing graphs can be used to implement a wide class of calculi. Here, we give a general characterization of sharing graphs independent from the calculus to be implemented. Such a characterization rests on an *algebraic semantics* of sharing graphs exploiting the methods of Geometry of Interaction. By this semantics we can define an unfolding partial order between proper sharing graphs, whose minimal elements are unshared graphs. The *least-shared-instance* of a sharing graph is the unique unshared graph that the unfolding partial order associates to it. The algebraic semantics allows to prove that we can associate a semantical read-back to each unshared graph and that such a *read-back* can be computed via suitable *read-back reductions*. The result is then lifted to sharing graphs proving that any read-back (or unfolding) reduction of them can be simulated on their least-shared- instances. The sharing graphs defined in this way allow to implement in a distributed and local way any calculus with a global reduction rule in the style of the beta rule of lambda calculus. Also in this case the proof technique is to prove that sharing reductions can be simulated on least-shared-instances. The result is proved under the only assumption that the structures of the calculus have a *box nesting property*, that is, that two beta redexes may not partially overlap. As a result, we get a sharing graph machine that seems to be the most natural low-level computational model for functional languages. The paper concludes showing that optimality is a by-product of this technique: optimal reductions are lazy reductions of the *sharing graph machine*. We stress the proof strategy followed in the paper: it is based on an amazing interplay between standard rewriting system properties (strong normalization, confluence, and unique normal form) and algebraic properties definable via the techniques of Geometry of Interaction.

## Comments

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*Institute for Research in Cognitive Science*

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**A General Theory of Sharing  
Graphs**

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# A general theory of sharing graphs

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Sharing graphs are the structures introduced by Lamping to implement optimal reductions of lambda calculus. Gonthier's reformulation of Lamping's technique inside Geometry of Interaction, and Asperti and Laneve's work on Interaction Systems have shown that sharing graphs can be used to implement a wide class of calculi. Here, we give a general characterization of sharing graphs independent from the calculus to be implemented. Such a characterization rests on an *algebraic semantics* of sharing graphs exploiting the methods of Geometry of Interaction. By this semantics we can define an unfolding partial order between proper sharing graphs, whose minimal elements are unshared graphs. The *least-shared-instance* of a sharing graph is the unique unshared graph that the unfolding partial order associates to it. The algebraic semantics allows to prove that we can associate a semantical *read-back* to each unshared graph and that such a read-back can be computed via suitable *read-back reductions*. The result is then lifted to sharing graphs proving that any read-back (or unfolding) reduction of them can be simulated on their least-shared-instances. The sharing graphs defined in this way allow to implement in a distributed and local way any calculus with a global reduction rule in the style of the beta rule of lambda calculus. Also in this case the proof technique is to prove that sharing reductions can be simulated on least-shared-instances. The result is proved under the only assumption that the structures of the calculus have a *box nesting property*, that is, that two beta redexes may not partially overlap. As a result, we get a *sharing graph machine* that seems to be the most natural low-level computational model for functional languages. The paper concludes showing that optimality is a by-product of this technique: optimal reductions are lazy reductions of the sharing graph machine. We stress the proof strategy followed in the paper: it is based on an amazing interplay between standard rewriting system properties (strong normalization, confluence, and unique normal form) and algebraic properties definable via the techniques of Geometry of Interaction.

# 1 Introduction

Techniques to implement functional calculi based on the use of pointers (and then on graphs) were known from long time (see [Wad71,PJ86]). In such solutions sharing of graphs was implemented by pointers connecting a shared subterm (the tree spanned by the paths rooted at a given node) to the nodes accessing it. The drawback of a naive implementation of such techniques was the possibility of unwanted side-effects during the execution of variable substitutions (see [Wad71]). In fact, to replace a term  $T_s$  for the occurrences of a variable  $x$  by substituting a pointer to  $T_s$  for any pointer to  $x$  would apply such a substitution in all the shared terms  $T_t$  containing  $x$ . To fix up such a problem of soundness, the usual solution was to create a new instance of  $T_t$  in which to safely replace  $T_s$  for  $x$ . Soundness were ensured but at the cost of the duplication of any redex in  $T_t$ . Therefore, any implementation of a functional calculus in which sharing be kept at the level of terms cannot avoid duplication of redexes.

## 1.1 A fine decomposition of the $\lambda$ -calculus $\beta$ rule

The basic point of sharing graphs is to implement a sharing finer than the one obtainable keeping multiple pointers to the same subterm. To have an idea of how to achieve this, let us see how the  $\beta$  rule of  $\lambda$ -calculus could be implemented via a step-by-step graph reduction system in which the objects rewritten are nodes rather than whole terms.

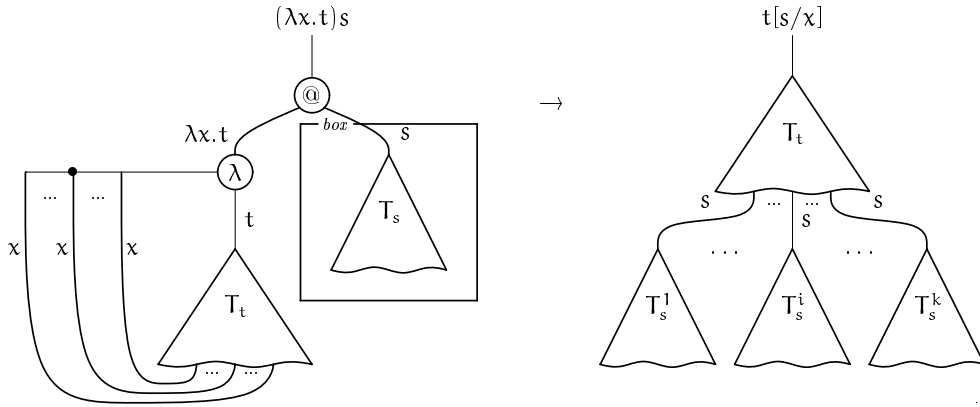


Fig. 1.  $\lambda$ -calculus  $\beta$  rule

Let us represent a  $\lambda$ -term by its abstract syntax tree and, to avoid any problem with the names of the variables, let us assume that all the occurrences of a variable  $x$  in a  $\lambda$ -term  $t$  are merged into a node (the bullet in Figure 1) connected to the  $\lambda$  abstraction  $\lambda x.t$ . According to our goal, the natural substitute for the  $\beta$  rule of Figure 1 is the sharing  $\beta$  rule of Figure 2.

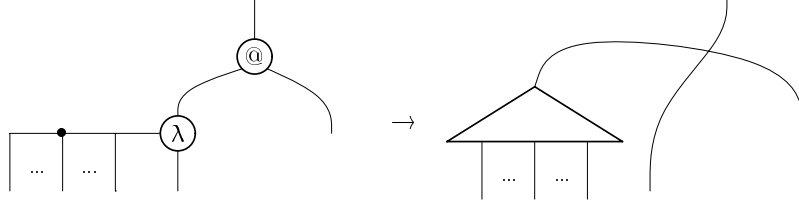


Fig. 2.  $\lambda$ -calculus sharing  $\beta$  rule

In the sharing  $\beta$  rule, a new node, say a multiplexer or *mux* for short, is inserted to connect the occurrences of  $x$  to the root of  $T_s$ . The second part of this decomposition is then the definition of a set of rules performing a step-by-step duplication of the term  $T_s$ , say a local and distributed duplication of  $T_s$ . Such rules are given in Figure 3 (for the sake of a clear design, all the muxes in that figure are binary; the generalization is straightforward). The meaning of most of them is self-evident, the only ones that deserve some thoughts are  $\lambda$ -up and absorption.

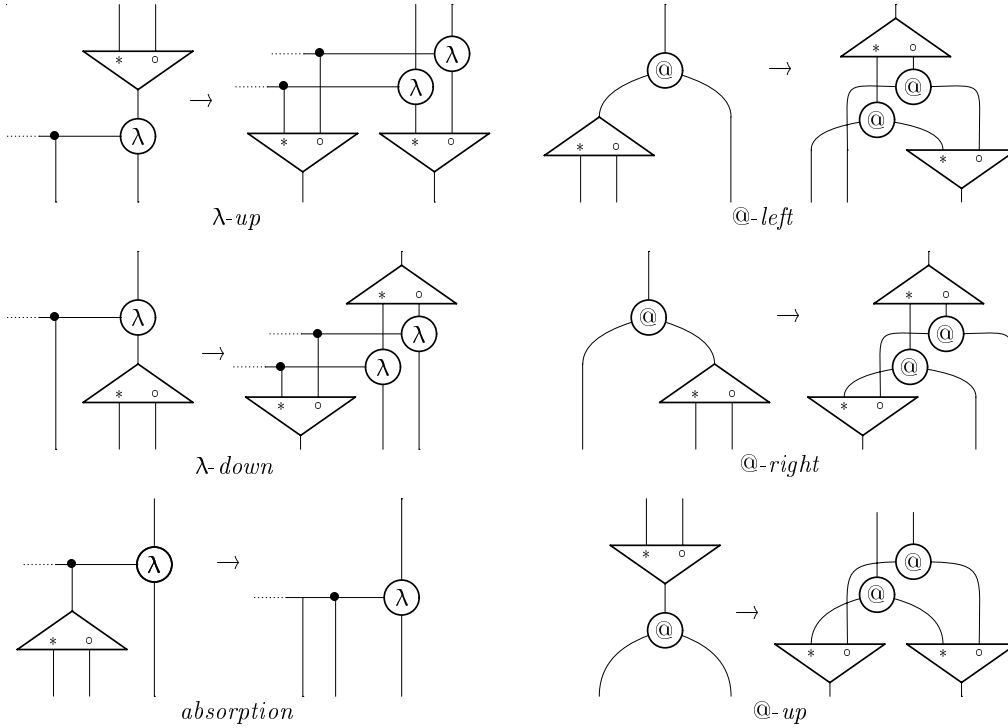


Fig. 3. Duplication rules

In  $\lambda$ -up, the mux not only duplicates the  $\lambda$  node that it points through its principal port (the vertex of the triangle), it also splits the occurrences of the corresponding bound variable (in Figure 3 we have drawn just one of such occurrences, but an analog duplication apply simultaneously to all the other occurrences). In practice, duplicating a  $\lambda$  node we also have to create a new instance of the variable that the node bound for each new instance of  $\lambda$ .

The  $\lambda$ -up duplication rule is the first divergence from standard graph reduction

techniques, for after the execution of a  $\lambda$ -up we cannot say any more that the subgraph pointed by a mux is a shared subterm. In fact, the node inserted to split the occurrences of the variable is not a sharing or multiplexing operator, it is instead a sort of *demultiplexing* node. (Since in most of the graphs we will consider the orientation is not so clear as in the case of  $\lambda$ -calculus, instead to distinguish multiplexers into muxes and demuxes, in the body of the paper we will rather prefer a more neutral classification of multiplexers into positive and negative muxes.) In some sense, each occurrence of the variable has been replaced by a hole which may be filled in two different ways, according to which instance of the shared subterm we are considering: the one relative to the left instance of the duplicated  $\lambda$  if we assume to access the shared part from the left port of the mux, or the one relative to the right instance in the other case. Hence, each demux inserted to split the variable matches with the mux below the new instances of the  $\lambda$  node, where by matching we mean that the edges entering through the back port of a mux, say through its auxiliary ports, are distinct, and that the variable occurrence connected to a given auxiliary port is bound by the  $\lambda$  node at the matching auxiliary port of the matching mux. For instance, in each rule of Figure 3, the auxiliary ports of each mux are marked by a symbol, using a distinct marker for each pair of matching ports.

Furthermore, demuxes too can duplicate nodes, as shown by the @-left rule for instance. As a consequence the duplication of  $T_s$  is no more strictly top-down, for demuxes move bottom-up.

The absorption rule applies when a mux reaches a free variable of  $T_s$  (let us assume w.l.o.g. that the we restrict to closed  $\lambda$ -terms, so any variable free in  $T_s$  is bound by some  $\lambda$  external to  $T_s$ ). The main remark about this rule is that it cannot be safely applied in any configuration as the one drawn in the picture. In fact, it is easy to build a term  $T_s$  in which a mux reaches a variable bound by a  $\lambda$  internal to  $T_s$ . Some thoughts allow to realize that in such a case to apply the absorption would not be sound. The mux of the rule would definitely have a matching mux above the  $\lambda$ , then in the place of the absorption it would be sound to let the mux duplicate the  $\lambda$  (to simplify the presentation, such a kind of  $\lambda$  rule has been dropped in Figure 3) as in the case of  $\lambda$ -down or  $\lambda$ -up.

The latter is a first bell ring warning us that we need some local information taking into account the global position of a node. In this case, such an information would help has to recognize when the  $\lambda$  is outside  $T_s$  and the absorption rule might be safely applied.

So far, absorption is the only rule that erases muxes. It would suffice to eliminate all the muxes and to complete the distributed duplication of  $T_s$ , only if this process could be performed moving top-down only. But the duplication rules work independently from such an orientation. Apart for the free

variables of  $T_s$ , after a few duplication steps, the residual shared part of  $T_s$  is a (possibly disjoint) graph delimited by a set of matching (de)muxes. The duplication may then continue by a random choice of any of such muxes, or in a concurrent setting, by the simultaneous firing of several duplication rules. Hence, proceeding with the reduction, a mux may stop duplicating nodes in two ways: reaching the binding port of a  $\lambda$  node external to  $T_s$ ; reaching the principal port of a demux. In the first case the absorption rule applies. In the second case, the two facing muxes correspond to a boundary around an empty shared subgraph. As a consequence it is sound to remove (annihilate) them directly connecting each port of the mux to the matching port of the demux.

We are done. The duplication rules plus the latter annihilation of muxes allow to perform a complete duplication of  $T_s$ . The  $\beta$  rule of Figure 1 can then be split into the execution of a sharing  $\beta$  rule, followed by the application (in any order) of a sequence of duplication or annihilation rules.

What we have get so far is however not yet particularly appealing. In fact, what we achieved is just a detailed implementation of  $\beta$ ; but there is up to now no relevant difference between an implementation based on the duplication rules and one obtained by directly coding the duplication of  $T_s$  in some programming language external to the graph rewriting system. Nevertheless, all the rules we have met, sharing  $\beta$  included, are local and can be fired independently from the shape of the graph. Hence, what does it happen if we freely execute them? Namely, what does it happen if we execute another sharing  $\beta$  rule, either internal or external to  $T_s$ , before that the duplication rules have accomplished their task of to completely duplicate  $T_s$ ?

The first consequence is that the muxes around the graph are no more part of the same duplication process. Therefore, when two of them face it is no more sure that they are matching and that they can annihilate. Some thoughts allow to realize that, when a pair of non matching muxes faces, the correct rewriting is rather the swap rule of Figure 4.

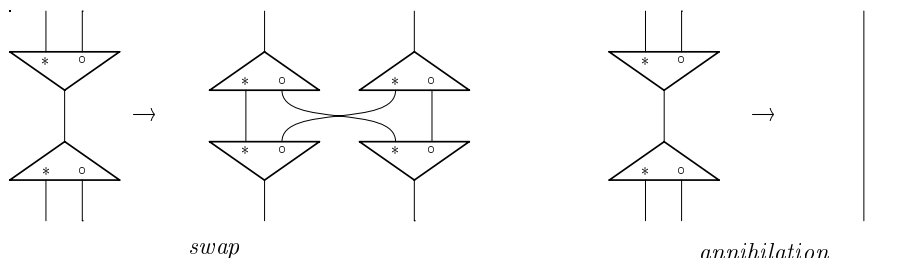


Fig. 4. Mux interactions

This is the second and more important bell ring. It warns us that we need a way to talk about the dependencies between the nodes of a graph due to their position in it, or to their origin. In fact, the rewriting rules we have got contain at least two kinds of rule that deserve to know such an information: the mux



interactions of Figure 4 and the absorption rule. In the mux interactions, such an information should enable us to recognize when two facing muxes have to be considered matching—and the annihilation should then be applied—and when such muxes are not matching—and the swap rule should be applied instead.

The first attempt would be to mark each mux with a label corresponding to the redex that created it. Unfortunately, Lamping (who firstly introduced sharing graphs, see [Lam90]) showed that this solution does not work, as in certain cases, even two muxes that have been created by the same redex have to be considered non matching. The right solution is to have a dynamical labeling of muxes, say an index, ensuring that the annihilation rule applies when the muxes have the same index, and that the swap rule applies otherwise. This introduces the next relevant ingredient for the understanding of sharing graphs, the so-called boxes.

## 1.2 Boxes

To generalize our analysis, let us try to identify the elements characterizing the  $\beta$  rule. The rule take a subgraph and after its duplication connects its new instances to some edges of the reducing graph. In the  $\lambda$ -calculus, the subgraphs that can be involved in this process are clearly recognizable: they are the subtrees rooted at the right edge of an @ node. More in general, when the graphs under analysis are not trees, we might rather have a frame, say a *box*, surrounding the subgraphs which might be argument of a  $\beta$ -like reduction rule. The  $\beta$  rule could then be depicted as a rewriting in which a box is duplicated and displaced somewhere in the graph. By the way, this point of view is sound, as far as boxes can be properly rearranged after the execution of a  $\beta$  rule.

A natural way to ensure this is asking that the only ways in which boxes overlap are the trivial ones, that is, to ask that two boxes are either disjoint or enclosed one into the other. In the  $\lambda$ -calculus this is definitely true, as it is readily seen that, replacing a term for a variable occurrence, the box of the replacing term is enclosed by the boxes that previously surrounded the replaced variable occurrence. More generally, we may say that the boxes of a graph must properly nest, say that the graph must have a *box nesting property*, and that the  $\beta$  rule must preserve such a box nesting property.

In the case of  $\lambda$ -calculus, the box nesting property is equivalent to say that  $\lambda$ -calculus is an orthogonal rewriting system. Therefore, to require that the graph rewriting systems we want to study have the box nesting property means to require that an analog orthogonality property hold for them.

The box nesting property gives us the tools to avoid the introduction of a global constructor for the representation of box frames. In fact, assuming that each node/edge is labeled by the number of boxes surrounding it, say the *box nesting level* of the node/edge, we see that each node with two edges whose levels are respectively  $n$  and  $n + k$  (with  $k > 0$ ) is the border of  $k$  boxes, and that each of them can be recovered finding the maximal connected subgraph containing the nodes whose edges are at a level greater or equal than  $n + i$ , for  $i = 1, 2, \dots, k$ .

These simple thoughts just give us a solution to the troubles with the application of the absorption rule. In fact, it is readily seen that a  $\lambda$  node external to  $T_s$  may bound a variable internal to  $T_s$  only if its level is lower than the level of the root of  $T_s$ . Furthermore, any node internal to  $T_s$  has a level greater or equal than the root of  $T_s$ . Hence, let us assume to label the mux inserted by the sharing  $\beta$  rule with an index equal to the level  $n$  of the  $\lambda/@$  pair reduced by the rule. Since a box frames  $T_s$  (see Figure 1), its root is at level  $n + 1$ . The index of the mux can then be seen as a sort of discriminant, say a *threshold*, that is capable to recognize when to stop the duplication. Namely, a mux duplicates all the nodes at a level greater or equal than its threshold  $m$ ; if during this process the mux reaches a  $\lambda$  node with a level  $n \leq m$ , the absorption rule applies and the mux is absorbed by the  $\lambda$  node.

The use of levels opens however another problem: how to correctly reassign a level to each node/edge of a new instance of  $T_s$ . According to the shape of the boxes of a  $\lambda$ -term, the difference  $k$  between the level  $n + k$  of the occurrence of a variable and the level  $n$  of the  $\lambda$  node binding it is the number of boxes in which the corresponding instance of  $T_s$  must be enclosed. Since firing the  $\beta$  rule the box around  $T_s$  disappears, it means that each node/edge of such a new instance of  $T_s$  must be increased by  $k - 1$ . It is worth to note that since  $k$  might be equal to 0—in the case that the variable occurrence does not close any box—a  $\beta$  rule might also imply the decreasing by 1 of all the levels in  $T_s$ .

It is not difficult to see how to perform such a reindexing during the duplication of  $T_s$ . Each auxiliary port of a mux should bring an offset by which to increase the level of the instance of the node duplicated by it.

Our duplication rules of Figure 3 should then be completed by inserting thresholds and levels. We omit to redraw them at this point, for we will detailedly discuss such a rewriting system (see Figure 20) in section 8. We only remark that the idea that a mux with threshold  $m_1$  lifts the levels above  $m_1$  apply also in the swap rule. In fact, assume that the thresholds of the two muxes are  $m_1, m_2$  and that  $m_1 < m_2$ . Each new instance of the mux with threshold  $m_2$  is lifted by the offset  $q$  of the corresponding port of the mux with threshold  $m_1$  (see Figure 20).

### 1.3 Soundness

The soundness of the approach based on levels is easily provable for the case in which a sharing  $\beta$  rule is followed by a whole duplication of  $\mathbb{T}_s$ . In this case muxes never change their thresholds and the only case of mux interaction that can arise is the annihilation rule. When sharing  $\beta$  and duplications are interleaved (hereafter, when not otherwise specified, we will include mux interactions into duplications), things become instead greatly involved. Muxes thresholds can arbitrarily change and it is not difficult to build a case in which two muxes that was created by the same redex meet with different thresholds.

The core of the paper is devoted to prove soundness in the general case of a rewriting system whose graphs have the box nesting property, and whose rewriting rule is the analog of the  $\beta$  rule of  $\lambda$ -calculus. We will prove that for any *sharing graph*—intuitively a graph obtained by applying a sequence of sharing  $\beta$  and duplication rules—there is a corresponding graph without muxes, write  $G \mapsto \mathcal{R}(G)$  and say  $\mathcal{R}(G)$  is the *read-back* of  $G$ , s.t. sharing  $\beta$  and duplication rules are sound w.r.t. such a read-back. Namely, we will prove that the following diagrams commute:

$$\begin{array}{ccc}
 G & \xrightarrow{\beta_s} & G' \\
 \downarrow & & \downarrow \\
 \mathcal{R}(G) & \xrightarrow{\beta^\dagger} & \mathcal{R}(G')
 \end{array}
 \qquad
 \begin{array}{ccc}
 G & \xrightarrow{\pi} & G' \\
 \downarrow & & \downarrow \\
 \mathcal{R}(G) & = & \mathcal{R}(G')
 \end{array}
 \tag{1}$$

where  $\beta_s$  denotes sharing  $\beta$ , and  $\pi$  denotes duplication rule.

According to the diagram on the left, we see that each  $\beta_s$  correspond to a (finite) non empty sequence of  $\beta$  reductions: the execution of a  $\beta$  inside a shared part implies the simultaneous reduction of all the instances of that redex.

The diagram on the right-hand side expresses instead that the read-back of a sharing graph is invariant under  $\pi$ . Furthermore, strong normalization and confluence—that are easily provable in the case  $\beta_s$  followed by a maximal sequence of  $\pi$ —still hold in the general case. Hence, the syntactical read-back we could define via  $\pi$ , *i.e.*, interpreting a sharing graph  $G$  as a shared representation of its  $\pi$  normal form  $\mathbf{NF}_\pi(G)$ , coincides with the semantical

one:

$$\begin{array}{ccc}
 & \text{NF}_\pi(\mathbf{G}) & \\
 & \nearrow^* \pi & \\
 \mathbf{G} & & \parallel \\
 & \searrow & \\
 & \mathcal{R}(\mathbf{G}) & 
 \end{array} \tag{2}$$

The proof of the previous results requires a careful study of the unfoldings of a sharing graph. In fact, the definition of the map  $\mathcal{R}$  splits in two steps: firstly, the individuation of a proper unfolding  $\mathbf{U}$  of  $\mathbf{G}$ , write  $\mathbf{U} \ll \mathbf{G}$ , in which all the muxes are unary; then, the definition of  $\mathcal{R}$  for such a graph  $\mathbf{U}$ . Assuming the uniqueness of the unfolding  $\mathbf{U}$ , say the *least-shared-instance* of  $\mathbf{G}$ , the read-back can then be defined in the general case taking  $\mathcal{R}(\mathbf{G}) = \mathcal{R}(\mathbf{U})$ .

Intuitively, the least-shared-instance  $\mathbf{U}$  of  $\mathbf{G}$  is a representation of  $\mathcal{R}(\mathbf{G})$  in which the reindexing of the nodes has not yet been accomplished. At the same time the unary muxes of  $\mathbf{U}$  mark the border of a sharable subgraph, for the image in  $\mathbf{G}$  of each of them is a mux. Nevertheless, to associate the correct graph  $\mathcal{R}(\mathbf{U})$  to the unshared graph  $\mathbf{U}$  (a graph is *unshared* when all its muxes are unary) is not an easy task. Apart for levels,  $\mathcal{R}(\mathbf{U})$  is obtained from  $\mathbf{U}$  just replacing its unary muxes by direct connections. But to reassign levels is not direct in the general case, despite its simplicity for  $\lambda$ -calculus graphs, in which the tree shape forces the existence of a unique level assignment.

Also the definition of least-shared-instance is not direct, as we cannot simply take an unshared graph for which there is a graph morphism  $M : \mathbf{U} \rightarrow \mathbf{G}$ . Because of the matching of muxes, not all the paths are admissible unfolding  $\mathbf{G}$ , and thus not all the  $\mathbf{U}$  definable via graph morphisms are correct.

Most of the paper is devoted to define the correct unfolding partial order  $\ll$ , by which to obtain the least-shared-instance of a sharing graph  $\mathbf{G}$  as the minimal element s.t.  $\mathbf{U} \ll \mathbf{G}$ . In order to do this we have to develop an algebraic semantics of sharing graphs by which to characterize the shape of a proper unfolding morphism  $M : \mathbf{U} \ll \mathbf{G}$ . In this way, we will get an algebraic characterization of the proper unshared graphs for which there is a shared representation, and at the same time this will give us a characterization of the (proper) sharing graphs as the ones for which  $\mathbf{U} \ll \mathbf{G}$  is defined.

The read-back of proper unshared graphs is an immediate consequence of their algebraic semantics. Furthermore, such a semantics will also allow to prove that diagram (1) and diagram (2) hold when  $\mathbf{G}$  and  $\mathbf{G}'$  are proper unshared structures, provided that  $\beta_s$  is replaced by an unshared version of the  $\beta$  rule, say  $\beta_u$  rule, positioned midway between  $\beta$  and  $\beta_s$ —in a  $\beta_u$  rule the duplication of a box is still executed globally, the reindexing is instead demanded to

some unary muxes inserted at the principal doors of the duplicated boxes (the principal door of a box is the generalization of the root of  $\mathbb{T}_s$ ). Such a decomposition of  $\beta$  reflects in a decomposition of the proof of soundness. In fact, given a sharing graph  $G$ , we will prove that any  $\beta_s$  or  $\pi$  reduction  $G \rightarrow G'$  can be simulated on its least-shared-instance  $U$  obtaining as result the least-shared-instance of  $G'$ . Therefore, diagram (1) is just diagram chasing of the previous results:

$$\begin{array}{ccc}
 G & \xrightarrow{\beta_s} & G' \\
 \Upsilon & & \Upsilon \\
 U & \xrightarrow{\beta_u^\dagger} & U' \\
 \downarrow & & \downarrow \\
 \mathcal{R}(G) & \xrightarrow{\beta^\dagger} & \mathcal{R}(G')
 \end{array}
 \qquad
 \begin{array}{ccc}
 G & \xrightarrow{\pi} & G' \\
 \Upsilon & & \Upsilon \\
 U & \xrightarrow{\pi^\dagger} & U' \\
 \downarrow & & \downarrow \\
 \mathcal{R}(G) & = & \mathcal{R}(G')
 \end{array}
 \tag{3}$$

Strong normalization, confluence, uniqueness of the  $\pi$  normal form, and diagram (2) can then be lifted to sharing graphs exploiting the upper part of diagram (3).

All these results will be proved under the only assumption that the graph rewriting system has the box nesting property. Furthermore, we will see that the algebraic semantics is indeed an abstract characterization of that property, since we will see that the proper sharing graphs defined by it are the ones which  $\pi$  normalize to a graph without muxes for which the box nesting property holds.

#### 1.4 Optimality and other related works

Sharing graphs were introduced by Lamping [Lam90] to implement Lévy's optimal reductions of  $\lambda$ -terms [Lév80]. Several refinements of them were successively proposed by Gonthier et al. [GAL92a,GAL92b], and by Asperti and Laneve [AL94,Asp95]. The work of Gonthier et al. addressed how Lamping's formalism could be interpreted inside the so-called Geometry of Interaction (GOI) of Girard [Gir89]; Asperti presented a more categorical justification of the technique; Asperti and Laneve gave a generalization of the methodology to the so called Interaction Systems, the subclass of the Combinatory Reduction Systems for which it is possible to find a Curry-Howard analogy with a suitable intuitionistic logic. Furthermore, Asperti used sharing graphs to implement an optimal version of an ML-like functional language [AGN95].

The main concern of all these studies was the implementation of optimal

reductions. Hence, the set of rules that they proposed was the minimal one useful for such a result. Here, we revert instead the point of view. The main concern is to get a distributed and local implementation of  $\beta$ -like rules.

The general framework in which our results will be achieved will not allow to connect our sharing graphs to optimality in the usual way based on redex families or labels. Nevertheless, in the case in which this makes sense, optimal implementations are obtained just assuming that the duplication rules are applied following a lazy reduction strategy.

Let us be more precise showing how this applies in the example developed so far. To execute the rules of Figure 3 following a lazy strategy means that a mux duplicates a node only when the presence of the mux might hide a  $\beta$  redex. For instance, a mux whose principal port is connected to the left port of an  $@$  node might hide a redex, for one of its auxiliary port might be (or become) connected to a  $\lambda$ . According to this, the only two duplication rules unavoidable in such a lazy strategy are  $@$ -left and  $\lambda$ -up. Further, dropping the other rules, not only has no impact on the computational power, it also improves efficiency, since in this way no  $\beta$  redex is unnecessarily duplicated. For instance, let us assume that there be a mux above the  $@$  node of a  $\beta$  redex. The application of an  $@$ -up should be followed by a  $\lambda$ -up. But this would create an instance of the redex for each auxiliary port of the mux.

According to the interpretation of muxes in terms of fans and brackets (see section 4.1), such a lazy system is exactly the optimal one.

One of the main consequences of the previously described change of perspective is that the proof technique is completely different from the usual ones based on a direct interpretation of sharing graphs into GOI. For instance, the proof technique of Gonthier et al. rests on the fact that the optimal rules define an interaction system (for the definition of interaction system see [Laf90]) and that this kind of rules are sound w.r.t. the paths definable using GOI. In fact, the paths of GOI give a way to extract the normal form of a proof net without reducing it or, in the case of  $\lambda$ -calculus where a term might not have a normal form, to extract its weak head-normal-form. Hence, any rule sound w.r.t. this set of paths can be safely applied, as it does not change the denotation of the graph. It might seem that such an approach make use of the weakest conditions necessary for a proof of soundness, and then we would expect that it would be able to prove the widest set of sound rules. On the contrary, apart for  $@$ -left and  $\lambda$ -up, the duplication rules are hardly provable following it. The point is that it does not exploit the knowledge about the shape of a sharing graph that we can infer knowing that it is the result of a sharing reduction.

Besides, even our algebraic semantics characterizes proper sharing graphs without assuming that they are the result of a reduction. Nevertheless, it

exploits the methods of GOI to find the paths that define the read-back of a sharing graph (This point is not explicit in the paper. It is indeed implicit in the definition of least-shared-instance we will give. For the case of  $\lambda$ -calculus, a more direct presentation based on paths can be found in [Gue97].) The proper sharing graphs defined in this way contain the ones obtainable as a result of a sharing reduction and maybe it could be also proved that the two classes coincide.

The result of such an approach is the integration between some algebraic techniques in the style of GOI and a more traditional proof technique exploiting properties of the duplication rules as confluence and strong normalization. We believe that such an integration is one of the principal novelty of the paper.

Another relevant point of the solution we propose is that it represents a sort of abstract *sharing graph machine*. Differing from optimal implementations, in such a sharing graph machine we do not need any external machinery to read-back the result of a computation. The duplication rules internalize the read-back into the system, and at the same time, a lazy application of them allows to compute via optimal reductions.

Finally, the point of view of the paper also has a good proof theoretic motivation. It fits into a well-known approach in which dependencies between the formulas of a proof (net) are represented by means of indexes. For a detailed discussion of these connections we refer the reader to [GMM97a].

### 1.5 Overview of the paper

The body of the paper starts with a formal definition of the structures we will study (section 2). The main difference w.r.t. what done so far is that we will present sharing graphs as hypergraphs. Hence, what was a node up to now will become a hyperedge, say a link, and what was an edge will become a vertex. Furthermore, we will introduce two special kinds of link: box door links, to delimit the border of boxes; contraction links to merge vertices. We will define levels of links and vertices, and we will define boxes in terms of levels.

In section 3 we will show the relevant calculi fitting in our structures of links.

The set of links used to build sharing graphs will be completed in section 4, after the introduction of muxes. There, we will also give the equivalence between muxes and Gonthier's fan and brackets.

In section 5 we will define sharing morphisms, the basic tool by which to unfold a sharing graph. We will also address why the unfolding problem is so difficult and requires the introduction of the algebraic semantics that we will

give in section 7.

The following part, starting from section 7, is the technical core of the paper. A summary of the results achieved in it is contained in section 6.

## 2 Levelled structures

We take the point of view that nets (the graphs of the calculi to be implemented) are hypergraphs. A levelled net is then a set of named hyperarcs, the so-called *links*, connected by vertices that we will call *arrows*. To both, links and arrows, we associate a *level*—despite we will later see that some levels are indeed redundant. The name of a link, say its *type*, fixes its cardinality and gives the constraints to which the link level and the levels of its incident arrows must accord.

### 2.1 Hypergraphs

Let  $V = \{v_1, v_2, \dots\}$  be a denumerable set of primitive objects called *vertices*. A directed hyperedge, or *hyperarc*, is an ordered pair  $e = (e_t, e_h)$  of (possibly empty) disjoint sequences of vertices. The first element ( $e_t$ ) of the pair is the *tail* of  $e$ , the second one ( $e_h$ ) the *head* of  $e$ . Since we will usually draw hyperarcs according to the top-down orientation—the tail of a hyperarc above its head—we will also say that any vertex  $v_t$  in the tail of a hyperarc  $e$  is above it; that any vertex  $v_h$  in the head of  $e$  is below it; and conversely, that  $e$  is above  $v_h$  and that  $e$  is below  $v_t$ .

A *hypergraph* is a pair  $G = (V, E)$ , where  $V$  is a denumerable set of vertices and  $E$  is a denumerable set of hyperarcs whose vertices range over  $V$ .

A vertex  $v$  of the hypergraph  $G$  is an *arrow* if there is at least a hyperarc above or below it, and there is no pair of links  $e'$  and  $e''$  s.t.  $v$  is either below both  $e'$  and  $e''$ , or above both  $e'$  and  $e''$ . In other words, assuming to say that  $\emptyset$  is below (above)  $v$  when there is no hyperarc below (above)  $v$ , an arrow is determined by a pair  $(e_a, e_b) \in (E \uplus \{\emptyset\})^2 \setminus \{(\emptyset, \emptyset)\}$  s.t.  $e_a$  is above  $v$  and  $e_b$  is below  $v$ . When  $\emptyset \in \{e_a, e_b\}$  the arrow  $v$  is called a *root arrow*. In particular, when  $e_a = \emptyset$ , the root  $v$  is said a *source arrow*; otherwise, when  $e_b = \emptyset$ , it is said a *target arrow*.

**Remark 1** Let  $G$  be a hypergraph whose vertices are arrows. Its dual  $G^*$  is a directed graph, as all its edges—the arrows of  $G$ —have only one source and one target node. This explains why we call arrows the vertices of our hypergraphs



and why, in drawing  $G$ , a vertex will be represented by an “arrow” from the link above to the link below it.

An (undirected) path of  $G$  is an alternated sequence  $\phi = v_0 e_1 v_1 \dots e_k v_k$  of vertices  $v_i$  and hyperarcs  $e_i$ , s.t.: (i)  $e_i \neq e_{i+1}$ ; (ii)  $v_{i-1}$  and  $v_i$  are distinct doors of  $e_i$ . According to this definition, a sequence  $\phi$  is a path of  $G$  iff it is an undirected path (a sequence of edges) of  $G^*$ . Therefore, we will say that  $G$  is connected and acyclic when  $G^*$  is.

## 2.2 Structures of links

**Definition 2 (link)** A link is a hyperarc  $e$  with:

- (i) a name, the type of  $e$ ;
- (ii) a set of named input ports, one for each arrow above  $e$ ;
- (iii) a set of named output ports, one for each arrow below  $e$ .

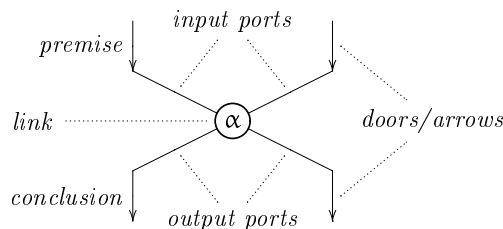


Fig. 5. A link of type  $\alpha$  with two input ports and two output ports.

The arrows above a link  $e$  are the *premises* of  $e$ ; the arrows below  $e$  are the *conclusions* of  $e$ ; both premises and conclusions are the *doors* of  $e$ . (See Figure 5 for the graphical representation of links.) A link with no premises is a *source link*. A link with no conclusions is a *target link*. Isolated links with no doors are not allowed.

**Definition 3 (structure of links)** A structure (of links)  $G$  over the signature  $\Sigma$  (a set of link names) is a hypergraph in which:

- (i) all the vertices of  $G$  are arrows;
- (ii) there is at least a (source or target) root arrow;
- (iii) the names of the links range over  $\Sigma$ .

The source arrows of a structure  $G$  are the *premises* of  $G$ ; the target arrows are the *conclusions* of  $G$ . Since a structure does not contain isolated vertices it is immediate that no vertex can be both premise and conclusion of  $G$ . Premises and conclusions are the *doors* of  $G$  (and  $\text{doors}(G)$  denotes their set). The links above a conclusion of  $G$  or below a premise are the *door links* of  $G$  (and  $\text{dlinks}(G)$  denotes their set).

### 2.2.1 Substructures

Given a structure  $G$  over  $\Sigma$ , we denote by  $\text{lnk}(G)$  the set of its links and by  $\text{vtx}(G)$  the set of its arrows. Furthermore, if  $\alpha \in \Sigma$ , we denote by  $\text{lnk}_\alpha(G)$  the set of the links of  $G$  of type  $\alpha$ .

Let  $G$  be a structure. A substructure  $R$  of  $G$ , or *G-substructure*, is a structure s.t.  $\text{vtx}(R) \subseteq \text{vtx}(G)$  and  $\text{lnk}(R) \subseteq \text{lnk}(G)$ . Since a structure does not contain isolated vertices, it is readily seen that the  $G$ -substructures coincide with the parts of  $\text{lnk}(G)$ . In fact, not only any  $G$ -substructure  $R$  is uniquely determined by the set  $\text{lnk}(R) \subseteq \text{lnk}(G)$  but, given a set of links  $E \subseteq \text{lnk}(G)$ , there exists a (unique)  $G$ -substructure s.t.  $\text{lnk}(R) = E$ . The standard inclusion relation and set operations apply to  $G$ -substructures according to their interpretation as set of links. Further,  $\mathcal{P}(G)$  will denote the set of the  $G$ -substructures.

### 2.3 Boxes

To implement boxes we use two reserved link types called *box door links*: the  $!$  (of-course) or *principal door link*; the  $?$  (why-not) or *auxiliary door link*. Both these links have two doors: the *external* and the *internal* door (denoted by  $\text{edoor}(e)$  and  $\text{idoor}(e)$ , respectively). For the reader acquainted with linear logic proof-nets, the reasons of the names  $?$  and  $!$  are self-evident, even if at this level of abstraction we could have chosen any other pair of names.

**Remark 4** According to Figure 6, we will generally assume that the external door of a box door link is a conclusion of the link and that the internal one is a premise. Such an orientation is however not mandatory, for instance, in the case of the  $\lambda$ -calculus we will swap the arrows of the  $!$  links in order to preserve the natural orientation of  $\lambda$ -terms.

**Definition 5 (box)** *Let  $G$  be a structure of link. A box of  $G$  is a connected  $G$ -substructure  $B$  s.t.:*

- (i) *there is a unique arrow  $\text{pdoor}(B) \in \text{doors}(B)$ —the principal door of  $B$ —which is external door of an  $!$  link  $\text{pdlink}(B)$ ;*
- (ii) *all the arrows  $\text{adoors}(B) = \text{doors}(B) \setminus \{\text{pdoor}(B)\}$ —the auxiliary doors of  $B$ —are external doors of  $?$  links.*

According to the previous definition, a box contains its door links. This choice is just a matter of taste. For instance, as a consequence of the merging of  $?$  and contraction links, in [GMM97a] we preferred to not include box door links into the corresponding boxes.

**Remark 6** The relevant point of Definition 5 is the *connectedness* of boxes.

The reader acquainted with linear logic should have already noted that this means to forbid weakenings, for weakening links might split boxes in several disjoint components. A solution to this problem has been proposed in [GMM97b], where, by a small modification of the underlying sequent calculus, weakening links are connected to axioms. We will come back to this issue discussing garbage collection (section 11.4).

### 2.3.1 Box nesting property

The only way in which boxes may overlap is the trivial one. Namely, two boxes are either disjoint or enclosed one into the other. Further, in the second case they cannot have the same principal door link.

**Definition 7 (box nesting property)** *Let  $\text{box}_G : \text{lnk}_!(G) \rightarrow \mathcal{P}(G)$  be an assignment of boxes to the  $!$  links of the structure  $G$ . We say that  $G$  with the boxes  $\text{BX}_G = \{\text{box}_G(e) \mid e \in \text{lnk}_!(G)\}$  has the box nesting property, when  $B_1 \cap B_2 \neq \emptyset$  implies either  $B_1 \subset B_2$  or  $B_2 \subset B_1$ , for any  $B_1, B_2 \in \text{BX}_G$ .*

As an immediate consequence of this definition, we see that:

- (i) If  $\text{pdlink}(B_1) = \text{pdlink}(B_2)$ , then  $B_1 = B_2$ , for any  $B_1, B_2 \in \text{BX}_G$ .
- (ii) The box nesting property does not forbid  $\text{doors}(B_1) \cap \text{doors}(B_2) \neq \emptyset$ . In fact, even though two boxes always have distinct principal doors, they might share some auxiliary door links.

### 2.3.2 Box nesting level

The box nesting property gives us a way to avoid the introduction of any global link for the representation of boxes. The technique rests on the assignment of a level to each link/arrow corresponding to the number of boxes enclosing it. According to this intended interpretation, the levels of the arrows incident to a box door link are the ones given in Figure 6.

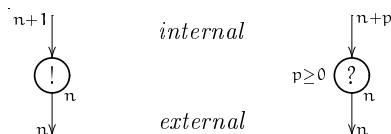


Fig. 6. Exponential links.

In an  $!$  link we always have a difference of 1 between the internal and the external door—for an  $!$  link is always principal door of just one box. Such a difference is rather equal to  $p \geq 0$  in a  $?$  link—for a  $?$  link may be auxiliary door of several or even 0 boxes. Note that the levels of the links are an harmless exception to our intended interpretation. In fact, even if box door links belong

to the boxes of which they are door links, the level of an ! or a ? link is equal to the level of its external door.

In the case of any other link of the signature, the level of the link and the levels of its doors coincide (see Figure 7).

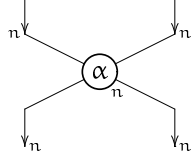


Fig. 7. Levels of a link.

**Definition 8 (leveled structure)** An  $\ell$ -structure  $G$  over the signature  $\Sigma \uplus \{!, ?\}$  is a structure of links with a level assignment  $\ell_G : (\text{vtx}(G) \cup \text{lnk}(G)) \rightarrow \mathbb{N}$  which associates a non-negative level to each vertex and each link of  $G$  s.t.: (i)  $\ell_G(v) = 0$  for any  $v \in \text{doors}(G)$ ; (ii) levels accord with the constraints of Figure 6, for the box door links, and of Figure 7, for the links of type  $\alpha \in \Sigma$ .

**Remark 9** We stress that all the conclusions of an  $\ell$ -structure have level 0.

**Remark 10** To label both arrows and links with a level is actually redundant: we might get the same result assigning levels to arrows only, or to links only (in the latter case, provided that levels strictly represent box nesting depths). In fact, links' levels might be recovered from arrows' levels, and vice versa. Both such choices correspond to solutions presented in literature. To label links is more faithful to the original presentation of optimal reduction algorithms (see [Lam90,GAL92a,GAL92b,AL94,Asp95]). To label arrows has instead a more tight correspondence with the logical interpretation of nets (see [MM95,GMM97a]).

## 2.4 Leveled boxes

Levels can be used to avoid the introduction on an explicit box constructor.

**Definition 11 (leveled box)** Let  $G$  be an  $\ell$ -structure and let  $e_! \in \text{lnk}_!(G)$ . The  $\ell$ -box of  $e_!$  is the  $G$ -substructure  $\text{box}_G^\ell(e_!)$  s.t., being  $B = \text{box}_G^\ell(e_!)$ :

- (i)  $\text{pdlink}(B) = e_!$ , i.e.,  $e_! \in \text{dlinks}(B)$ ;
- (ii)  $e_! \in \text{lnk}_?(G)$ , for any  $e \in \text{dlinks}(B)$ ;
- (iii)  $B$  is connected;
- (iv)  $\ell_G(v) \leq \ell_G(e_!)$ , for any  $v \in \text{doors}(B)$ ;
- (v)  $\ell_G(v) > \ell_G(e_!)$ , for any arrows  $v \in \text{vtx}(B) \setminus \text{doors}(B)$ .

Note that we explicitly exploit connectedness of boxes.

According with previous notations,  $\text{BX}_G^\ell$  is the set of boxes defined by the function  $\text{box}_G^\ell$ , and “the  $\ell$ -structure  $G$  has the box nesting property” means that  $G$  with the boxes  $\text{BX}_G^\ell$  has the box nesting property.

**Fact 12** *Let  $\text{BX}_G$  be the boxes of a structure of links  $G$ . Let  $\ell_G$  be the map assigning to each arrow/link of  $G$  its box nesting level. If  $G$  has the box nesting property, then  $\text{BX}_G = \text{BX}_G^\ell$ .*

**Remark 13** The converse of the previous fact is not true. In fact, we may easily construct an  $\ell$ -structure for which the box nesting property does not hold. Furthermore, even in the case that the  $\ell$ -structure  $G$  have the box nesting property, its levels might differ from the ones induced by the box nesting. The latter case has however no impact on our study, Independently from the origin of the levels, it suffices that the corresponding  $\ell$ -structure has the box nesting property. One of the key step of our theory will be the introduction of an algebraic semantics (see section 7) characterizing the  $\ell$ -structures for which the box nesting property holds (see Proposition 51).

## 2.5 Contraction

To complete the assumptions on the calculi we are interested in, we assume that they contain a contraction operator. Namely, a *contraction* link—being  $\curlyvee$  its name—with  $k + 1$  doors: the  $k > 0$  *contracting doors* and one *contracted door*. All the contracting doors of a  $\curlyvee$  link  $e$  are external doors of  $?$  links (see Figure 8), and are connected to ports of  $e$  having the same name (in other words the contracting ports are indistinguishable). In the following, we will use  $\text{adoors}(e)$  to denote the set of the contracting (auxiliary) doors of a  $\curlyvee$  link  $e$  and  $\text{pdoor}(e)$  to denote its contracted (principal) door.

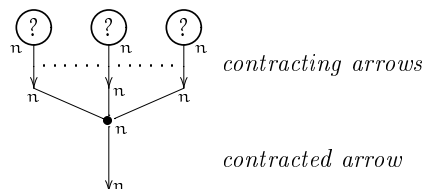


Fig. 8. Contraction link.

Because of the previous assumptions, the orientation of  $\curlyvee$  links must accord with the orientation of  $?$  links—therefore, as in the following we will only meet  $?$  links whose external door is their conclusion, the orientation of  $\curlyvee$  links will correspond to the one in Figure 8.

**Remark 14** We admit the presence of unary contraction links. Their intended interpretation is a direct connection between their doors. In spite of this, for technical reasons, we add the assumption that a  $?$  link is always

followed by a  $\vee$  link, which implies to insert a dummy unary contraction link below some  $?$  links (this allows a more directed definition of sharing morphism, Definition 17). A different possibility would have been to merge  $?$  and  $\vee$  link (as for instance has been done in [GMM97a]).

### 3 Examples

The next are the relevant examples to which our methodology applies. They are indeed the ones for which sharing graphs have been introduced. Their description is not complete, for we just want to point out their box rewriting rules. For each of them we give anyhow a pointer to an unabridged presentation.

#### 3.1 $\lambda$ -calculus

The use of box door links to explicitly delimit the border of boxes implies that  $\lambda\ell$ -structures slightly differ from the graphs used in the introduction—apart for the fact that what was a node in one of such graphs becomes a link in a  $\lambda\ell$ -structure.

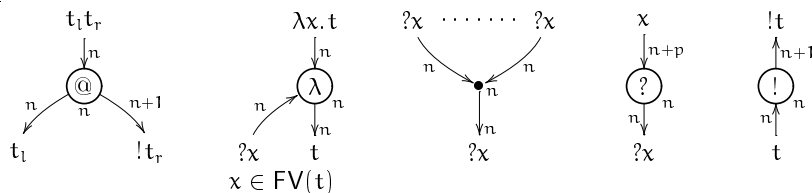


Fig. 9.  $\lambda\ell$ -terms.

Figure 9 gives the links used to build  $\lambda\ell$ -structures. The labels at the vertices of the arrows define the formation rules by which to build the correct  $\lambda\ell$ -structures, say  $\lambda\ell$ -nets. The introduction of box door links and contraction also cause a small change in the  $\beta$  rule. In fact, the  $\beta$  rule does not involve any more just a pair  $@/\lambda$ , but also the  $!$  link connected to the right port of the  $@$  link, and the contraction and the  $?$  links connected to the binding port of the  $\lambda$  link. Taking into account these considerations, the reformulation of  $\beta$  is direct, and because of this we omit to draw it.

#### 3.2 MELL

The links for the multiplicative-exponential fragment of linear logic (MELL) are drawn in Figure 10. Since contraction,  $?$  and  $!$  links have been borrowed

from the exponentials of **MELL**, to complete the system it suffices to add multiplicative and identity links. Because of the restrictions we have imposed in the definition of box, weakening cannot be directly introduced (for a treatment of weakening see [GMM97a]). Hence, our **MELL** is without weakening.

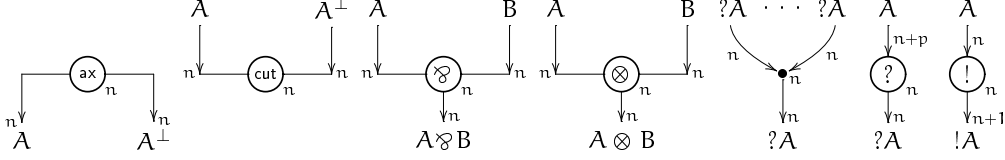


Fig. 10. **MELL** proof  $\ell$ -nets

The correct **MELL**  $\ell$ -structures, or **MELL** proof  $\ell$ -nets, are the ones obtainable by the step-by-step translation of a **MELL** proof, or by checking that a structure built according to the labeling given in Figure 10 satisfies a given correctness criterion (see [Gir87]), for instance the Danos and Regnier’s one.

The rewriting rules are the usual ones, see [Gir87] for their complete set. The only one relevant for our purposes is however the exponential cut-elimination that will be depicted in Figure 22.

### 3.3 Pure proof nets

Pure proof nets (see [Reg92]) are the nets corresponding to the interpretation of  $\lambda$ -calculus inside linear logic given by the isomorphism  $!O \multimap O \simeq O$ ; that using  $\wp$  instead of  $\multimap$  becomes  $?I \wp O \simeq O$ , where  $I$  and  $O$  are two constants s.t.  $I = O^\perp$  and  $O = I^\perp$ . The links are then the same of **MELL**, while formulas differ. There are only four types of formulas:  $I, ?I$ , say inputs; and  $O, !O$ , say outputs. Any input formula is the dual of the corresponding output formula, *i.e.*,  $I = O^\perp$ ,  $?I = (!O)^\perp$ , and  $X^{\perp\perp} = X$ , for any  $X$ . The rules for link composition are given in Figure 11.

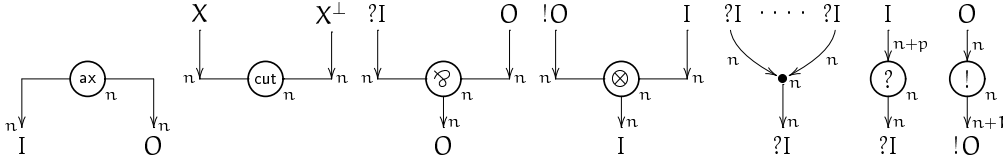


Fig. 11. Pure proof  $\ell$ -nets

It is immediate to see that apart for the identity links, and for name and orientation of the other links, there is a direct embedding of  $\lambda\ell$ -nets into pure proof  $\ell$ -nets: replace each  $\lambda$  link with a  $\wp$  link and each  $@$  link with a  $\otimes$  link, then, insert a suitable cut or ax link in any place where the orientation of the arrows would otherwise be inconsistent. The set of the pure proof  $\ell$ -nets is however much wider than the one obtainable translating  $\lambda\ell$ -nets and is defined by the same correctness criteria used to define **MELL** proof  $\ell$ -nets

(see [Reg92]). Nevertheless, not any pure proof  $\ell$ -net can be interpreted as a MELL proof  $\ell$ -net—for instance, for the nets obtained by translating  $\lambda\ell$ -nets, the pure proof  $\ell$ -net is also a MELL proof net only if the corresponding  $\lambda$ -term is typable.

The rewriting rules are the same of MELL. Hence, the relevant one for our purposes is the box interaction that will be depicted in Figure 22. The main difference between  $\lambda\ell$ -nets and pure proof  $\ell$ -nets is that the  $\beta$  rule of the first ones splits in two phases in the second ones. Firstly, an interaction between the  $\wp$  and  $\otimes$  links above the box door links of the redex, then a box interaction as the one of Figure 22. This explains why there are pure proof  $\ell$ -nets which are not image of any  $\lambda\ell$ -net—executing some  $\beta$  rules, we might have done the  $\wp/\otimes$  interactions only. Besides, assuming to complete all such  $\beta$  rules the result is the image of a  $\lambda\ell$ -term. The latter property holds in general for any pure proof  $\ell$ -net and not only for the one obtained by reducing the image of a  $\lambda\ell$ -net (see [Reg92], for more details).

## 4 Sharing structures

The link in charge of the lazy duplication of boxes is the multiplexer. As for contraction and box door links, we assume that its type  $\nabla$  is a reserved name.

**Definition 15 (multiplexer)** *A  $k$ -ary multiplexer or  $k$ -mux is a link of type  $\nabla$  with:*

- (i) *one principal port;*
- (ii) *a sequence of  $k > 0$  auxiliary ports, whose names  $\mathbf{a}_1, \dots, \mathbf{a}_k$  are chosen over a denumerable set of symbols, say  $\mathbb{N}$ , with the proviso  $\mathbf{a}_i = \mathbf{a}_j$  iff  $i = j$ ;*
- (iii) *an associate non-negative integer  $\mathbf{m}$ , the threshold of the mux;*
- (iv) *an associate sequence of  $k$  integers  $\mathbf{q}_1, \dots, \mathbf{q}_k$ , the (auxiliary port) offsets, s.t.  $\mathbf{q}_i \geq -1$  for  $i = 1, 2, \dots, k$ .*

*The level of a mux  $\mathbf{e}_\nabla$  is equal to its threshold, that is,  $\ell(\mathbf{e}_\nabla) = \mathbf{m}$ .*

In a mux  $\mathbf{e}_\nabla$  the principal door  $\mathbf{pdoor}(\mathbf{e}_\nabla)$  may be either a conclusion or a premise—in the first case  $\mathbf{e}_\nabla$  is a *positive mux*, in the second case it is a *negative mux*. The auxiliary doors  $\mathbf{adoors}(\mathbf{e})$  accord with the orientation of the principal one—in a positive mux they are premises, in a negative mux they are conclusions.

Figure 12 gives the relations between the levels assigned to the doors of a mux (and the graphical representation of a mux link). Namely:



- each offset  $q_i$  is the difference between the level of the  $i$ -th auxiliary door and the principal door;
- the threshold  $m$  is lower than the level of the principal door.

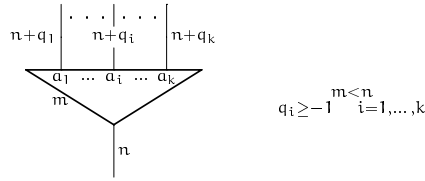


Fig. 12. The mux (multiplexer) link.

**Definition 16 (sl-structure)** A (leveled) sharing structure or sl-structure over  $\Sigma \uplus \{!, ?, \gamma\}$  is an  $\ell$ -structure over  $\Sigma \uplus \{!, ?, \gamma, \nabla\}$  in which, for any  $\alpha \in \Sigma$ , all the links of type  $\alpha$  have a fixed set of named ports.

It is worth to summarize some consequences of the previous definition. In any sl-structure:

- (i) For any  $\alpha \in \Sigma$ , all the links of type  $\alpha$  have the same cardinality.
- (ii) The port names of a link of type  $\alpha \in \Sigma$  are distinct.
- (iii) Box door links and contractions are the only links whose cardinality is not strictly fixed.
- (iv) Non unary contractions are the only links in which several distinct ports (all the contracting arrows) have the same name.

#### 4.1 Muxes, fans, and brackets

Muxes could be easily reformulated in terms of fans and brackets. Figure 13 gives the translation for the binary case. We see that a mux is a way to aggregate suitable patterns of fans and brackets. The offset  $q$  of each auxiliary port corresponds to a sequence of  $q + 1$  brackets followed by a croissant. The unique fan used in the binary case is replaced by a tree of fans with  $k$  leaves in the case of a  $k$ -mux.

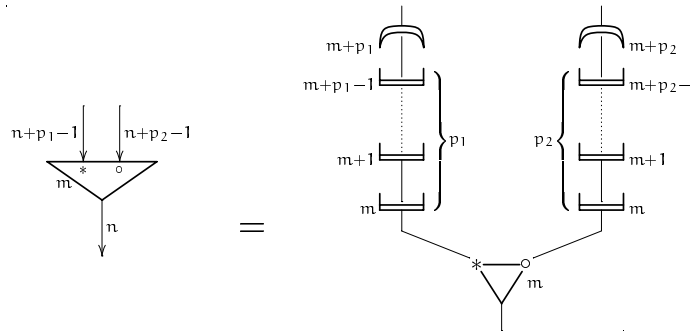


Fig. 13. Equivalence between muxes and brackets.

## 5 Unfolding the sharing

**Definition 17 (sharing morphism)** An  $s$ -morphism (sharing morphism) is a surjective homomorphism of  $sl$ -structures  $M : G_0 \rightarrow G_1$  whose restriction to  $\text{doors}(G_0)$  is injective. Namely,  $M$  is a map from  $\text{lnk}(G_0) \uplus \text{vtx}(G_0)$  to  $\text{lnk}(G_1) \uplus \text{vtx}(G_1)$  s.t.:

- (i)  $M(\text{lnk}(G_0)) = \text{lnk}(G_1)$  and  $M(\text{vtx}(G_0)) = \text{vtx}(G_1)$ ;
- (ii) the links  $e$  and  $M(e)$  are of the same type;
- (iii) if the arrow  $v$  is connected to the port with name  $a$  of the link  $e$ , then  $M(v)$  is connected to the port with name  $a$  of the link  $M(e)$ ;
- (iv)  $\ell_{G_0}(x) = \ell_{G_1}(M(x))$ , for any  $x \in \text{lnk}(G_0) \uplus \text{vtx}(G_0)$ ;
- (v) if  $v, v' \in \text{doors}(G_0)$  and  $M(v) = M(v')$ , then  $v = v'$ .

It is worth to note that an  $s$ -morphism establishes a bijection between the doors of  $G_0$  and  $G_1$ . In fact, as it maps roots to roots, a door of  $G_1$  is definitely image of a (unique) door of  $G_0$ .

Contraction and muxes are the unique links that may change cardinality via an  $s$ -morphism. Nevertheless, muxes and  $\gamma$  links present a relevant difference. In fact, while it is impossible to equate two auxiliary ports of a mux via an  $s$ -morphism, we may definitely have  $M(v) = M(v')$  for some  $\gamma$  link  $e_\gamma$  and some pair  $v, v' \in \text{adoors}(e_\gamma)$ .

Figure 14 is an example of  $s$ -morphism between two  $\lambda sl$ -structures.

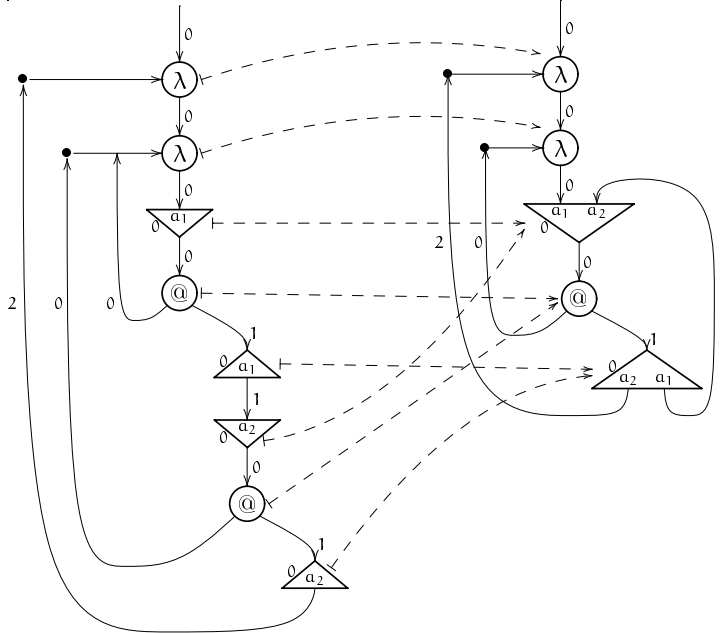


Fig. 14. Sharing morphism.

**Proposition 18** The  $sl$ -structures are a partial order w.r.t. the relation  $\preceq$

defined by “ $G \preceq G'$  when there exists an s-morphism  $M : G \rightarrow G'$ ”.

**Proof.** Reflexivity and transitivity of  $\preceq$  are immediate. So we need to show antisymmetry only. Namely, we have to prove that  $G \preceq G'$  and  $G' \preceq G$  implies  $G \simeq G'$  (i.e., they are isomorphic). Let  $M : G \rightarrow G'$  and  $M' : G' \rightarrow G$ . These two s-morphisms induce two corresponding pairs of surjective maps between  $\text{lnk}(G)$  and  $\text{lnk}(G')$ , and between  $\text{vtx}(G)$  and  $\text{vtx}(G')$ . Thus,  $|\text{lnk}(G)| = |\text{lnk}(G')|$  and  $|\text{vtx}(G)| = |\text{vtx}(G')|$ , that is, both  $M$  and  $M'$  are isomorphisms.  $\square$

### 5.1 Unshared structures

A relevant case of  $\text{s}\ell$ -structures are the ones in which there is no sharing at all, that is, in which all the muxes are unary. In our intention, they should be the minimal elements of the equivalence classes that  $\preceq$  defines.

**Definition 19 (lift)** A lift is a multiplexer with only one auxiliary port.

**Definition 20 (unshared structure)** A  $\text{u}\ell$ -structure over the signature  $\Sigma$  is an  $\text{s}\ell$ -structure over  $\Sigma$  in which all the muxes are lifts.

Unfortunately, the relation  $\preceq$  is not yet the unfolding ordering we are looking for, since the presence of contraction links implies that a  $\text{u}\ell$ -structure might not be minimal. For instance, let us take a  $\text{u}\ell$ -structure  $U$  such that all its doors are conclusions of a contraction; there exist a denumerable set of  $\text{u}\ell$ -structures  $U'$  s.t.  $U' \preceq U$ —the  $k$ -th of them can be built making  $k$  instances of  $U$  and merging the  $\vee$  links above corresponding conclusions.

### 5.2 Correctness of the unfolding

The absence of a minimal element is not the only lack of  $\preceq$ . The quest for a correct definition of the unfolding partial order faces with an even stronger problem: not all the less-shared-instances definable via  $\preceq$  can be considered a “correct” unfolding of an  $\text{s}\ell$ -structure  $G$ .

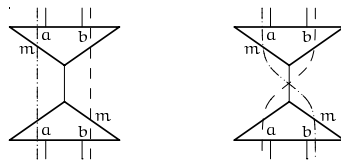


Fig. 15. Matching of ports

In fact, let us assume that  $G$  contain a pair of binary muxes  $e_1$  and  $e_2$  with  $\ell(e_1) = \ell(e_2)$ . In our aims this situation corresponds to a case of matching muxes. Therefore, in a correct  $s\ell$ -structure there should be a perfect matching between the ports of  $e_1$  and the ports of  $e_2$ , and any “correct” unfolding of  $G$  should preserve such a property. For instance, let  $U$  be a  $u\ell$ -structure s.t.  $M : U \preceq G$ . Any pair of lifts  $e'_1, e'_2$  of  $U$  s.t.  $M(e'_i) = e_i$ , with  $i = 1, 2$ , should be matching. Referring to Figure 15, this means that each pair  $e'_1, e'_2$  should be the image of one of the paths drawn on the left-hand side pair of muxes. Any unfolding of  $G$  containing an image of one of the paths drawn on the right-hand side pair of muxes should instead be forbidden in any correct unfolding of  $G$ .

## 6 Overview of the main results

The algebraic semantics we are going to present in section 7 formalizes the matching problem we mentioned in the previous section. Such a semantics is an algebraic characterization of the *proper  $u\ell$ -structures*—the only  $u\ell$ -structures which are “correct” unfoldings of sharing graphs. Via a suitable restriction of the  $s$ -morphisms, we get at the same time a characterization of the *proper  $s\ell$ -structures*—the  $s\ell$ -structures for which there exists a correct unfolding (*i.e.*, s.t. among their less-shared-instances there is a proper  $u\ell$ -structure).

In more details. Once defined the proper  $u\ell$ -structures, we will restrict  $\preceq$  to a partial order  $\ll$  (is a proper unfolding of), and we will define proper an  $s\ell$ -structure  $G$  when  $U \ll G$  for some proper  $u\ell$ -structure  $U$ .

That  $\ll$  is the right unfolding partial order will be proved in section 8 showing that:

- (i) Any proper  $\ell$ -structure is the only element of its equivalence class w.r.t. the transitive, reflexive and symmetric closure of  $\ll$ .
- (ii) The proper  $u\ell$ -structures are minimal w.r.t.  $\ll$ .
- (iii) For any proper  $s\ell$ -structure  $G$ , there is exactly one proper  $u\ell$ -structure  $U$ , say the *least-shared-instance* of  $U$ , for which  $U \ll G$ .

The basic tool to prove such properties is the read-back reduction system, or  $\pi$  rules, that we will define in section 8—a direct implementation of the interpretation of muxes as reindexing-duplication operators. In fact, properness will be proved stable under the step-by-step unfolding performed applying the  $\pi$  rules. Moreover, we will see that the proper  $s\ell$ -structures are indeed the  $s\ell$ -structures which  $\pi$  normalize to an  $\ell$ -structure with the box nesting property.

The uniqueness of the least-shared-instance of a proper  $\mathit{sl}$ -structure will allow to semantically associate an  $\ell$ -structure to each proper  $\mathit{sl}$ -structure. In fact, the notion of *solution* of a  $\mathit{ul}$ -structure, that we will introduce to define properness, will give us a natural way to *semantically read-back* an  $\ell$ -structure  $\mathcal{R}(\mathbf{U})$  from a proper  $\mathit{ul}$ -structure  $\mathbf{U}$ : to take as semantical read-back of a proper  $\mathit{sl}$ -structure the semantical read-back of its least-shared-instance. Such a definition will be proved sound w.r.t. the  $\pi$  rules and, moreover, we will show that the  $\pi$  normal form of a proper  $\mathit{sl}$ -structure coincides with its semantical read-back.

Soundness of properness and read-back w.r.t.  $\beta$  rule will be shown in section 10.

## 7 Algebraic semantics

To introduce the algebraic part, a prelude on the naive attempts that originated this kind of approach.

### 7.1 Lifting functions

It is natural to interpret a  $\mathit{ul}$ -structure  $\mathbf{U}$  as a representation of the structure  $\mathbf{N}$  obtainable removing the lifts of  $\mathbf{U}$  and merging the pairs of corresponding arrows that otherwise would remain dangling. Nevertheless, it is readily seen that this procedure would erase the boxes of  $\mathbf{U}$  (if any), for in general there would not be a unique way to reassign a level to each arrow/link of  $\mathbf{N}$  (the  $\lambda$ -structures are a remarkable exception to this, the levels of their links/arrows are determined once given the underlying  $\lambda$ -tree).

To understand the nature of the problem, it is worth to attempt a direct algebraic interpretation of lifts, following the idea that a lift with offset  $\mathbf{q}$  is a sort of bracket delimiting the part of a structure, say its scope, that has to be lifted by the offset  $\mathbf{q}$ . As an arrow might be in the scope of several lifts (to simplify, let us assume that lift scopes are substructures whose border is made of lifts with the same parameters, and that lift scopes have a well-nesting property similar to the one of boxes), we need a general way to reason about the displacement of levels that take place at a given arrow. Hence, let us assume that each arrow carry a function  $f_v : \mathbb{Z} \rightarrow \mathbb{Z}$  assigning a local name to each level, that is, let  $f_v(\mathbf{n})$  be the actual value of the level  $\mathbf{n}$  at the arrow  $v$ . By the way, in order to get a sound renaming at each arrow, we should also impose some restrictions on the functions  $f$ , as for instance their monotonicity. Anyhow, let us go on without too details, our purpose is just to show why such an approach is still too weak.

The  $\ell$ -structures are the base case. In them, there is no reindexing at all, for there are no lifts around. Thus, any arrow of an  $\ell$ -structure should carry the identity function. In the  $u\ell$ -structures instead, the interpretation of a lift  $e_\nabla$  as a reindexing (or shifting) operator for the levels above its threshold leads to associate to  $e_\nabla$  a functional  $\delta_{m,q} : \mathbb{Z}^{\mathbb{Z}} \rightarrow \mathbb{Z}^{\mathbb{Z}}$  (being  $m$  the threshold and  $q$  the offset of  $e_\nabla$ ) defined by

$$\delta_{m,q}(f)(i) = \begin{cases} f(i) & \text{when } i \leq m \\ f(i + q) & \text{otherwise} \end{cases}$$

and to assume that

$$f_{v_p} = \delta_{m,q}(f_{v_a})$$

where  $v_p = \text{pdoor}(e_\nabla)$  and  $\{v_a\} = \text{adoors}(e_\nabla)$ .

Therefore, the functions assigned to the arrows  $v$  of  $U$  should have the shape

$$f_v = \delta_{m_1,q_1} \delta_{m_2,q_2} \cdots \delta_{m_k,q_k} (id) \quad (4)$$

with a factor  $\delta_{m_i,q_i}$  for each lift that contains  $v$  in its scope.

Provided to succeed assigning a function  $f_v$  to each arrow  $v$ . We see that  $f_{v_p}(\ell(v_p)) = f_{v_a}(\ell(v_a))$  for any lift  $e_\nabla$ . Hence, given a  $u\ell$ -structure  $U$  we could read-back an  $\ell$ -structure  $N$  just assigning the level  $f_v(\ell_U(v))$  to each arrow of  $U$  and removing the lifts contained in it. (For the sake of completeness, some other constraints should be added to the one for lifts in order to ensure that proper levels are assigned to the doors of  $?$  and  $!$  links, but for the moment this is irrelevant.)

The latter procedure is the base of the semantical read-back we will give in section 7.9. Nevertheless, the approach based on the functional  $\delta$  is still inadequate: it does not ensure that two lifts with the same threshold connected through their principal ports have the same name and offset (see section 5.2). Besides, even when this happens, it does not ensure that the function assigned to the premise of the positive lift is equal to the function assigned to the conclusion of the negative one, which is mandatory if we want that, after the replacement of a symmetric pair of facing lifts by an arrow connecting their auxiliary ports, the assignment of the functions  $f$  be preserved.

## 7.2 Lifting operators

Having in mind the last considerations of the previous section, we aim at finding a better algebraic interpretation of lifts by a further step of abstraction.

Let  $\mathbb{D}$  be a domain with a family  $\{\mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}] \mid \mathfrak{m}, \mathfrak{q}, \mathfrak{a} \in \mathbb{Z}, \mathfrak{m} \geq 0, \mathfrak{q} \geq -1\}$  of indexed endomorphisms, say *lifting operators* (see section 7.4), for which the equations:

$$\begin{aligned} \mathcal{L}[\mathfrak{m}, \mathfrak{q}_1, \mathfrak{a}_1](\mathfrak{d}_1) &= \mathcal{L}[\mathfrak{m}, \mathfrak{q}_2, \mathfrak{a}_2](\mathfrak{d}_2) \\ \text{implies } \mathfrak{q}_1 &= \mathfrak{q}_2 \wedge \mathfrak{a}_1 = \mathfrak{a}_2 \wedge \mathfrak{d}_1 = \mathfrak{d}_2 \end{aligned} \quad (\text{LO1})$$

$$\mathcal{L}[\mathfrak{m}_2, \mathfrak{q}_2, \mathfrak{a}_2] \mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1] = \mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1] \mathcal{L}[\mathfrak{m}_2 + \mathfrak{q}_1, \mathfrak{q}_2, \mathfrak{a}_2] \quad (\text{LO2})$$

$$\begin{aligned} \mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1](\mathfrak{d}_1) &= \mathcal{L}[\mathfrak{m}_2, \mathfrak{q}_2, \mathfrak{a}_2](\mathfrak{d}_2) \\ \text{iff } \exists \mathfrak{d} : \mathcal{L}[\mathfrak{m}_2 + \mathfrak{q}_1, \mathfrak{q}_2, \mathfrak{a}_2](\mathfrak{d}) &= \mathfrak{d}_1 \wedge \mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1](\mathfrak{d}) = \mathfrak{d}_2 \end{aligned} \quad (\text{LO3})$$

hold provided  $\mathfrak{m}_1 < \mathfrak{m}_2$ .

**Remark 21** Equation (LO1) encodes the matching problem described in section 5.2. Equation (LO2) corresponds to the idea that, when  $\mathfrak{m}_1 < \mathfrak{m}_2$ , to lift the levels above  $\mathfrak{m}_1$  by  $\mathfrak{q}_1$  and the levels above  $\mathfrak{m}_2$  by  $\mathfrak{q}_2$  is equivalent at to lift the levels above  $\mathfrak{m}_2 + \mathfrak{q}_1$  by  $\mathfrak{q}_2$  and the levels above  $\mathfrak{m}_1$  by  $\mathfrak{q}_1$ . This fact is immediate for positive offsets and is true even when  $\mathfrak{q}_i \geq -1$ . It corresponds to the analogous commutativity equation:

$$\delta_{\mathfrak{m}_2, \mathfrak{q}_2} \delta_{\mathfrak{m}_1, \mathfrak{q}_1} = \delta_{\mathfrak{m}_1, \mathfrak{q}_1} \delta_{\mathfrak{m}_2 + \mathfrak{q}_1, \mathfrak{q}_2}.$$

Equation (LO3) is the analog of the swap equation (LO2), but for the case in which a certain  $\mathfrak{d}_0$  can be obtained applying two different operators, that is,  $\mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1](\mathfrak{d}_1) = \mathfrak{d}_0 = \mathcal{L}[\mathfrak{m}_2, \mathfrak{q}_2, \mathfrak{a}_2](\mathfrak{d}_2)$ . Its aim is to force the uniqueness, modulo Equation (LO3), of the way in which such a  $\mathfrak{d}_0$  is constructed, *i.e.*, that  $\mathfrak{d}_0 = \mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1] \mathcal{L}[\mathfrak{m}_2, \mathfrak{q}_2, \mathfrak{a}_2](\mathfrak{d})$ , for some (unique)  $\mathfrak{d}$ .

We want to find a semantics that assign a suitable product like the one of equation (4) to each arrow of a  $\mathcal{UL}$ -structure, assuming that the lifting operators  $\mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]$  take the place of the integer functionals  $\delta_{\mathfrak{m}, \mathfrak{q}}$ . But to accomplish our project, we still need a better axiomatization of lifting operators (in the style of Danos and Regnier's dynamic algebra, *e.g.*, see [DR93]) and a detailed study of their properties.

### 7.3 Left inverses of lifting operators

The endomorphisms  $\mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]$  are injective (by equation (LO1)). Hence, each  $\mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]$  has a left inverse  $\overline{\mathcal{L}}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]$ . Such an inverse is however not unique in general, since it may assume any value outside the codomain of  $\mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]$ . But, if we consider partial functions too, the natural left inverse  $\mathcal{F}^*$  of a partial endomorphism  $\mathcal{F}$  is the less defined partial transformation  $\mathcal{F}^*$  s.t.  $\mathcal{F} \mathcal{F}^* \mathcal{F} = \mathcal{F}$  (*i.e.*,  $\text{dom}(\mathcal{F}^*) = \text{codom}(\mathcal{F})$  and  $\mathcal{F}^*(\mathcal{F}(\mathfrak{d})) = \mathfrak{d}$ , for any  $\mathfrak{d} \in \text{dom}(\mathcal{F})$ ).

In more details, for any endomorphism  $\mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]$ , we have that:

$$(\mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1] \cdots \mathcal{L}[\mathfrak{m}_k, \mathfrak{q}_k, \mathfrak{a}_k])^* = \overline{\mathcal{L}}[\mathfrak{m}_k, \mathfrak{q}_k, \mathfrak{a}_k] \cdots \overline{\mathcal{L}}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1],$$

where

$$\overline{\mathcal{L}}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}](d) = \begin{cases} \bar{d} & \text{when } d = \mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}](\bar{d}) \\ \perp & \text{when } d \notin \text{codom}(\mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]) \end{cases}$$

(being  $\overline{\mathcal{L}}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}](d) = \perp$  just a denotation for  $d \notin \text{dom}(\mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}])$ ).

We get in this way a monoid  $\text{LSeq}^*$  of injective partial transformations of  $\mathbb{D}$  that is closed under left inversion, for it is immediate that

$$\overline{\mathcal{L}}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]^* = \mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}].$$

Furthermore,  $\text{LSeq}^*$  is a left inverse semigroup, and the equations (LO1-3) can be nicely reformulated in it.

**Remark 22** All the results of the paper could be indeed obtained without left inverse lifting operators. In fact, finding the solutions of a  $\text{ul}$ -structure (section 7.8) we will use lifting operators only (*cf.* [GMM97a]). Nevertheless, their introduction simplify the proof of some algebraic properties and, more important, gives a better idea of the relations between our algebraic approach and Geometry of Interaction.

#### 7.4 The inverse semigroup $\text{LSeq}^*$

First, a remind of the definition of (left) inverse semigroup.

**Definition 23 (left inverse semigroup)** An inverse semigroup with  $0$  is a monoid  $\mathbb{S}$  with an absorbing element (i.e., an element  $0$  s.t.  $0\mathcal{F} = 0 = \mathcal{F}0$ , for any  $\mathcal{F}$ ) closed under an involution operation  $(\cdot)^*$  s.t., for any  $\mathcal{F}, \mathcal{F}_1, \mathcal{F}_2 \in \mathbb{S}$ :

$$\begin{aligned} \mathcal{F}^{**} &= \mathcal{F} \\ (\mathcal{F}_1 \mathcal{F}_2)^* &= \mathcal{F}_2^* \mathcal{F}_1^* \\ \langle \mathcal{F} \rangle \mathcal{F} &= \mathcal{F} \\ \langle \mathcal{F}_1 \rangle \langle \mathcal{F}_2 \rangle &= \langle \mathcal{F}_2 \rangle \langle \mathcal{F}_1 \rangle \end{aligned}$$

where  $\langle \mathcal{F} \rangle \equiv \mathcal{F}\mathcal{F}^*$ .

The axioms of inverse semigroups allow to immediately prove that:



(i) Each  $\langle \mathcal{F} \rangle$  is invariant under involution and is an idempotent of  $\mathbb{S}$ , *i.e.*,

$$\begin{aligned}\langle \mathcal{F} \rangle^* &= \langle \mathcal{F} \rangle \\ \langle \mathcal{F} \rangle \langle \mathcal{F} \rangle &= \langle \mathcal{F} \rangle\end{aligned}$$

since  $\langle \mathcal{F} \rangle \langle \mathcal{F} \rangle = \langle \mathcal{F} \rangle \mathcal{F} \mathcal{F}^* = \mathcal{F} \mathcal{F}^* = \langle \mathcal{F} \rangle$ .

(ii) Each idempotent of  $\text{LSeq}^*$  may be written as  $\langle \mathcal{F} \rangle$  for some  $\mathcal{F}$  and is then invariant under involution, *i.e.*,

$$\begin{aligned}\mathcal{F} \mathcal{F} = \mathcal{F} &\text{ implies } \langle \mathcal{F} \rangle = \mathcal{F} \\ \mathcal{F} \mathcal{F} = \mathcal{F} &\text{ implies } \mathcal{F}^* = \mathcal{F}\end{aligned}$$

since, if  $\mathcal{F} \mathcal{F} = \mathcal{F}$ , then  $\mathcal{F} = \langle \mathcal{F} \rangle \mathcal{F} = \langle \mathcal{F} \rangle \langle \mathcal{F}^* \rangle = \langle \mathcal{F}^* \rangle \langle \mathcal{F} \rangle = \mathcal{F}^* \langle \mathcal{F} \rangle = (\langle \mathcal{F} \rangle \mathcal{F})^* = \mathcal{F}^*$ , and then  $\langle \mathcal{F} \rangle = \mathcal{F}$ . Which in particular implies  $1^* = 1$  and  $0^* = 0$ .

A *lifting operator* is a triple of integers  $\mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]$ , with  $\mathfrak{m}, \mathfrak{a} \geq 0$  and  $\mathfrak{q} \geq -1$ . The index  $\mathfrak{m}$  is the *threshold* of the lifting operator; the index  $\mathfrak{q}$  is its *offset*; the index  $\mathfrak{a}$  is its *name*. For each lifting operator there is a corresponding barred triple  $\overline{\mathcal{L}}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]$  called its *left inverse*.

**Definition 24** ( $\text{LSeq}^*$ ) *The inverse semigroup  $\text{LSeq}^*$  is the smallest inverse semigroup generated by composition of lifting operators and left inverse lifting operators according to the axioms:*

$$\mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}]^* = \overline{\mathcal{L}}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}] \quad (\text{LS0})$$

$$\overline{\mathcal{L}}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}] \mathcal{L}[\mathfrak{m}, \mathfrak{q}, \mathfrak{a}] = 1 \quad (\text{LS1})$$

$$\overline{\mathcal{L}}[\mathfrak{m}, \mathfrak{q}_2, \mathfrak{a}_2] \mathcal{L}[\mathfrak{m}, \mathfrak{q}_1, \mathfrak{a}_1] = 0 \quad \text{if } \mathfrak{q}_1 \neq \mathfrak{q}_2 \text{ or } \mathfrak{a}_1 \neq \mathfrak{a}_2 \quad (\text{LS2})$$

$$\mathcal{L}[\mathfrak{m}_2, \mathfrak{q}_2, \mathfrak{a}_2] \mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1] = \mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1] \mathcal{L}[\mathfrak{m}_2 + \mathfrak{q}_1, \mathfrak{q}_2, \mathfrak{a}_2] \quad (\text{LS3})$$

$$\overline{\mathcal{L}}[\mathfrak{m}_2, \mathfrak{q}_2, \mathfrak{a}_2] \mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1] = \mathcal{L}[\mathfrak{m}_1, \mathfrak{q}_1, \mathfrak{a}_1] \overline{\mathcal{L}}[\mathfrak{m}_2 + \mathfrak{q}_1, \mathfrak{q}_2, \mathfrak{a}_2] \quad (\text{LS4})$$

when  $\mathfrak{m}_1 < \mathfrak{m}_2$ .

The natural model of  $\text{LSeq}^*$  is the monoid generated by the indexed endomorphisms of  $\mathbb{D}$  (the lifting operators) and by their left inverses (the left inverse lifting operators). Under this interpretation, it is readily seen that:

- (i) The constant 0 is the nowhere defined partial transformation of  $\mathbb{D}$ .
- (ii) Any idempotent  $\langle \mathcal{F} \rangle$  of  $\text{LSeq}^*$  is the identity function restricted to the codomain of  $\mathcal{F}$ , *i.e.*,  $\text{dom}(\langle \mathcal{F} \rangle) = \text{codom}(\mathcal{F})$ . In particular, 1 is the identity endomorphism of  $\mathbb{D}$ .
- (iii) Axioms (LS1) and (LS2) are equivalent to equation (LO1). Axiom (LS3) coincide with equation (LO2). Axiom (LS4) corresponds to the *only if* part of equation (LO3) (the *if* part is subsumed by equation (LO3)).

## 7.5 Lifting sequences

A *lifting sequence*  $\mathcal{H}$  is a finite product of lifting operators. The monoid  $\text{LSeq}$  is the smallest one containing the lifting operators (*i.e.*, no left inverse operator  $\overline{\mathcal{L}}[m, q, a]$  belongs to it).

By iterated application of axiom (LS3), each lifting sequence is equivalent to a (unique) sequence in *canonical form* with the thresholds non-decreasingly ordered, *i.e.*, for any  $\mathcal{H}$  there exists a sequence  $\mathcal{H} = \prod_{0 < i \leq k} \mathcal{L}[m_i, q_i, a_i]$ , with  $m_i \leq m_j$  if  $i < j$ .

By induction on the length  $|\mathcal{H}| = k$  of the lifting sequence, it is indeed direct to see that  $\mathcal{H}^* \mathcal{H} = \langle \mathcal{H}^* \rangle = 1$  (note that this accord with the interpretation in terms of partial endomorphisms of  $\mathbb{D}$ , for  $\text{codom}(\mathcal{H}^*) = \text{dom}(\mathcal{H}) = \mathbb{D}$ ).

Let  $n_0 \leq n_1$ . A *lifting sequence from  $n_0$  to  $n_1$*  is a product of lifting operators  $\mathcal{H} = \prod_{0 < i \leq k} \mathcal{L}[m_i, q_i, a_i]$  in which

$$n_0 \leq m_i < n_1 + \sum_{0 < j < i} q_j,$$

for  $i = 1, 2, \dots, k$ .

**Definition 25** ( $\text{LSeq}[n_0, n_1]$ ) *The monoid  $\text{LSeq}[n_0, n_1]$  is the smallest one containing the lifting sequences from  $n_0$  to  $n_1$ .*

The last definition is sound as it is invariant under application of axiom (LS3). Furthermore,

$$\text{LSeq} = \text{LSeq}[0, \omega] = \bigcup_{n \in \omega} \text{LSeq}[0, n]$$

for  $\text{LSeq}[n_0, n_1] \subseteq \text{LSeq}[m_0, m_1]$ , when  $m_0 \leq n_0$  and  $n_1 \leq m_1$ .

The *global offset*  $\|\mathcal{H}\|$  of a lifting sequence is the sum of the offsets of its lifting operators, *i.e.*, if  $\mathcal{H} = \prod_{0 < i \leq k} \mathcal{L}[m_i, q_i, a_i]$ , then

$$\|\mathcal{H}\| = \sum_{0 < i \leq k} q_i.$$

Let  $\mathcal{H} = \prod_{0 < i \leq k} \mathcal{L}[m_i, q_i, a_i]$  be a lifting sequence and let  $r$  be an integer s.t.  $m_i + r \geq 0$ , for  $i = 1, 2, \dots, k$ . The *lifting of  $\mathcal{H}$  by the offset  $r$*  is the lifting sequence

$$\mathcal{H}^{\uparrow r} = \prod_{0 < i \leq k} \mathcal{L}[m_i + r, q_i, a_i].$$

**Fact 26** Let  $n_0 \leq n_1 \leq n_2$ .

- (i)  $n_1 + \|\mathcal{H}\| \geq n_0$ , for any  $\mathcal{H} \in \text{LSeq}[n_0, n_1]$ .
- (ii) If  $\mathcal{H}_1 \in \text{LSeq}[n_0, n_1]$  and  $\mathcal{H}_2 \in \text{LSeq}[n_1, n_2]$ , then:
  - (a)  $\mathcal{H}_2 \mathcal{H}_1 \in \text{LSeq}[n_0, n_2]$ ;
  - (b)  $\mathcal{H}_2 \mathcal{H}_1 = \mathcal{H}_1 \mathcal{H}_2^{\uparrow \|\mathcal{H}_1\|}$ ;
  - (c)  $\mathcal{H}_2^* \mathcal{H}_1 = \mathcal{H}_1 (\mathcal{H}_2^{\uparrow \|\mathcal{H}_1\|})^*$ .
- (iii) For any  $\mathcal{H} \in [n_0, n_2]$ , there exists a unique pair  $\mathcal{H}_1 \in \text{LSeq}[n_0, n_1]$  and  $\mathcal{H}_2 \in \text{LSeq}[n_1, n_2]$  s.t.  $\mathcal{H} = \mathcal{H}_2 \mathcal{H}_1$ .

**Proof.** The first two items are immediate by induction on  $|\mathcal{H}|$ . For the last one, let  $\mathcal{H} = \prod_{0 < i \leq k} \mathcal{L}[m_i, q_i, a_i]$  be in canonical form. Let  $h$  be the first index for which  $n + Q_i \geq m_i$  (where  $Q_i = \sum_{0 \leq j < i} q_j$ ) if any, or let  $h = k + 1$  otherwise. Let us take  $\mathcal{H}_1 = \prod_{0 < i < h} \mathcal{L}[m_i, q_i, a_i]$  and  $\mathcal{H}_2 = \prod_{h \geq i < k} \mathcal{L}[m_i - Q_h, q_i, a_i]$ . By definition,  $\mathcal{H}_1 \in \text{LSeq}[n_0, n_1]$  and  $\mathcal{H}_2 \in \text{LSeq}[n_1, n_2]$ . Furthermore,  $\mathcal{H}_2 \mathcal{H}_1 = \mathcal{H}_1 \mathcal{H}_2^{\uparrow Q_h} = \mathcal{H}$ . The construction of  $\mathcal{H}_1$  and  $\mathcal{H}_2$  also proves their uniqueness.  $\square$

**Proposition 27 (canonical form)** For any  $\mathcal{F} \in \text{LSeq}^*$ , with  $\mathcal{F} \neq 0$ , there is a unique pair  $\mathcal{H}_+, \mathcal{H}_- \in \text{LSeq}$  s.t.  $\mathcal{F} = \mathcal{H}_+ \mathcal{H}_-^*$ .

**Proof.**

**(existence)** By induction on  $|\mathcal{F}|$ . The cases  $|\mathcal{F}| = 0$  and  $\mathcal{F} = \mathcal{L}[m, q, a] \mathcal{F}_1$  are direct. So, let us take  $\mathcal{F} = \overline{\mathcal{L}}[m, q, a] \mathcal{F}_1$ . By the induction hypothesis, we have  $\mathcal{F} = \overline{\mathcal{L}}[m, q, a] \mathcal{H}_+ \mathcal{H}_-^*$ , for some  $\mathcal{H}_+, \mathcal{H}_- \in \text{LSeq}$ . Let  $\mathcal{H}_+$  be in canonical form. By Fact 26, there are  $\mathcal{H} \in \text{LSeq}[0, m]$  and  $\mathcal{H} \in \text{LSeq}[m, \omega]$  s.t.  $\mathcal{H} = \mathcal{H}_2 \mathcal{H}_1$ . If  $\mathcal{H}_2 \in \text{LSeq}[m + 1, \omega]$ , then  $\overline{\mathcal{L}}[m, q, a] \mathcal{H} = \mathcal{H}_2^{\uparrow q} \overline{\mathcal{L}}[m, q, a] \mathcal{H}_1 = \mathcal{H}_2^{\uparrow q} \mathcal{H}_1 \overline{\mathcal{L}}[m, q + \|\mathcal{H}_1\|, a]$ , and thus the thesis. Otherwise,  $\mathcal{H}_2 = \mathcal{L}[m, q, a] \mathcal{H}'_2$  with  $\mathcal{H}'_2 \in \text{LSeq}$  (by the hypothesis  $\mathcal{F} \neq 0$ ) and thus  $\overline{\mathcal{L}}[m, q, a] \mathcal{F} = \mathcal{H}'_2 \mathcal{H}_-^*$ .

**(uniqueness)** Let us start proving that the following claims hold for any  $\mathcal{H}_1, \mathcal{H}_2 \in \text{LSeq}$ : (i)  $\mathcal{H}_1 \mathcal{H}_2^* = 1$  iff  $|\mathcal{H}_1| = |\mathcal{H}_2| = 0$ ; (ii)  $\langle \mathcal{H}_1 \rangle = \langle \mathcal{H}_2 \rangle$  iff  $\mathcal{H}_1 = \mathcal{H}_2$ . For the first claim, let us assume  $\mathcal{H}_1 = \mathcal{L}[m, q, a] \mathcal{H}'$ . For any pair  $q', a'$  s.t.  $q \neq q'$  or  $a \neq a'$ , we would get  $1 = \overline{\mathcal{L}}[m, q', a'] \mathcal{H}_1 \mathcal{H}_2^* \mathcal{L}[m, q', a'] = 0$ . Hence,  $|\mathcal{H}_+| = 0$ , etc. For the second claim, let  $\mathcal{H}_+ \mathcal{H}_-^*$  be a canonical form of  $\mathcal{H}_1^* \mathcal{H}_2$ . We have  $1 = \mathcal{H}_1^* \langle \mathcal{H}_1 \rangle \mathcal{H}_1 = \mathcal{H}_1^* \langle \mathcal{H}_2 \rangle \mathcal{H}_1 = \mathcal{H}_1^* \mathcal{H}_2 (\mathcal{H}_1^* \mathcal{H}_2)^* = \mathcal{H}_+ \mathcal{H}_-^* \mathcal{H}_- \mathcal{H}_+^* = \mathcal{H}_+ \mathcal{H}_+^*$ . From which, (by the previous claim)  $|\mathcal{H}_+| = 0$ . In an analogous way we prove that  $|\mathcal{H}_-| = 0$  and then that  $\mathcal{H}_1^* \mathcal{H}_2 = 1$ . Hence,  $\mathcal{H}_2 = \langle \mathcal{H}_2 \rangle \mathcal{H}_2 = \langle \mathcal{H}_1 \rangle \mathcal{H}_2 = \mathcal{H}_1$ .

Let  $\mathcal{H}_+ \mathcal{H}_-^*$  and  $\widehat{\mathcal{H}}_+ \widehat{\mathcal{H}}_-^*$  be two canonical forms of  $\mathcal{F}$ . We have  $\langle \mathcal{H}_+ \rangle = \langle \mathcal{F} \rangle = \langle \widehat{\mathcal{H}}_+ \rangle$ . Which implies  $\mathcal{H}_+ = \widehat{\mathcal{H}}_+$  and  $\mathcal{H}_- = \widehat{\mathcal{H}}_-$ . Hence, to conclude we

just have to show the uniqueness of the canonical form of a lifting sequence  $\mathcal{H}$ .

The case  $\mathcal{H} = 1$  is subsumed by the claim. So, let  $\mathcal{L}[m_1, q_1, a_1] \mathcal{H}_1 = \mathcal{H} = \mathcal{L}[m_2, q_2, a_2] \mathcal{H}_2$  be canonical forms, and w.l.o.g. let  $m_1 \leq m_2$ . If  $m_1 < m_2$ , then  $\mathcal{H}_1 = \mathcal{L}[m_2 + q_1, q_2, a_2] \mathcal{H}_2^{\dagger q_1} \overline{\mathcal{L}}[m_1, q_1, a_1]$ , that leads to the contradiction  $\mathcal{H}_1 \mathcal{L}[m_1, q'_1, a'_1] = 0$  when  $q'_1 \neq q_1$  or  $a'_1 \neq a_1$ . Hence, the only possibility is  $m_1 = m_2$ , and then also  $q_1 = q_2, a_1 = a_2$ . Concluding, from the initial canonical forms we have got two shorter equivalent ones  $\mathcal{H}_1 = \mathcal{H}_2$ .  $\square$

The elements of  $\text{LSeq}^*$  can then be written in *canonical form* assuming that both  $\mathcal{H}_+$  and  $\mathcal{H}_-$  of Proposition 27 are canonical.

**Remark 28** A key assumption in the latter proof is  $1 \neq 0$ . We could then reformulate the proposition saying: The only model of  $\text{LSeq}^*$  in which Proposition 27 does not hold is the trivial one.

## 7.6 LSeq lower semilattice

The lifting sequences are partially ordered by the binary relation:

$$\mathcal{H}_1 \sqsubseteq \mathcal{H}_2 \quad \text{when} \quad \mathcal{H}_2 = \mathcal{H}_1 \mathcal{H}$$

for some lifting sequence  $\mathcal{H} \in \text{LSeq}$ . In fact,

- the reflexivity:  $\mathcal{H} = \mathcal{H} 1$ ;
- the transitivity:  $\mathcal{H}_0 \sqsubseteq \mathcal{H}_1 \sqsubseteq \mathcal{H}_2$  implies  $\mathcal{H}_1 = \mathcal{H}_0 \mathcal{H}$  and  $\mathcal{H}_2 = \mathcal{H}_0 \mathcal{H} \mathcal{H}'$ ;
- the antisymmetry:  $\mathcal{H}_2 = \mathcal{H}_1 \mathcal{H}$  and  $\mathcal{H}_1 = \mathcal{H}_2 \mathcal{H}'$  implies  $1 = \mathcal{H}_1^* \mathcal{H}_1 = \mathcal{H}_1^* \mathcal{H}_2 \mathcal{H}' = \mathcal{H}_1^* \mathcal{H}_1 \mathcal{H} \mathcal{H}' = \mathcal{H} \mathcal{H}'$ , and thus  $\mathcal{H} = \mathcal{H}' = 1$ .

**Fact 29** Let  $\mathcal{H}_1, \mathcal{H}_2 \in \text{LSeq}$ .

$$\mathcal{H}_1 \sqsubseteq \mathcal{H}_2 \quad \text{iff} \quad \mathcal{H}_1^* \mathcal{H}_2 \in \text{LSeq} \quad \text{iff} \quad \langle \mathcal{H}_1 \rangle \mathcal{H}_2 = \mathcal{H}_2.$$

**Proof.**

- (i)  $\langle \mathcal{H}_1 \rangle \mathcal{H}_2 = \mathcal{H}_2$  implies  $\mathcal{H}_1^* \mathcal{H}_2 \in \text{LSeq}$ : Let  $\mathcal{H}_+ \mathcal{H}_-$  be the canonical form of  $\mathcal{H}_1^* \mathcal{H}_2$ . By hypothesis,  $\mathcal{H}_1 \mathcal{H}_1^* \mathcal{H}_2 = \mathcal{H}_1 \mathcal{H}_+ \mathcal{H}_- = \mathcal{H}_2$ , which implies  $\mathcal{H}_- = 0$ . Thus,  $\mathcal{H}_1^* \mathcal{H}_2 = \mathcal{H}_+ \in \text{LSeq}$ .
- (ii)  $\mathcal{H}_1^* \mathcal{H}_2 \in \text{LSeq}$  implies  $\mathcal{H}_1 \sqsubseteq \mathcal{H}_2$ : Let  $\mathcal{H} = \mathcal{H}_1^* \mathcal{H}_2$ . We have  $\mathcal{H}_2 = \mathcal{H}_2 \mathcal{H}^* \mathcal{H} = \langle \mathcal{H}_2 \rangle \langle \mathcal{H}_1 \rangle \mathcal{H}_2 = \langle \mathcal{H}_1 \rangle \langle \mathcal{H}_2 \rangle \mathcal{H}_2 = \langle \mathcal{H}_1 \rangle \mathcal{H}_2 = \mathcal{H}_1 \mathcal{H}$ , and thus  $\mathcal{H}_1 \sqsubseteq \mathcal{H}_2$ .

- (iii)  $\mathcal{H}_1 \sqsubseteq \mathcal{H}_2$  implies  $\langle \mathcal{H}_1 \rangle \mathcal{H}_2 = \mathcal{H}_2$ : If  $\mathcal{H}_2 = \mathcal{H}_1 \mathcal{H}$ , then  $\langle \mathcal{H}_1 \rangle \mathcal{H}_2 = \langle \mathcal{H}_1 \rangle \mathcal{H}_1 \mathcal{H} = \mathcal{H}_1 \mathcal{H} = \mathcal{H}_2$ .  $\square$

The *meet* of two lifting sequences  $\mathcal{H}_1$  and  $\mathcal{H}_2$  is defined by:

$$\mathcal{H}_1 \sqcap \mathcal{H}_2 = \begin{cases} \mathcal{L}[\mathbf{m}, \mathbf{q}, \mathbf{a}](\mathcal{H}'_1 \sqcap \mathcal{H}'_2) & \text{if } \mathcal{H}_i = \mathcal{L}[\mathbf{m}, \mathbf{q}, \mathbf{a}] \mathcal{H}'_i, \text{ for } i = 1, 2 \\ 1 & \text{otherwise} \end{cases}$$

By an easy induction on  $\mathcal{H}_1$  it is not difficult to check that: the definition of  $\sqcap$  is sound;  $\mathcal{H}_1 \sqcap \mathcal{H}_2 \sqsubseteq \mathcal{H}_i$ , for  $i = 1, 2$ ; and that  $\mathcal{H} \sqsubseteq \mathcal{H}_i$  for  $i = 1, 2$ , implies  $\mathcal{H} \sqsubseteq \mathcal{H}_1 \sqcap \mathcal{H}_2$ .

**Fact 30** *The sets  $\text{LSeq}$  and  $\text{LSeq}[\mathbf{n}_0, \mathbf{n}_1]$  are lower semilattices for the partial order  $\sqsubseteq$  with 1 as minimum and  $\sqcap$  as greatest-lower-bound operator.*

It is also not difficult to see that  $\sqcap$  distributes on the composition of lifting sequences in the following relevant case.

**Fact 31** *Let  $\mathcal{H}_0, \mathcal{H}'_0 \in \text{LSeq}[\mathbf{n}_0, \mathbf{n}_1]$  and  $\mathcal{H}_1, \mathcal{H}'_1 \in \text{LSeq}[\mathbf{n}_1, \mathbf{n}_2]$ . We have that  $\mathcal{H}_1 \mathcal{H}_0 \sqcap \mathcal{H}'_1 \mathcal{H}'_0 = (\mathcal{H}_1 \sqcap \mathcal{H}'_1)(\mathcal{H}_0 \sqcap \mathcal{H}'_0)$ .*

## 7.7 Lifting assignments

**Definition 32 (lifting assignment)** *A lifting assignment for a  $\text{ul}$ -structure  $\mathbf{U}$  is a map  $\mathcal{A} : \text{vtx}(\mathbf{U}) \rightarrow \text{LSeq}$  s.t. (see Figure 16):*

- (i)  $\mathcal{A}(\mathbf{v}) \in \text{LSeq}[0, \ell(\mathbf{v})]$ , for any  $\mathbf{v} \in \text{vtx}(\mathbf{U})$ ;
- (ii)  $\mathcal{A}(\mathbf{v}_i) = \mathcal{S}_e \mathcal{A}(\mathbf{v}_e)$ , for some  $\mathcal{S}_e \in \text{LSeq}[\ell(\mathbf{v}_e), \ell(\mathbf{v}_i)]$ , when  $\mathbf{v}_i$  and  $\mathbf{v}_e$  are respectively the internal and the external doors of a box door link  $e$  (i.e., a ? or an ! link);
- (iii)  $\mathcal{A}(\mathbf{v}_p) = \mathcal{L}[\mathbf{m}, \mathbf{q}, \mathbf{a}] \mathcal{A}(\mathbf{v}_a)$ , when  $\mathbf{v}_p = \text{pdoor}(e_\nabla)$  and  $\{\mathbf{v}_a\} = \text{adoors}(e_\nabla)$  for a lift  $e_\nabla$  s.t.  $\mathbf{m} = \ell(e_\nabla)$ ,  $\mathbf{q} = \ell(\mathbf{v}_a) - \ell(\mathbf{v}_p)$ , and  $\mathbf{a}$  is the name of the auxiliary port of  $e_\nabla$ ;
- (iv)  $\mathcal{A}(\mathbf{v}_2) = \mathcal{A}(\mathbf{v}_1)$ , when  $\mathbf{v}_1$  and  $\mathbf{v}_2$  are doors of the same link  $e$ , and  $e$  is neither a box door link nor a lift.

The second and third items of the previous definition are compatible with the first one (by Fact 26); the fourth item is a special case of the second one, for in such a case  $\ell(\mathbf{v}_1) = \ell(\mathbf{v}_2)$  and  $\text{LSeq}[\mathbf{n}, \mathbf{n}] = \{1\}$ . Further, for any box door link  $e$ , the parameter  $\mathcal{S}_e$  for which  $\mathcal{A}(\mathbf{v}_i) = \mathcal{S}_e \mathcal{A}(\mathbf{v}_e)$  is uniquely determined by the lifting sequences assigned to the internal and external doors  $\mathbf{v}_i$  and  $\mathbf{v}_e$  (it is indeed determined by  $\mathcal{A}(\mathbf{v}_i)$  only, see Fact 26).

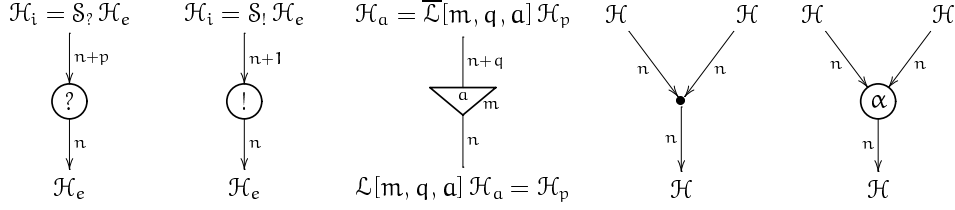


Fig. 16. Lifting assignment ( $\mathcal{S}_? \in \text{LSeq}[n, n + p]$  and  $\mathcal{S}_! \in \text{LSeq}[n, n + 1]$ ).

**Remark 33** The constraints of the box door links encode that such links are global boundaries for the scope of the reindexing operators associated to lifts. Moreover, the introduction of the parameters  $\mathcal{S}_e$  also corresponds to the idea that we can take the interior of a box and lift it by any quantity, provided that no level inside the box become lower than the level of the box doors. (To this purpose, let us note that, given  $\mathcal{H} \in \text{LSeq}[n_0, n_1]$ , we have  $n_0 \leq n_1 + \|\mathcal{H}\|$  (Fact 26). Hence, interpreting  $\|\mathcal{H}\|$  as the global quantity by which we increase the levels above  $n_1 - 1$ , we see that the previous constraint on the levels is not violated at the box door links.) There is however a remarkable difference between principal and auxiliary door links. The reindexing parameter of an ! link  $e_!$ —the lifting sequence  $\mathcal{S}_{e_!}$  relating the values assigned to the internal and the external doors of  $e_!$ —is an independent parameter, at least from the interior of  $\text{box}^\ell(e_!)$ . On the contrary, the reindexing parameter of a ? link is not independent. In fact, a ? link may only erase the reindexing operators introduced by the principal doors of the boxes of which the link is auxiliary door, or introduced by reductions executed inside such boxes. We will farther see that this has a direct correspondence in the dynamics of boxes we will discuss in section 10, and in the read-back reduction system that we will introduce in section 8.

### 7.8 Solutions of a $\mathcal{U}$ -structure

As a consequence of Remark 33, we see that, among all the lifting assignments for a  $\mathcal{U}$ -structure  $\mathcal{U}$ , we need a way to pick up one of them for each choice of the parameters  $\mathcal{S}_!$  associated to the ! links.

In the relevant cases of  $\lambda$ -calculus and MELL the problem has an immediate solution—in the second case, provided that the levels of the structure correspond to the ones induced by the box nesting, see [GMM97a]. In fact, the topology of the corresponding structures forces the uniqueness of the lifting assignment once fixed the parameters  $\mathcal{S}_!$ . Unfortunately, in the general case this is not true. The solution is to exploit the  $\sqcap$  operator to prune away the parts of a lifting assignment for  $\mathcal{U}$  which are not introduced by lifts of  $\mathcal{U}$  or by parameters  $\mathcal{S}_!$ .

A map  $\mathcal{S}$  from the ! links of a  $\mathcal{U}$ -structure  $\mathcal{U}$  to  $\text{LSeq}$  is said an *internal state*

of  $\mathbf{U}$  when  $\mathcal{S}(e_i) \in \text{LSeq}[\ell(\text{pdoor}(e_i)), \ell(\text{pdoor}(e_i + 1))]$ .

By what previously said on the parameters  $\mathcal{S}_e$ , any lifting assignment determines a unique internal state of  $\mathbf{U}$  s.t.  $\mathcal{S}(e) = \mathcal{S}_e$ .

A lifting assignment for  $\mathbf{U}$  is an  $\mathcal{S}$ -assignment when, for any  $e_i \in \text{lnk}_!(\mathbf{U})$ , the constraints of Definition 32 hold with  $\mathcal{H}_{e_i} = \mathcal{S}(e_i)$ .

The partial order relation and the meet operation between lifting sequences extend (pointwise) to lifting assignments:

$$\mathcal{A}_1 \sqsubseteq \mathcal{A}_2 \quad \text{iff} \quad \forall v \in \text{vtx}(\mathbf{U}) : \mathcal{A}_1(v) \sqsubseteq \mathcal{A}_2(v)$$

and analogously for the internal states of  $\mathbf{U}$ , replacing vertices with ! links.

**Fact 34** *If  $\mathcal{A}_1$  is an  $\mathcal{S}_1$ -assignment for the  $\text{ul}$ -structure  $\mathbf{U}$  and  $\mathcal{A}_2$  is an  $\mathcal{S}_2$ -assignment for  $\mathbf{U}$ , then  $\mathcal{A}_1 \sqcap \mathcal{A}_2$  is an  $(\mathcal{S}_1 \sqcap \mathcal{S}_2)$ -assignment for  $\mathbf{U}$ .*

**Proof.** Let  $e$  be an ! link, we have  $\mathcal{A}_1(v_e) \sqcap \mathcal{A}_2(v_e) = (\mathcal{S}_1(e) \sqcap \mathcal{S}_2(e)) (\mathcal{A}_1(v_e) \sqcap \mathcal{A}_2(v_e))$  (by Fact 31). And so on for the other kinds of link.  $\square$

The latter fact implies that, for any internal state  $\mathcal{S}$  with at least an  $\mathcal{S}$ -assignment, the set  $\{\mathcal{A} \mid \mathcal{A} \text{ is an } \mathcal{S}\text{-assignment of } \mathbf{U}\}$  is closed under meet and has a minimum.

**Definition 35 (solutions)** *Let  $\mathbf{U}$  be a  $\text{ul}$ -structure with an  $\mathcal{S}$ -assignment. The minimum  $\mathcal{S}$ -assignment for  $\mathbf{U}$  is the  $\mathcal{S}$ -solution of  $\mathbf{U}$ . In particular, for the quiescence internal state  $\mathcal{I}$  (being  $\mathcal{I}(e) = 1$  for any  $e \in \text{lnk}_!(\mathbf{U})$ ), the  $\mathcal{I}$ -solution is said the quiescence solution of  $\mathbf{U}$ .*

An example of  $\text{s}\ell$ -structure with a quiescence solution is the MELL  $\text{s}\ell$ -structure in Figure 17. Near each arrow of the  $\text{s}\ell$ -structure we have written (framed) the lifting sequence that the quiescence solution assigns to that arrow. Near each ? link  $e_?$  there is instead the value of the corresponding internal parameter  $\mathcal{H}_{e_?}$ —note that such parameters are not equal to 1, even though we are considering a quiescence solution.

## 7.9 Semantical read-back

The relevance of the solutions of a  $\text{ul}$ -structure, and in particular of its quiescence solution, is that they allow to remove the lifts from the structure recovering at the same time a sound level assignment (*cf.* section 7.1). In fact, for any  $\mathcal{S}$ -solution  $\mathcal{A}$  of a  $\text{ul}$ -structure  $\mathbf{U}$ , it is readily seen that:

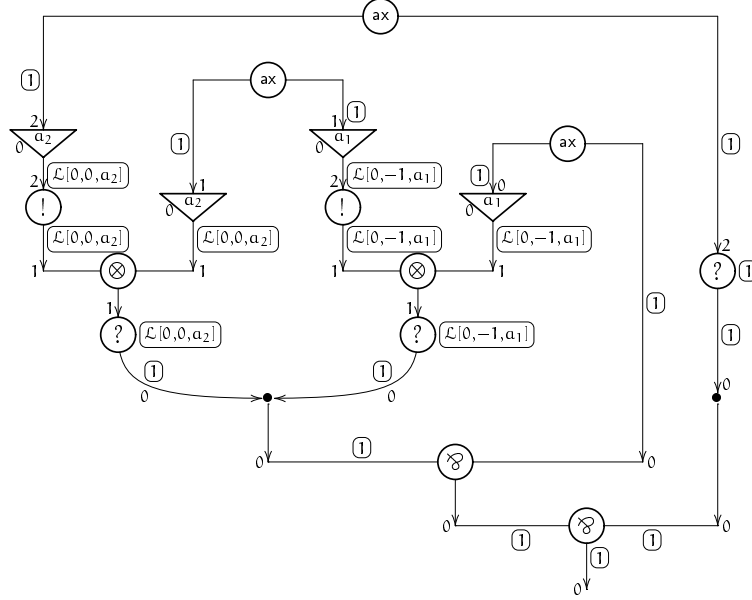


Fig. 17. An example of quiescence solution.

- (i)  $\ell_{\mathbf{U}}(v) + \|\mathcal{A}(v)\| \geq 0$ , for any  $v \in \text{vtx}(\mathbf{U})$ ;
- (ii)  $\ell_{\mathbf{U}}(v_e) + \|\mathcal{A}(v_e)\| \leq \ell_{\mathbf{U}}(v_i) + \|\mathcal{A}(v_i)\|$ , when  $v_i$  and  $v_e$  are respectively the internal and the external door of a box door link  $e$ ;
- (iii)  $\ell_{\mathbf{U}}(v_p) + \|\mathcal{A}(v_p)\| = \ell_{\mathbf{U}}(v_a) + \|\mathcal{A}(v_a)\|$ , when  $v_p = \text{pdoor}(e_{\nabla})$  and  $\{v_a\} = \text{adoors}(e_{\nabla})$  for some lift  $e_{\nabla}$ .

**Definition 36 (read-back)** Let  $\mathbf{U}$  be a  $\mathbf{ul}$ -structure with quiescence solution  $\mathcal{Q}$ . Its read-back  $\mathcal{R}(\mathbf{U})$  is the  $\ell$ -structure obtainable from  $\mathbf{U}$ :

- (i) associating to each arrow  $v$  its actual level  $\ell_{\mathbf{U}}^{\mathcal{Q}}(v) = \ell_{\mathbf{U}}(v) + \|\mathcal{Q}(v)\|$ ;
- (ii) replacing each lift  $e_{\nabla}$  and its doors  $v_p$  and  $v_a$  by a unique arrow with level  $\ell_{\mathbf{U}}^{\mathcal{Q}}(v_p) = \ell_{\mathbf{U}}^{\mathcal{Q}}(v_a)$  connecting the ports to which  $v_p$  and  $v_a$  were connected.

The  $\ell$ -structure in Figure 18 is the read-back of the  $\mathbf{ul}$ -structure in Figure 17.

### 7.10 Proper structures

Using the solutions of a  $\mathbf{ul}$ -structure we are now able to characterize the “proper” unfoldings of  $\mathbf{sl}$ -structures.

**Definition 37 (complete unfolding)** A triple  $M : \mathbf{U} \ll \mathbf{G}$  is a complete unfolding of the  $\mathbf{sl}$ -structure  $\mathbf{G}$ , and  $\mathbf{U}$  is a least-shared-instance of  $\mathbf{G}$ , when:

- (i)  $M : \mathbf{U} \preceq \mathbf{G}$ ;
- (ii)  $\mathbf{U}$  has an  $\mathcal{S}$ -solution for any internal state  $\mathcal{S}$ ;
- (iii) if  $\mathcal{A}$  is a solution of  $\mathbf{U}$ , then  $M(v) = M(v')$  and  $\mathcal{A}(v) = \mathcal{A}(v')$  implies



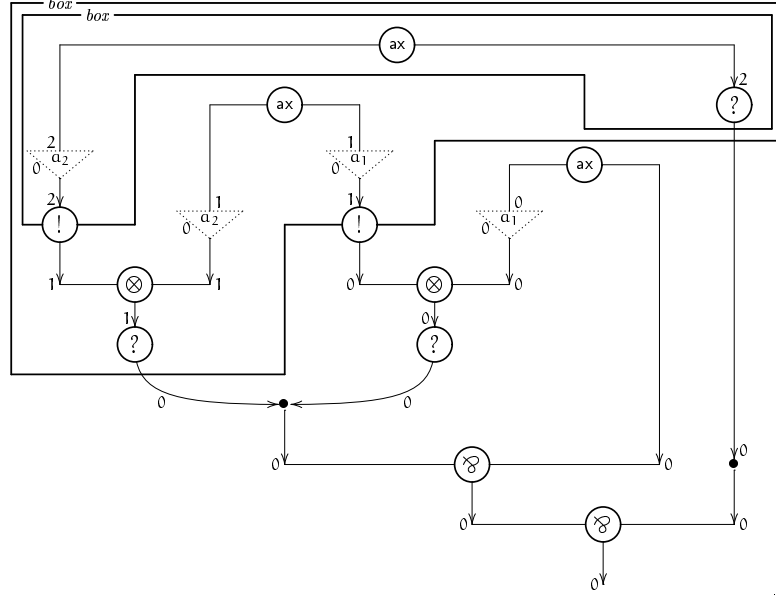


Fig. 18. Read-back.

$v = v'$ , for any  $v, v' \in \text{vtx}(\mathbf{U}) \setminus \bigcup_{e_\gamma \in \text{lnk}_\gamma(\mathbf{U})} \text{adoors}(e_\gamma)$ .

**Remark 38** The reasons because of which in item (iii) we exclude  $v, v' \in \bigcup_{e_\gamma \in \text{lnk}_\gamma(\mathbf{U})} \text{adoors}(e_\gamma)$  are merely technical. The distinction between two contracting arrows s.t.  $\mathcal{A}(v) = \mathcal{A}(v')$  and  $M(v) = M(v')$  is ensured by the fact that the same property cannot hold for the internal doors of the ? links above them.

**Remark 39** To require the existence of a solution for any internal state accords with the idea that the reindexing parameter  $H_!$  of the ! lifts is an independent parameter.

**Definition 40 (proper sl-structure)** An sl-structure is proper when it has a complete unfolding.

**Remark 41** According to Definition 37 the partial order  $\ll$  is flat: a proper l-structure is related only with its least-shared-instance (later we will prove that the least-shared-instance of a proper sl-structure is unique). However, the relation  $\ll$  could be extended defining  $G_1 \ll G_2$ , say  $G_1$  is a *proper unfolding* of  $G_2$ , when  $G_1$  and  $G_2$  are proper sl-structures with the same least-shared-instance and  $G_1 \preceq G_2$ . Besides, in our study we will use only the case in which  $G_1$  is unshared. For this reason, in the following we will use  $\mathbf{U} \ll \mathbf{G}$  to denote that  $\mathbf{U}$  is the least-shared-instance of  $\mathbf{G}$ .

Any ul-structure with an  $\mathcal{S}$ -solution for any internal state  $\mathcal{S}$  is proper, e.g., the ul-structure in Figure 17, and the identity s-morphism is its complete unfolding. An example of proper sl-structure is instead drawn in Figure 19, its least-shared-instance is the ul-structure in Figure 17.

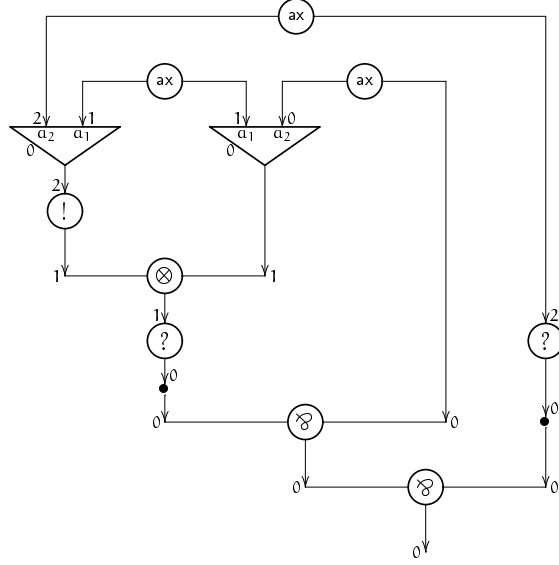


Fig. 19. A proper  $sl$ -structure.

The first step toward the acknowledgment of  $\ll$  as the proper unfolding partial order is the proof that a proper  $sl$ -structure  $G$  has a unique least-shared-instance. Such a result is direct when  $G$  is an  $l$ -structure. The proof for the general case (see Proposition 50) rests instead on the read-back reduction system we are going to introduce in section 8.

## 8 Read-back reductions

Let  $G$  be a proper  $sl$ -structure whose complete unfolding is unique. The natural way to associate an  $l$ -structure to  $G$  is defining the read-back of  $G$  as the read-back of its least-shared-instance.

**Definition 42 (read-back)** *Let  $G$  be a proper  $sl$ -structure with a unique  $U \ll G$ . The read-back of  $G$  is the  $l$ -structure  $\mathcal{R}(G) = \mathcal{R}(U)$ .*

### 8.1 $\pi$ rules

The test bed for the latter definition of semantical read-back is the *read-back rewriting system*, or  $\pi$  rules, of Figure 20 (all the muxes in the picture are binary since the extension to the  $k$ -ary case is direct). Such rules implement the interpretation of lifts that guided us so far, taking into account the additional duplicating task that  $k$ -muxes have w.r.t. lifts. We recognize three sets of rules:

- (i) the *propagations* or duplications (at the top in Figure 20), in which  $\alpha$  stands for a generic type;

- (ii) the *absorption* (at the center in Figure 20);
- (iii) the *mux rules* (at the bottom in Figure 20).

Note that the stem of the arrows have been omitted in the propagation and mux rules of Figure 20. The reason is that the drawings for such rules are indeed schemata valid independently from the orientation of the arrows (*e.g.*, in the  $!/?$ -propagation the principal door of the mux might be the internal door of the box door link).

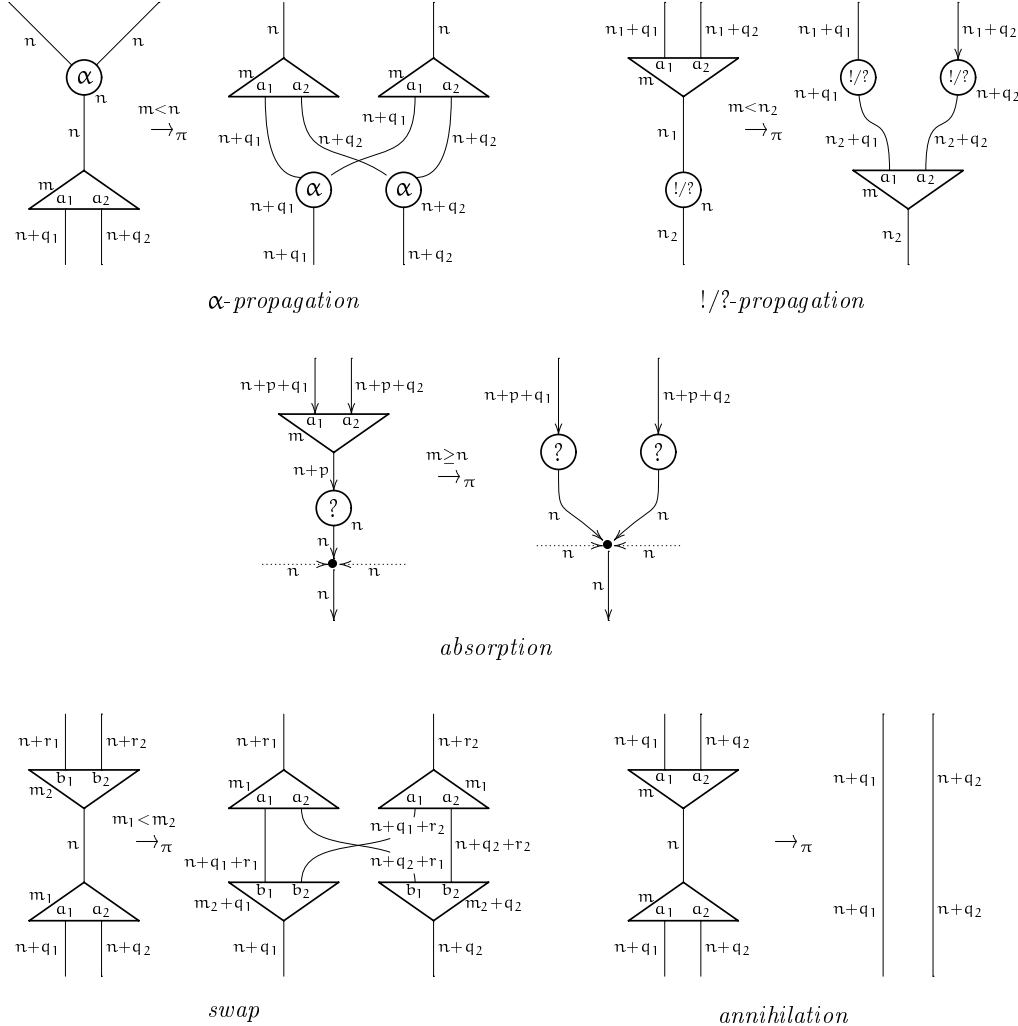


Fig. 20. Read-back rewriting system ( $\pi$  rules).

The *propagation* rules correspond to the idea that a mux has to reindex and duplicate every link it finds at a level greater than its threshold—see the side-condition of the rules. (The proviso  $m < n$  in the  $\alpha$ -propagation rule is actually redundant; by definition,  $\ell(e_{\nabla}) \leq \ell(\text{pdoor}(e_{\nabla}))$  for any mux  $e_{\nabla}$ . Nevertheless, to stress that in an  $\alpha$ -propagation the mux is acting on a link whose level is above its threshold, we preferred to restate it adding the proviso.)

The proviso on the propagation rules forbids their application in the case the principal door of a mux with threshold  $m$  is the internal door of a box door link whose level is lower or equal than  $m$ . The *absorption* rule covers such a case when the box door link  $e_?$  is a  $?$  link—it corresponds to the idea that  $e_?$  is a boundary to the reindexing operators originated inside the boxes that  $e_?$  closes. There is instead no rule for the case in which the box door link  $e_!$  is an  $!$  link—it corresponds to the fact that the reindexing (and duplication) that  $e_!$  may impose do not depend on the contents of its box (see also Remark 33). Hence, the missing  $!$  link case has to be regarded as an unlikely situation—it is not incidentally that this configuration cannot arise in a proper  $s\ell$ -structure, see Fact 47.

The two mux  $\pi$  rules implement the idea that mux thresholds are a machinery to correctly match muxes. The *swap* rule corresponds to the fact that two muxes with different thresholds denote independent reindexing-duplication operators. Therefore, the interaction of a pair of them must preserve all the I/O connections of the pair. At the same time, the interaction must properly lift the threshold  $m_2$  of the higher mux; since a mux with threshold  $m_1$  lifts all the levels above it, and then  $m_2 > m_1$  too. (We stress that, generalizing the rule to the  $k$ -ary case, the interacting muxes may have different cardinalities.) The *annihilation* rule corresponds instead to the matching property described in section 5.2: the only legal paths crossing a pair of facing muxes with the same threshold connect ports with the same name and offset.

The first property to check is that any proper  $s\ell$ -structure  $G$  is rewritten by  $\pi$  reduction into a proper  $s\ell$ -structure. This will be done in Lemma 44 showing at the same time that any  $\pi$  reduction of a proper  $s\ell$ -structure can be simulated on its least-shared-instance. But for this purpose we need the invariance of properness at least when  $G$  is a  $u\ell$ -structure.

**Lemma 43** *If  $U \rightarrow_\pi U'$ , then  $U$  is a proper  $u\ell$ -structure iff  $U'$  is a proper  $u\ell$ -structure.*

**Proof.** Let  $r : U \rightarrow_\pi U'$ . We see that when  $r$  is any  $\pi$  rule but an  $!$ -propagation, the  $u\ell$ -structure  $U$  has an  $\mathcal{S}$ -solution iff  $U'$  has a corresponding  $\mathcal{S}$ -solution. Hence, let  $r = (e_\nabla, e_!)$  be an  $!$ -propagation s.t.  $q$  is the offset of  $e_\nabla$  (assume that a  $\pi$  redex  $r$  is a pair  $(e_\nabla, e)$ , where  $e_\nabla$  is the mux of  $r$  and  $e$  is the link s.t.  $\text{pdoor}(e_\nabla) \in \text{doors}(e)$ ). In this case,  $U$  has an  $\mathcal{S}$ -solution iff  $U'$  has a corresponding  $\mathcal{S}'$ -solution, being  $\mathcal{S}'$  the internal state s.t.  $\mathcal{S}'(e_!) = \mathcal{S}(e_!)^{\uparrow q}$  and  $\mathcal{S}'(e) = \mathcal{S}(e)$  otherwise.  $\square$

The key property of the  $\pi$  rules is the possibility to simulate any  $\pi$  reduction of a proper  $s\ell$ -structure by a  $\pi$  reduction of its least-shared-instance.

**Lemma 44** *Let  $G_0$  be a proper  $sl$ -structure and let  $U_0 \ll G_0$ . For any  $G_0 \rightarrow_\pi G_1$ , there exists  $U_0 \rightarrow_\pi^+ U_1$  s.t.  $U_1 \ll G_1$ .*

**Proof.** Let  $M_0 : U_0 \ll G_0$  and let  $r$  be a redex of  $G_0$ . The counterimage of  $r$  is a set of redexes  $M_0^{-1}(r)$  that may contain only a case of critical pair: two (or more) lifts whose principal doors are contracting doors of the same  $\gamma$  link. When  $r$  is an absorption, any of such critical pairs is trivially confluent. When  $r$  is a ?-propagation, all the lifts in the critical pairs are equal, since  $U_0$  is proper. Thus, it is readily seen that in this case too all the critical pairs are confluent. Hence, let us execute in any order the redexes in the set  $M_0^{-1}(r)$ , closing the critical pairs present in it. The result is the proper  $ul$ -structure  $U_1$  (by Lemma 43,  $U_1$  is definitely proper). The  $s$ -morphism between  $M_1 : U_1 \ll G_1$  is the one induced by the function mapping any residual of  $e \in \text{lnk}(U_0)$  into the corresponding residual of  $M_0(e)$ .  $\square$

Finally, we can show that the definitions of proper  $sl$ -structure and read-back are sound.

**Proposition 45** *Let  $G$  be a proper  $sl$ -structure. For any read-back reduction  $G \rightarrow_\pi G'$ , we have that:*

- (i)  $G'$  is proper;
- (ii)  $G$  has a unique  $U \ll G$  iff there is a unique  $U' \ll G'$ ; in which case
- (iii)  $\mathcal{R}(G) = \mathcal{R}(G')$ .

**Proof.**

- (i) It is an immediate consequence of Lemma 43 and Lemma 44.
- (ii) (if) Let  $U_1 \ll G \gg U_2$ . By Lemma 44,  $U_i \rightarrow_\pi^+ U'$  by a sequence of reductions corresponding to  $G \rightarrow_\pi G'$ . By inspection of the rules, we see that this is possible only if  $U_1 = U_2$ . (only if) Revert the arrows of the  $\pi$  rules and proceed as in the previous case.
- (iii) By inspection of the  $\pi$  rules, we see that  $\mathcal{R}(U) = \mathcal{R}(U')$ .  $\square$

It is worth to note that the invariance of properness under  $\pi$  reduction holds for the  $sl$ -structures too. Such an invariance will be farther exploited to prove that an  $sl$ -structure is proper iff it normalizes to a proper  $l$ -structure.

**Lemma 46** *If  $G \rightarrow_\pi G'$ , then  $G$  is a proper  $sl$ -structure iff  $G'$  is a proper  $sl$ -structure.*

**Proof.** The if part has been already proved. By Lemma 43, the only if part holds when  $G$  is a  $u\ell$ -structure. To lift the result to the  $s\ell$ -structures, let us note that even the simulation property can be reversed. Namely, if  $G \rightarrow_{\pi} G'$  and  $U' \ll G'$ , then there is  $U \ll G$  s.t.  $U \rightarrow_{\pi}^+ U'$ .  $\square$

## 8.2 Local confluence of the $\pi$ rules

Unfortunately, the  $\pi$  rules are not locally confluent in general. Nevertheless, the  $\pi$  rules are strongly normalizing and confluent on proper  $s\ell$ -structures (see Proposition 49). To prove such a result let us start proving that local confluence holds, modulo some mux permutations, for the  $s\ell$ -structures in which each mux eventually interacts with some other link (Lemma 48).

Let  $e_{\nabla}$  be a mux whose principal door is not auxiliary door of another mux. We say that  $e_{\nabla}$  forms a *deadlock* when no  $\pi$  rule can be applied to it.

**Fact 47 (deadlock-freeness)** *An  $s\ell$ -structure  $G$  is deadlock-free when there is no  $G \rightarrow_{\pi}^* G'$  s.t.  $G'$  contains a deadlock. Any proper  $s\ell$ -structure is deadlock-free.*

**Proof.** Let  $U \ll G$ . If  $U$  would contain a deadlock, the constraints of Definition 32 would be unsatisfiable for at least the quiescence internal state. Thus, there is no deadlock in  $U$ . It is indeed easy to verify that the presence of a deadlock in  $G$  would imply the presence of a deadlock in  $U$ . Thus, there is no deadlock in  $G$ , and more generally there is no deadlock in any proper  $s\ell$ -structure.  $\square$

The read-back reduction system has several critical pairs. They can be classified under two patterns: absorption-propagation and propagation-propagation.

The absorption-propagation critical pairs do not cause any problem: they are confluent for any value of the thresholds, offsets, and port names.

The propagation-propagation pairs cause instead the loss of the locally confluence. Nevertheless, let us note that when the  $s\ell$ -structure is proper:

- (i) If the pair is formed of two muxes with the same threshold whose principal doors are doors of the same link, then the two muxes have the same set of port offsets and names, and the critical pair is confluent (for a proper  $s\ell$ -structure is deadlock-free).
- (ii) If the muxes have different thresholds, the result depends on the order in which the two propagation redexes  $r_1$  and  $r_2$  of  $G$  are contracted. But,

there are two reductions  $G \xrightarrow{r_1}_\pi \rightarrow^+_\pi G_1$  and  $G \xrightarrow{r_2}_\pi \rightarrow^+_\pi G_2$  s.t.  $G_1$  and  $G_2$  coincide modulo the permutation of muxes in Figure 21.

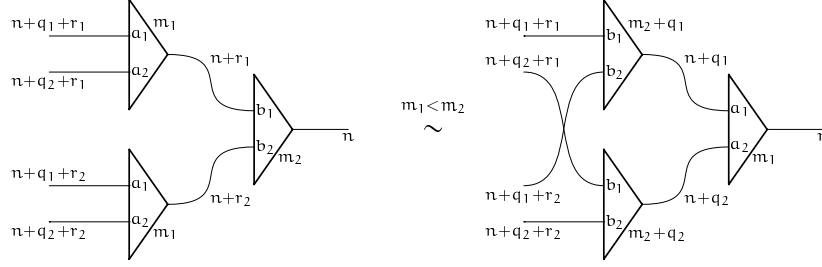
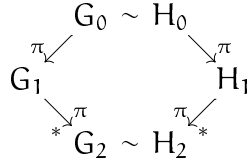


Fig. 21. Mux permutation equivalence.

The *mux permutation equivalence* is the symmetric, transitive and reflexive closure  $\sim$  of the rule in Figure 21.

The equivalence relation  $\sim$  is sound w.r.t. to properness and unfolding. In fact, if  $G_1 \sim G_2$ , then  $U_1 \sim U_2$ , where  $U_i \ll G_i$  for  $i = 1, 2$ .

**Lemma 48** *The proper  $sl$ -structures modulo  $\sim$  are locally confluent under  $\pi$  reduction. Namely, the diagram*



*commutes for any pair of proper  $sl$ -structures  $G_0, H_0$ , and for any pair of  $\pi$  reductions  $G_0 \rightarrow_\pi G_1, H_0 \rightarrow_\pi H_1$ .*

**Sketch of the proof.** The complete proof is a tedious analysis of all the possible configurations. The key idea is that  $G_0$  and  $H_0$  are equal in all respects apart for some substructures  $T[G_0] \sim T[H_0]$  composed of muxes only, that w.l.o.g. we may assume to be trees. When  $r : G_0 \rightarrow_\pi G_1$  and  $s : H_0 \rightarrow_\pi H_1$  does not involve the root mux of any of such trees, the lemma follows from the already remarked property that the diagram commutes when  $G_0 = H_0$ . In the other cases, let  $T[G_0]$  (or mutatis mutandis  $T[H_0]$ ) be the tree whose root mux form the redex  $r$  with a link  $e$ . There is a reduction  $R : G_0 \xrightarrow{*}_\pi G'$  s.t. all the muxes of  $T[G_0]$  have interacted with a residual of  $e$  (assume that, when a residual of  $e$  annihilates interacting with a mux  $e'$  of  $T[G_0]$ , the interaction is complete for all the muxes of  $T[G_0]$  following  $e'$  too). Since  $r$  and  $s$  are the first reductions of the corresponding sequences  $R$  and  $S$ , the property is proved showing that the diagram commutes replacing  $R$  and  $S$  for  $r$  and  $s$ .  $\square$

We stress that the previous lemma could be reformulated replacing proper

$s\ell$ -structures with deadlock-free  $s\ell$ -structures, since deadlock-freeness is the only property of the proper  $s\ell$ -structures that has been used in the proof.

### 8.3 Existence of the read-back

The next two propositions show that the algebraic semantics is indeed a way to talk about the  $\pi$  rules.

**Proposition 49** *Let  $G$  be a proper  $s\ell$ -structure.*

- (i) *There is no infinite  $\pi$  reduction of  $G$ .*
- (ii)  *$G$  has a unique  $\pi$  normal form.*

**Proof.**

- (i) Let  $\mathcal{Q}$  be the quiescence solution of a proper  $u\ell$ -structure  $U$ . Let us take:
  - (a)  $k_1 = \sum_{e \in L} \sum_{v \in \text{doors}(e)} \|\mathcal{Q}(v)\|$ , where  $L = \text{lnk}(U) \setminus \text{lnk}_\nabla(U)$ ;
  - (b)  $k_2 = \sum_{e \in \text{lnk}_\nabla(U)} \|\mathcal{Q}(\text{pdoor}(e))\|$ .
 Any propagation or absorption rule decreases  $k_1$ , but may increase  $k_2$ . Any mux rule decreases  $k_2$ . Therefore, any  $\pi$  rule decreases the pair  $(k_1, k_2)$  (w.r.t. the lexicographic order), and because of this there is no infinite  $\pi$  reduction of  $U$ . To conclude, let us take  $U \ll G$ , it follows by Lemma 44 that there is no infinite  $\pi$  reduction of  $G$  (note that a single  $\pi$  reduction of  $G$  is simulated by a non-empty sequence of  $\pi$  reductions of  $U$ ).
- (ii) The uniqueness of the normal form up to  $\sim$  follows by the previous item, Lemma 48, and Newman's Lemma. Furthermore, a  $\pi$  normal form  $N$  of  $G$  cannot contain muxes, for  $G$  is deadlock-free. Thus,  $N$  is unique.  $\square$

**Proposition 50** *Any proper  $s\ell$ -structure  $G$  has a unique least-shared-instance and  $\mathcal{R}(G)$  is the unique  $\pi$  normal form of  $G$ .*

**Proof.** The unique least-shared-instance of a proper  $\ell$ -structure  $N$  is  $N$  itself: the quiescence solution of  $N$  associates 1 to each arrow of  $N$ ; thus,  $M(v) = M(v')$  iff  $v = v'$  for any  $M : N \ll G$ , etc. As a consequence,  $G$  has a unique least-shared-instance (by Proposition 45) and  $\mathcal{R}(G)$  is defined. By the invariance of the read-back under  $\pi$  reduction (Proposition 45) we can then conclude that  $N = \mathcal{R}(N) = \mathcal{R}(G)$ .  $\square$



## 9 Properness and box nesting property

We will now see that in order to get a proper structure the box nesting property is a necessary requirement. Moreover, we will see that properness is indeed an implicit characterization of the box nesting property.

**Proposition 51** *An  $\ell$ -structure is proper iff it has the box nesting property.*

**Proof.**

(if) Let  $\mathcal{S}$  be an internal state of  $\mathbf{U}$ . Let us take the sequence of internal states  $\mathcal{S} = \mathcal{S}_0, \mathcal{S}_1, \dots, \mathcal{S}_k = \mathcal{S}$  defined in this way:  $\mathcal{S}_i(e_i) = \mathcal{S}(e_i)$  if  $\ell_{\mathbf{U}}(e_i) < i$  and  $\mathcal{S}_i(e_i) = 1$  otherwise. Let  $\mathcal{A}_0$  be the quiescence solution of  $\mathbf{U}$ . For  $i \geq 0$ , we inductively define  $\mathcal{A}_{i+1}(v) = \mathcal{S}(e_i) \mathcal{A}_i(v)$  when  $v \in \text{box}_{\mathbf{U}}^{\ell}(e_i)$  for some  $e_i \in \text{lnk}_i(\mathbf{U})$  s.t.  $\ell_{\mathbf{U}}(e_i) = i$ , and  $\mathcal{A}_{i+1}(v) = \mathcal{A}_i(v)$  otherwise. The previous definition is sound because of the box nesting property, and gives a sequence of assignments  $\mathcal{A}_i$  which are indeed the  $\mathcal{S}_i$ -solution of  $\mathbf{U}$ .

(only if) Let  $e_1, e_2 \in \text{lnk}_i(\mathbf{U})$  be s.t.  $\text{box}_{\mathbf{U}}^{\ell}(e_1) \cap \text{box}_{\mathbf{U}}^{\ell}(e_2) \neq \emptyset$ . W.l.o.g, let  $\ell_{\mathbf{U}}(e_1) \leq \ell_{\mathbf{U}}(e_2)$ . By the definition of  $\text{box}_{\mathbf{U}}^{\ell}$ , we see that  $v \in \text{vtx}(\text{box}_{\mathbf{U}}^{\ell}(e_2))$  implies  $v \in \text{vtx}(\text{box}_{\mathbf{U}}^{\ell}(e_1))$ . Hence,  $\text{box}_{\mathbf{U}}^{\ell}(e_2) \subseteq \text{box}_{\mathbf{U}}^{\ell}(e_1)$ . When  $\ell_{\mathbf{U}}(e_1) = \ell_{\mathbf{U}}(e_2)$ , let  $\mathcal{S}_{12}$  be an internal state of  $\mathbf{U}$  s.t.  $\mathcal{S}_{12}(e) \neq 1$  iff  $e \in \{e_1, e_2\}$ . We see that  $\text{box}_{\mathbf{U}}^{\ell}(e_1) = \text{box}_{\mathbf{U}}^{\ell}(e_2)$ , since  $e_2 \in \text{dlnks}(\text{box}_{\mathbf{U}}^{\ell}(e_1))$ . But in this case,  $\mathbf{U}$  has an  $\mathcal{S}_{12}$ -solution iff  $\mathcal{S}_{12}(e_1) = \mathcal{S}_{12}(e_2)$ . Thus,  $e_1 = e_2$ . When  $\ell_{\mathbf{U}}(e_1) < \ell_{\mathbf{U}}(e_2)$ , we have that  $e_2 \in \text{box}_{\mathbf{U}}^{\ell}(e_1) \setminus \text{dlnks}(\text{box}_{\mathbf{U}}^{\ell}(e_1))$ . Thus,  $\text{box}_{\mathbf{U}}^{\ell}(e_1) \subset \text{box}_{\mathbf{U}}^{\ell}(e_2)$ .  $\square$

The maps  $\text{box}_{\mathbf{U}}^{\ell}$  and  $\text{BX}_{\mathbf{U}}^{\ell}$  can be extended to the case in which  $\mathbf{U}$  is a proper  $\mathbf{u}\ell$ -structure replacing the actual level function  $\ell_{\mathbf{U}}^{\mathcal{S}}$  for  $\ell_{\mathbf{U}}$  in Definition 11 (this is sound even if  $\mathbf{U}$  is an  $\ell$ -structure, as in such a case  $\ell_{\mathbf{U}}^{\mathcal{S}} = \ell_{\mathbf{U}}$ ). Hence, we have a notion of box nesting property for the  $\mathbf{u}\ell$ -structures too. A relevant consequence of the results on the  $\pi$  reductions is that properness and box nesting property are equivalent also in this case (note that to define  $\ell_{\mathbf{U}}^{\mathcal{S}}$  and then  $\mathcal{R}(\mathbf{U})$  we suffice the existence of the quiescence solution of  $\mathbf{U}$ ).

**Proposition 52** *A  $\mathbf{u}\ell$ -structure is proper iff it has a quiescence solution and the box nesting property.*

**Proof.**

(only if) By definition any proper  $\mathbf{u}\ell$ -structure  $\mathbf{U}$  has a quiescence solution. The rest of the proof is by induction on the length of a normalizing  $\pi$  reduction of  $\mathbf{U}$ . The base case is proved by Proposition 51. For the induction

step, let  $\mathbf{U} \rightarrow_{\pi} \mathbf{U}'$ . There is a one-to-one correspondence  $f$  between the links of  $\mathbf{U}$  which are not lifts and the links of  $\mathbf{U}'$  which are not lifts. By inspection of the  $\pi$  rules, we see that  $f(e) \in \text{box}_{\mathbf{U}'}^{\ell}(f(e_!))$  iff  $e \in \text{box}_{\mathbf{U}}^{\ell}(e_!)$ , for any  $e \in \text{lnk}(\mathbf{U}) \setminus \text{lnk}_{\nabla}(\mathbf{U})$  and any  $e_! \in \text{lnk}_!(\mathbf{U})$ . Thus,  $\mathbf{U}$  has the box nesting property iff  $\mathbf{U}'$  has the box nesting property.

(if) By inspection of the proof of Lemma 49, we see that the existence of a quiescence solution suffices to prove that the  $\pi$  rules are strongly normalizing and that the unique  $\pi$  normal form of  $\mathbf{U}$  is  $\mathcal{R}(\mathbf{U})$ . Proving the only if part, we have already seen that the box nesting property is invariant under  $\pi$  reduction. Thus,  $\mathcal{R}(\mathbf{U})$  has the box nesting property, and by Proposition 51 it is proper. That  $\mathbf{U}$  is proper follows by the invariance of properness under  $\pi$  reduction (Lemma 46).  $\square$

**Proposition 53** *An  $\text{s}\ell$ -structure  $\mathbf{G}$  is proper iff its  $\pi$  normal form is an  $\ell$ -structure for which the box nesting property holds.*

**Proof.** By the invariance of properness (Lemma 46), an  $\text{s}\ell$ -structure is proper iff its  $\pi$  normal form  $\mathbf{N}$  is. Since all the proper  $\text{s}\ell$ -structures are deadlock-free (Fact 47), the proper  $\ell$ -structures are the only proper  $\text{s}\ell$ -structures in  $\pi$  normal form. By Proposition 51, we conclude that  $\mathbf{G}$  is proper iff  $\mathbf{N}$  has the box nesting property.  $\square$

The latter propositions have two relevant consequences that will be used in the following. The first one is that, for any internal state of a  $\text{u}\ell$ -structure  $\mathbf{U}$  and any  $e_! \in \text{lnk}_!(\mathbf{U})$ , the level its solution  $\mathcal{A}$  assigns to  $\text{idoor}(e_!)$  is lower or equal than the level that  $\mathcal{A}$  assigns to any arrow contained in  $\text{box}_{\mathbf{U}}^{\ell}(e_!)$  (as for the actual levels induced by the quiescence solution, we say that  $\mathcal{A}$  assigns the level  $\ell_{\mathbf{U}}^{\mathcal{A}} = \ell_{\mathbf{U}}(v) + \|\mathcal{A}(v)\|$  to the arrow  $v$ ), and is strictly greater when we assume that the internal state does not force a decreasing of  $-1$ .

**Lemma 54** *Let  $\mathbf{U}$  be a proper  $\text{u}\ell$ -structure. For any  $v \in \text{vtx}(\mathbf{U})$  and any  $e \in \text{lnk}_!(\mathbf{U})$ , we have that:*

- (i)  $v \in \text{box}_{\mathbf{U}}^{\ell}(e)$  iff  $v$  is connected to  $e$  and  $\ell_{\mathbf{U}}^{\mathcal{A}}(v) \geq \ell_{\mathbf{U}}^{\mathcal{A}}(\text{idoor}(e))$ , for any  $\mathcal{S}$ -solution  $\mathcal{A}$ ;
- (ii)  $v \in \text{box}_{\mathbf{U}}^{\ell}(e)$  iff  $v$  is connected to  $e$  and  $\ell_{\mathbf{U}}^{\mathcal{A}}(v) > \ell_{\mathbf{U}}^{\mathcal{A}}(\text{idoor}(e))$  for any  $\mathcal{S}$ -solution  $\mathcal{A}$  s.t.  $\|\mathcal{S}(e)\| \geq 0$ .

**Proof.** By induction on the length of a normalizing  $\pi$  reduction. The statement of the lemma is invariant under  $\pi$  reduction (see the proof of Lemma 43). The if part of the proof of Proposition 51 gives a way to build a generic solution of an  $\ell$ -structure. By inspection of this construction we see that the lemma holds for any  $\ell$ -structure.  $\square$

The second consequence is that any  $\mathcal{S}$ -solution of a  $\mathbf{ul}$ -structure  $\mathbf{U}$  can be built starting from the quiescence solution of  $\mathbf{U}$ , following a procedure similar to the one used for the  $\ell$ -structures in the proof of Proposition 51.

**Lemma 55** *Let  $\mathcal{A}$  be an  $\mathcal{S}$ -solution of a proper  $\mathbf{ul}$ -structure  $\mathbf{U}$ . For any  $e_0 \in \text{Ink}_!(\mathbf{U})$ , we have that*

$$\mathcal{A}(v) = \begin{cases} \mathcal{A}_0(v) \mathcal{S}(e_0)^{\uparrow q_0} & \text{when } v \in \text{vtx}(\text{box}_{\mathbf{U}}^{\ell}(e_0)) \setminus \text{doors}(\text{box}_{\mathbf{U}}^{\ell}(e_0)) \\ \mathcal{A}_0(v) & \text{otherwise} \end{cases}$$

where  $q_0 = \|\mathcal{A}_0(\text{edoor}(e_0))\|$  and  $\mathcal{A}_0$  is the  $\mathcal{S}_0$ -solution of  $\mathbf{U}$ , being

$$\mathcal{S}_0(e) = \begin{cases} 1 & \text{when } e = e_0 \\ \mathcal{S}(e) & \text{otherwise} \end{cases}.$$

**Proof.** Let  $\mathcal{A}_0$  be the  $\mathcal{S}_0$ -solution. We start proving that the  $\mathcal{A}$  defined in the statement is an  $\mathcal{S}$ -assignment. The lifting assignment constraints hold by hypothesis for  $\mathbf{U} \setminus \text{box}_{\mathbf{U}}^{\ell}(e_0)$ . Furthermore, for any  $v \in \text{vtx}(\text{box}_{\mathbf{U}}^{\ell}(e_0))$ , we have that  $\mathcal{A}(v) = \mathcal{A}_0(v) \mathcal{S}(e_0)^{\uparrow q_0} \in \text{LSeq}[0, \ell_{\mathbf{U}}(v)]$ , since  $\ell_{\mathbf{U}}(v) + \|\mathcal{A}_0(v)\| \geq \ell_{\mathbf{U}}(e_0) + 1 + q_0$  (by Lemma 54) and  $\mathcal{S}(e_0) \in \text{LSeq}[\ell_{\mathbf{U}}(e_0), \ell_{\mathbf{U}}(e_0) + 1]$ . So, we left to verify if the lifting assignment constraints hold for any  $e \in \text{box}_{\mathbf{U}}^{\ell}(e_0)$ . The only relevant case is  $e \in \text{adoors}(\text{box}_{\mathbf{U}}^{\ell}(e_0))$ . Let  $v_i = \text{idoor}(e)$  and  $v_e = \text{edoor}(e)$ . By Lemma 54, we know that  $\ell_{\mathbf{U}}(v_e) + \|\mathcal{A}_0(v_e)\| \leq \ell(e_0) + 1 + q_0$ . Thus,  $\mathcal{A}_0(v_i) \mathcal{S}(e_0)^{\uparrow q_0} = \mathcal{H} \mathcal{S}(e_0)^{\uparrow q_0 + \|\mathcal{A}_0(v_e)\|} \mathcal{A}_0(v_e)$ , with  $\mathcal{H} \mathcal{S}(e_0)^{\uparrow q_0 + \|\mathcal{A}_0(v_e)\|} \in \text{LSeq}[\ell_{\mathbf{U}}(v_e), \ell_{\mathbf{U}}(v_i)]$ . It is then easy to check that if  $\mathcal{A}$  would not be the  $\mathcal{S}$ -solution of  $\mathbf{U}$ , then  $\mathcal{A}_0$  would not be the  $\mathcal{S}_0$ -solution of  $\mathbf{U}$ .  $\square$

## 10 Sharing graph machine

We are now ready to prove that the  $s\ell$ -structures plus the  $\pi$  rules are the abstract machine by which to obtain sharing implementations of calculi whose rewriting rules are similar to the  $\beta$ -rule of the  $\lambda$ -calculus or to the exponential cut-elimination rule of linear logic.

### 10.1 Box reductions

An example of the box reduction rules we want to implement is drawn in Figure 22. The example depicts the (pure) proof net case. But, independently

from the calculus under analysis, there is a general pattern characterizing the global or  $\beta$  rewriting rules for which we can give a sharing implementation.

- (i) In a  $\beta$  rule, the principal door link  $e_!$  of a box  $B_1$  interacts with several ? box door links  $e_?^1, e_?^2, \dots$  which may either be auxiliary doors of a box  $B_2$  or not, respectively when  $\ell(\text{idoor}(e_?)) > \ell(\text{edoor}(e_?))$  or  $\ell(\text{idoor}(e_?)) = \ell(\text{edoor}(e_?))$ .
- (ii) None of the ? links  $e_?^i$  is auxiliary door of  $\text{box}^\ell(e_!)$ .
- (iii) The result of the interaction is a new structure in which:
  - (a) there is an instance  $B_1^i$  of  $B_1$  for each  $e_?^i$ ;
  - (b) the box around each  $B_1^i$  is removed (*i.e.*, its ! link is erased) and  $B_1^i$  is pushed inside the boxes of which  $e_?^i$  is auxiliary door;
  - (c) each  $e_?^i$  is erased and its internal door is connected to  $\text{pdoor}(e_!)^i$  (*i.e.*, the copy of  $\text{pdoor}(e_!)$  in  $B_1^i$ );
  - (d) each  $\gamma$  link  $e_\gamma$  which was below a door  $v$  of  $B_1$  is transformed into a  $\gamma$  link  $\bar{e}_\gamma$  s.t.  $\text{pdoor}(\bar{e}_\gamma) = \text{pdoor}(e_\gamma)$ , and  $\text{adoors}(\bar{e}_\gamma) = (\text{adoors}(e_\gamma) \setminus \text{adoors}(B_1)) \cup \{v^i \mid v \in \text{adoors}(e_\gamma) \cap \text{adoors}(B_1)\}$ .

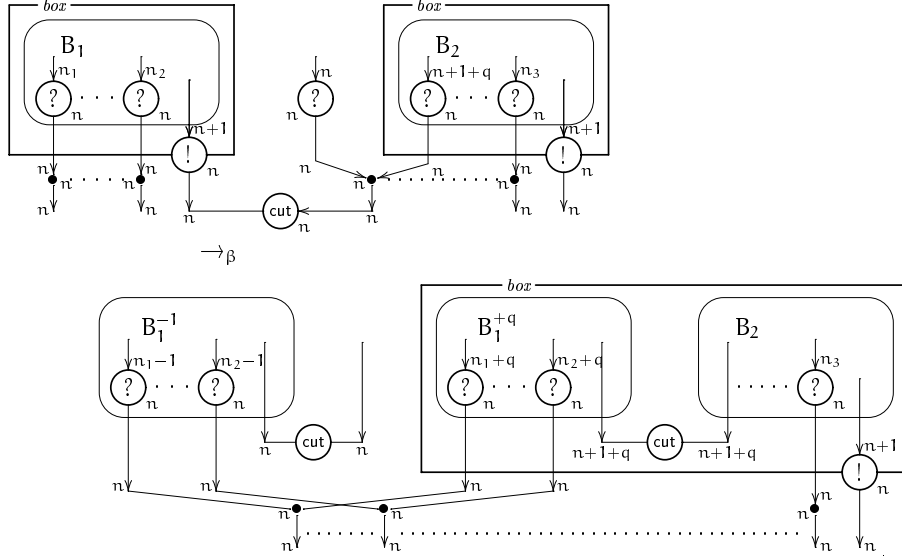


Fig. 22. Interacting boxes: global ( $\beta$ ) rule.

**Remark 56** The extent of the previous pattern is not completely defined. For instance, to faithfully reproduce the schema of Figure 22 in the  $\lambda$ -calculus structures, we should split the usual beta rule in two interactions (*i.e.*, we should switch from  $\lambda$ -terms to pure proof nets): a first one merging the top arrow of the redex to the body arrow of the  $\lambda$  link; a second one that, apart for the cut link, would be an instance of the rule of Figure 22. We could instead prefer to maintain the usual formulation of beta rule assuming that it is a valid instance of the pattern in Figure 22. In fact, all the theorems we are going to give in the next sections are still valid under this assumption. Besides, in order to not be worthlessly abstract, we will refer to the schema of Figure 22 when it will help in exposing the proofs of the forthcoming theorems.

**Remark 57** In terms of levels, to push  $B_1^i$  inside the boxes of  $e_?^i$  means to increase all the levels in  $B_1^i$  by the offset  $q_i = \ell(\text{idoor}(e_?^i)) - \ell(\text{edoor}(e_?^i)) - 1$  (operation denoted by the superscript  $+q_i$  in Figure 22). By the restriction on the levels of a structures, we see that this effectively means to enclose  $B_1^i$  inside  $q_i + 1 > 0$  new boxes when  $e_?^i$  is auxiliary door of some boxes (see  $B_1^{+q}$  in Figure 22); otherwise, when  $e_?^i$  did not close any box, it just means to remove the box around  $B_1^i$  (see  $B_1^{-1}$  in Figure 22).

### 10.2 Sharing $\beta$ rule

Our aim is to show that, just assuming that the  $\ell$ -nets of the calculus to be implemented have the box nesting property, the global version of the  $\beta$  rule in Figure 22 may be replaced by the sharing or  $\beta_s$  rule of Figure 23.

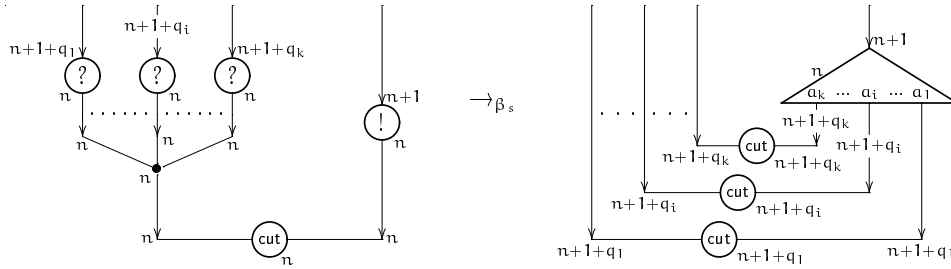


Fig. 23. Interacting boxes: shared ( $\beta_s$ ) rule.

In more details, we want to prove that, applying with no restriction the  $\beta_s \cup \pi$  rules to an  $\ell$ -net  $N$ , we obtain a proper  $sl$ -structure  $G$  whose read-back  $\mathcal{R}(G)$  is a  $\beta$  reduct of  $N$ .

Before to go on with this plan, let us note that the  $\beta$  rule is sound w.r.t. the box nesting property.

**Fact 58** *The box nesting property is stable under  $\beta$  reduction.*

### 10.3 Unshared reductions

The last step towards the achievement of our goal is the proof of a simulation property for  $\beta_s$  similar to the one already proved for  $\pi$  (Lemma 44). Unfortunately, in the case of  $\beta_s$  the procedure is not so direct as in the case of the  $\pi$  rules, since the  $\beta_s$ -reduct of a  $ul$ -structure is a  $ul$ -structure only when the contracted redex is unary (*i.e.*, it involves only one  $?$  link).

There is however a way to transform a  $k$ -ary  $\beta$  redex into a set of unary ones. In fact, let us split the reduction of a  $\beta$  redex  $r = (e_1, \{e_?^1, \dots, e_?^k\})$  in two steps (assume to represent a  $\beta$  redex by the pair formed of the box door links

involved in it): firstly, the box  $B_1$  of the  $!$  link  $e_i$  is duplicated, creating an instance  $B_1^i$  for each  $e_i^i$  (see Figure 24); secondly, each of the  $k$  redexes  $(e_i, \{e_i^i\})$  is reduced applying the  $\beta$  rule. The first step of this decomposition defines a *box duplication* rule transforming a  $k$ -ary  $\beta$  redex into  $k$  unary redexes.

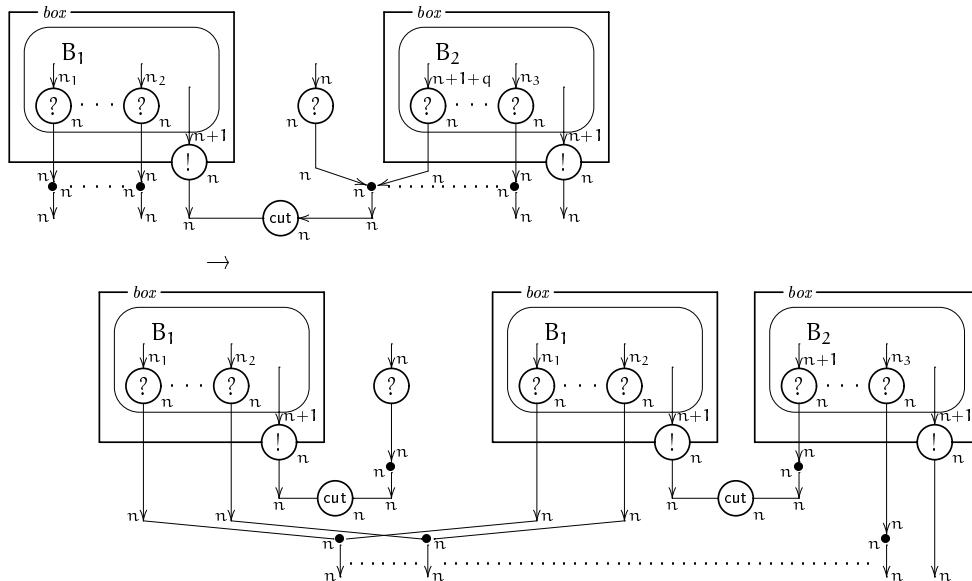


Fig. 24. Interacting boxes: box duplication.

The previous decomposition can be exploited to define the unshared version of the  $\beta$  rule we need, the  $\beta_u$  rule, just replacing  $\beta_s$  for  $\beta$  in the second step.

The  $\beta$  rule can be applied to  $\ell$ -structures only. Exploiting the definition of boxes we gave for the proper  $u\ell$ -structures (see section 9), the  $\beta_u$  rule can be instead used to reduce proper  $u\ell$ -structures—this is not possible for  $\beta$  as we would be in trouble reindexing the thresholds of the lifts. This approach is sound. In fact, the  $\beta_u$ -reduct of a proper  $u\ell$ -structure  $U$  is a proper  $u\ell$ -structure and its read-back is the correct  $\beta$ -reduct of  $\mathcal{R}(U)$ .

**Lemma 59** *Let  $U$  be a proper  $u\ell$ -structure. If  $U \rightarrow_{\beta_u} U'$ , then  $U'$  is proper and  $\mathcal{R}(U) \rightarrow_{\beta} \mathcal{R}(U)'$ .*

**Proof.** Let  $U \rightarrow U''$  be a box duplication (see Figure 24). Each arrow/link of  $U''$  is a residual of a unique arrow/link of  $U$ . More formally, the residual relation  $\mapsto_C \text{vtx}(U) \times \text{vtx}(U'')$  is defined by: (i)  $v \mapsto v^i$ , if  $v \in \text{vtx}(B_1)$  and  $v^i$  is the  $i$ -th instance of  $v$ ; (ii)  $\text{pdoor}(e_\gamma) \mapsto \text{pdoor}(e_\gamma)^i$ , if  $e_\gamma$  is the contraction link of the redex; (iii)  $v \mapsto v$ , if  $v$  is not one of the previous cases. And analogously for  $\mapsto_C \text{lnk}(U) \times \text{lnk}(U'')$ .

Let  $r : U \rightarrow_{\beta_u} U'$ , with  $r = (e_i, \{e_i^1, \dots, e_i^k\})$ . For any internal state  $\mathcal{S}$  of  $U''$ , let us take  $\mathcal{S}^i(e) = \mathcal{S}(e^i)$ , when  $e \in B_1$  and  $e \mapsto e^i$ , and  $\mathcal{S}^i(e) = \mathcal{S}(e)$  otherwise, for  $i = 1, \dots, k$ . Let  $\mathcal{S}^i$  be the  $\mathcal{S}^i$ -solution of  $U$ . By Lemma 55, all

the  $\mathcal{A}^i$  coincide outside  $B_1$ , and in particular,  $\mathcal{A}^1(v) = \mathcal{A}^2(v) = \dots \stackrel{def}{=} \mathcal{A}(v)$ , when  $v \mapsto v$ . The previous map  $\mathcal{A}(v)$  can then be extended to the whole  $\text{vtx}(\mathbf{U}'')$  defining  $\mathcal{A}(v) = \mathcal{A}^i(v^i)$ , when  $v \in B_1$  and  $v \mapsto v^i$ . The map  $\mathcal{A}$  is the  $\mathcal{S}$ -solution of  $\mathbf{U}''$ .

Let  $q_i = \ell_{\mathbf{U}}(\text{idoor}(e_i^\dagger)) - \ell_{\mathbf{U}}(e_i) - 1$  and let  $a_i$  be the name chosen for the lift inserted reducing the  $i$ -th  $\beta$  redex of  $\mathbf{U}''$ . For any internal state  $\mathcal{S}'$  of  $\mathbf{U}''$ , let us take the internal states  $\mathcal{S}_0(e)$  and  $\mathcal{S}(e)$  s.t.: (i)  $\mathcal{S}_0(e) = \mathcal{S}(e) = \mathcal{S}'(e)$ , when  $e \neq e_i^\dagger$ ; (ii)  $\mathcal{S}_0(e_i^\dagger) = 1$ ; (iii)  $\mathcal{S}(e_i^\dagger) = \mathcal{L}[\ell(e_i), q_i, a_i] \mathcal{H}^i$ , being  $\mathcal{A}_0$  the  $\mathcal{S}_0$ -solution of  $\mathbf{U}$  (actually  $\mathcal{S}_0$  is an internal state of  $\mathbf{U}''$ , but we use the same name for the internal state of  $\mathbf{U}$  obtained replacing  $\mathcal{S}_0(e_i) = 1$  for  $\mathcal{S}_0(e_i^\dagger) = 1$ ) and  $\mathcal{H}^i \mathcal{A}_0(\text{edoor}(e_i^\dagger)) = \mathcal{A}_0(\text{idoor}(e_i^\dagger))$ . By Lemma 55 and by the result of the previous paragraph, the  $\mathcal{S}$ -solution of  $\mathbf{U}''$  is s.t.  $\mathcal{A}(v) = \mathcal{A}_0(v)$  when  $v \notin B_1$ , and  $\mathcal{A}(v^i) = \mathcal{A}_0(v) \mathcal{S}(e_i^\dagger)^r$  when  $v \in B_1$  and  $v \mapsto v^i$ , where  $r = \|\mathcal{A}_0(\text{pdoor}(e_i))\|$ .

The map  $\mathcal{A}'$  obtained restricting  $\mathcal{A}$  to  $\text{vtx}(\mathbf{U}'') \cap \text{vtx}(\mathbf{U}')$  (since all the  $\beta$  reduced redexes are unary each arrow of  $\mathbf{U}''$  has at most one residual) is the  $\mathcal{S}'$ -solution of  $\mathbf{U}'$ . Thus,  $\mathbf{U}'$  is proper, for the previous procedure applies to any  $\mathcal{S}'$ .

The way in which  $\mathcal{A}'$  is constructed shows that (take  $\mathcal{S}'$  equal to the quiescence internal state)  $\ell_{\mathbf{U}'}^{\mathcal{S}'}(v) = \ell_{\mathbf{U}}^{\mathcal{S}'}(v)$  when  $v \notin B_1$ , and  $\ell_{\mathbf{U}'}^{\mathcal{S}'}(v) + q_i = \ell_{\mathbf{U}}^{\mathcal{S}'}(v^i)$  when  $v \in B_1$  and  $v \mapsto v^i$ . That is,  $\mathcal{R}(\mathbf{U}) \rightarrow_{\beta} \mathcal{R}(\mathbf{U}')$ .  $\square$

**Proposition 60** *Let  $G_0$  be a proper  $\mathfrak{sl}$ -structure and let  $\mathbf{U}_0 \ll G_0$ . For any  $G_0 \rightarrow_{\beta_s} G_1$ , there exists  $\mathbf{U}_0 \rightarrow_{\beta_u}^+ \mathbf{U}_1$  s.t.  $\mathbf{U}_1 \ll G_1$ .*

**Proof.** Let  $r : G_0 \rightarrow_{\beta_s} G_1$ . The corresponding reduction  $\rho : \mathbf{U}_0 \rightarrow_{\beta_u}^+ \mathbf{U}_1$  we are looking for is a development of the redexes  $R = M_0^{-1}(r)$ , with  $M_0 : \mathbf{U}_0 \ll G_0$ . The complete unfolding  $M_1 : \mathbf{U}_1 \ll G_1$  is instead the natural one induced by the residual relation.

Before to give all the details of the proof let us note that the residual relation defined in the proof of Lemma 59 can be defined for any rule and extended to redexes in the natural way. We already remarked that  $\mapsto$  is a partial one-to-many relation in the case of box duplication (*i.e.*,  $y \mapsto x$  and  $z \mapsto x$  only if  $y = z$ ). In the case of  $\beta_s$  it is indeed an injective partial function.

Let  $r : \mathbf{U}_0 \rightarrow \mathbf{U}'$  by a box duplication. For any  $v' \in \text{vtx}(\mathbf{U}')$ , let us define  $M'(v') = M(v)$  if  $v \mapsto v'$ , and analogously for the links. Though  $M' : \mathbf{U}' \ll G_0$ , the proper  $\mathfrak{ul}$ -structure  $\mathbf{U}'$  is not a least-shared-instance of  $G_0$ . In fact, for any internal state  $\mathcal{S}$  s.t.  $\mathcal{S}(e_1^\dagger) = \mathcal{S}(e_2^\dagger)$  with  $e_1 \mapsto e_1^\dagger$  and  $e_1 \neq e_2$ , if  $v \mapsto v^i$  and  $v^i \in \text{box}_{\mathbf{U}'}^{\ell}(e_1^\dagger)$ , then  $M'(v^1) = M'(v^2)$  and  $\mathcal{A}(v^1) = \mathcal{A}(v^2)$ , where  $\mathcal{A}$

is the  $\mathcal{S}$ -solution of  $\mathbf{U}'$ . Nevertheless, we stress that such internal states are the only ones for which we can simultaneously have  $M'(v^1) = M'(v^2)$  and  $\mathcal{A}(v^1) = \mathcal{A}(v^2)$ , for some  $v^1 \neq v^2$  that are not contracting arrows of a  $\vee$  link.

A finite development of box duplications relative to the set  $\mathbf{R}$  is a sequence of box duplication rules  $\rho' : \mathbf{U}_0 \rightarrow^* \mathbf{U}'$  s.t.  $\mathbf{U}'$  does not contain any residual of  $r \in \mathbf{R}$  and, for any  $r' \in \rho'$ , there is  $r \in \mathbf{R}$  s.t.  $r \mapsto r'$ . Finite developments of box duplications definitely exist (actually all the developments are finite); for instance, reduce the redexes of  $\mathbf{R}$  using an innermost-outermost strategy. We have already seen that  $M' : \mathbf{U}' \preceq \mathbf{G}_0$ . Furthermore, any redex  $r'$  of  $\mathbf{U}'$  s.t.  $M'(r') = r$  is unary. Hence, let  $\alpha_i$  be the name of the lift port to which  $\text{idoor}(e_i^i)$  is connected in  $\mathbf{G}_1$ ; and let  $\rho'' : \mathbf{U}' \rightarrow_{\beta_s} \mathbf{U}_1$  be obtained reducing all the (unary) redexes  $\mathbf{R}' = \{(e_i', \{e_j'\}) \mid M'(e_i') = e_i, M'(e_j') = e_j^i\}$ , and inserting a lift with port name  $\alpha_i$  for each redex  $(e_i', \{e_j'\})$  s.t.  $M'(e_j') = e_j^i$ , for  $i = 1, 2, \dots$ . By construction, there is  $M_1 : \mathbf{U}_1 \preceq \mathbf{G}_1$  (the natural one induced by the restriction of  $M'$  to the arrows of  $\mathbf{U}_1$ ). Moreover, the way in which names are assigned to lifts, plus the concluding remark of the previous paragraph, allows to state that  $M_1 : \mathbf{U}_1 \preceq \mathbf{G}_1$ .

To conclude the proof, just note that  $\mathbf{U}_0 \xrightarrow{\rho'} \mathbf{U}' \xrightarrow{\rho''} \mathbf{U}_1$  can be easily rearranged in a sequence of  $\beta_u$  reductions.  $\square$

## 11 Sharing reductions of nets

We now have all the ingredients we need to state our main results:

- (i) We know how to semantically and syntactically read-back an  $\ell$ -structure from a proper  $s\ell$ -structure—remind that  $\mathbf{G} \rightarrow_{\pi}^* \mathcal{R}(\mathbf{G})$  (see Proposition 50).
- (ii) We know that properness and box nesting property are tightly related (see section 9).
- (iii) We know that  $\beta_s$  and  $\pi$  reductions of proper  $s\ell$ -structures can be simulated by corresponding reductions of proper  $u\ell$ -structures (see Lemma 44 and Proposition 60).
- (iv) We know that  $\beta_s$  and  $\pi$  are sound w.r.t. read-back.

In other words, the rules  $\beta_s$  and  $\pi$  define an abstract computational system, say a *sharing graph machine*, in which the implementation of the global rule  $\beta$  is decomposed in a set of atomic steps.

In order to formalize the latter claim by a theorem, let us introduce some final notations.



## 11.1 Nets

Let  $\mathbf{N}$  be an  $\ell$ -structure over the signature  $\Sigma \uplus \{!, ?, \gamma\}$ . We say that  $\mathbf{N}$  is *correct*, or that it is a  $\Sigma\ell$ -net, when it satisfies some given correctness criterion that subsumes the box nesting property.

A proper  $s\ell$ -structure over  $\Sigma \uplus \{!, ?, \gamma\}$  is *correct*, say a  $\Sigma s\ell$ -net, when its read-back is a  $\Sigma\ell$ -net. Further, given a set  $\mathbf{Nets}$  of correct  $\ell$ -nets, we will say that a set  $\mathbf{SNets}$  of proper  $s\ell$ -structures over  $\Sigma$  is the set of the correct  $s\ell$ -nets corresponding to  $\mathbf{Nets}$ , when  $G \in \mathbf{SNets}$  iff  $\mathcal{R}(G) \in \mathbf{Nets}$ .

## 11.2 Net rewriting systems

A *net rewriting system* over the signature  $\Sigma$  (let us implicitly assume that  $\{!, ?, \gamma\} \subset \Sigma$ ) is a triple  $\sigma = (\Sigma, \mathbf{Nets}(\sigma), \rightarrow_\sigma)$ , where  $\mathbf{Nets}(\sigma)$  is some set of  $\Sigma\ell$ -nets, and  $\rightarrow_\sigma \subset \mathbf{Nets}(\sigma) \times \mathbf{Nets}(\sigma)$  is the congruence induced by some set of graph rewriting rules. Moreover,  $\beta \in \sigma$  is the only rule involving box door links and contractions.

**Definition 61 (sharing implementation)** *The sharing implementation of a net rewriting system  $\sigma = (\Sigma, \mathbf{Nets}(\sigma), \rightarrow_\beta \cup \rightarrow_\gamma)$  is the (sharing) net rewriting system  $\sigma_s = (\Sigma \uplus \{\nabla\}, \mathbf{SNets}(\sigma), \rightarrow_{\beta_s} \cup \rightarrow_\pi \cup \rightarrow_\gamma)$ , where  $\mathbf{SNets}(\sigma)$  is the set of  $\Sigma s\ell$ -nets corresponding to  $\mathbf{Nets}(\sigma)$ .*

**Theorem 62** *Let  $\sigma_s$  be the sharing implementation of the net rewriting system  $\sigma = (\Sigma, \mathbf{Nets}(\sigma), \rightarrow_\beta \cup \rightarrow_\gamma)$ . We have that:*

- (i) *for any  $\sigma_s$  reduction  $G \rightarrow_{\sigma_s}^* G'$ , there is a corresponding  $\sigma$  reduction  $\mathcal{R}(G) \rightarrow_\sigma^* \mathcal{R}(G')$ ;*
- (ii) *for any  $\sigma$  reduction  $N \rightarrow_\sigma^* N'$ , there is a corresponding  $\sigma_s$  reduction  $N \rightarrow_{\sigma_s}^* N'$ ;*
- (iii)  *$\sigma_s$  is strongly normalizing iff  $\sigma$  is;*
- (iv)  *$\sigma_s$  is confluent iff  $\sigma$  is;*
- (v) *any  $\sigma_s$  normal form of  $G \in \mathbf{SNets}(\sigma)$  is a  $\sigma$  normal form of  $\mathcal{R}(G)$ .*

**Proof.**

- (i) Since it is trivial to see that  $G \rightarrow_\gamma G'$  implies  $\mathcal{R}(G) \rightarrow_\gamma \mathcal{R}(G')$ , it follows immediately from Lemma 44 and Proposition 60.
- (ii) A *standard strategy* for  $\sigma_s$  is a reduction strategy s.t. each  $\beta_s$  is followed by a normalizing  $\pi$  reduction. By Proposition 50 and Proposition 60, we see that  $r : N \rightarrow_\beta N''$  implies  $\rho : N \xrightarrow{r}_{\beta_s} G \rightarrow_\pi^* \mathcal{R}(G) = N''$ , where  $\rho$  is

standard. Thus, for any  $N \rightarrow_{\sigma}^* N'$  there is a standard strategy reduction s.t.  $N \rightarrow_{\sigma_s}^* N'$ .

- (iii) The  $\pi$  rules are strongly normalizing (Proposition 49). Hence, a  $\sigma_s$  reduction  $\rho_s$  contains an infinite number of them iff it contains an infinite number of rules  $\beta_s$  or  $\gamma$ . As a consequence, any infinite  $\sigma_s$  reduction  $\rho_s$  contains an infinite number of rules  $\beta_s$  or  $\gamma$ . But this is possible iff the  $\sigma$  reduction  $\rho$  corresponding to  $\rho_s$  is infinite.
- (iv) Trivial.
- (v) Remind that the  $\pi$  normal form of a proper  $s\ell$ -structure  $G$  is equal to  $\mathcal{R}(G)$  (see Proposition 49).  $\square$

### 11.3 Optimality

Sharing implementations are tightly related to  $\lambda$ -calculus optimal reductions [Lév80] and to their generalization to Interaction Systems [AL94]. Anyhow, because of the generality of the rewriting system  $\sigma$ , such a correspondence is restricted to the implementation of the  $\beta$  rule only.

Let us assume the hypotheses and notations of Theorem 62. A  $\sigma_s$  reduction  $\rho_s : G \rightarrow_{\sigma_s} G'$  is  *$\beta$ -optimal* when the number of  $\beta_s$  rules executed by  $\rho_s$  is lower or equal than the number of  $\beta_s$  rules applied by any other equivalent reduction  $\rho'_s : G \rightarrow_{\sigma_s} G'$  (*i.e.*,  $\rho_s$  can be obtained from  $\rho'_s$  applying the rules of Lévy's permutation equivalence, see [Lév80]).

A  *$\beta$ -duplicating* rule is a  $\pi$  rule in which the redex  $(e_{\nabla}, e)$  is formed of a mux  $e_{\nabla}$  whose principal door is the internal door of a box door link  $e$ . Let  $\sigma_o$  ( $\pi_o$ ) be the set of all the  $\sigma_s$  ( $\pi$ ) rules but the  $\beta$ -duplicating ones.

**Theorem 63** *Any  $\sigma_o$  reduction is  $\beta$ -optimal.*

**Proof.** Let  $\rho_o : G \rightarrow_{\sigma_o}^* G'$ . By hypothesis, there are no erasing  $\beta$  redexes (in each  $\beta$  redex there is at least a ? link) and the  $\gamma$  rules do not erase  $\beta$  redexes. Hence, at least a residual of each  $r \in \rho_o$  must be reduced in any reduction  $\rho : G \rightarrow_{\sigma_s}^* G'$ . Moreover, any redex  $r$  of  $G_1$  s.t.  $G \rightarrow_{\sigma_o}^* G_1$  has at most a residual in any  $G_2$  s.t.  $G_1 \rightarrow_{\sigma_o}^* G_2$ .  $\square$

By the way, the  $\beta$ -optimal reductions correspond to the usual  $\lambda$ -calculus or linear logic optimal reductions ([Lam90,GAL92b,GAL92a,AG97]) when  $\sigma_s$  is the corresponding sharing implementation (*cf.* section 4.1). In such cases, to delay or to not execute a  $\beta$ -duplicating rule does not hide any  $\beta$  redex. In fact, for any  $\beta$  redex  $r$  of  $\mathcal{R}(G)$ , there is a reduction  $G \rightarrow_{\pi_o}^* G'$  s.t. the image of  $r$  in  $G'$  is a  $\beta$  redex (note that  $\mathcal{R}(G') = \mathcal{R}(G)$ ). As a consequence, it is not

difficult to prove that for any  $\lambda$  or MELL  $s\ell$ -net  $G$ , if  $N$  is the  $\sigma$  normal form of  $\mathcal{R}(G)$ , then the  $\sigma_o$  normal form of  $G$  is an  $s\ell$ -net  $G'$  s.t.  $\mathcal{R}(G') = N$ .

Besides, an analog property does not hold in the general case. In fact, let us assume that  $\sigma$  is not confluent. This means that  $\gamma$  is not confluent. Hence, let  $\rho_1$  and  $\rho_2$  be two  $\gamma$  reductions internal to a box  $B$  of an  $\ell$ -net  $N$ . Let us assume that in  $N$  there are at least two instances of  $B$ , say  $B'$  and  $B''$ . A possible reduction of  $N$  is  $\rho'_i \rho''_j : N \rightarrow_\gamma N_{ij}$ , in which  $\rho'_i$  is internal to  $B'$  and  $\rho''_j$  is internal to  $B''$ , with  $i, j \in \{1, 2\}$ . For the sake of simplicity let us also assume that each  $N_{ij}$  is in normal form. If  $G$  is a proper  $s\ell$ -structure in which  $B'$  and  $B''$  are shared, there is no  $\beta$ -optimal reduction  $G \rightarrow_{\sigma_o} G_{12}$  s.t.  $\mathcal{R}(G_{12}) = N_{12}$ , and analogously for  $N_{21}$ .

The latter point shows that the  $\pi$  rules are not just a nice way to present Lévy's optimality. They give indeed a low-level implementation interesting per se, that applies even to systems for which Lévy's optimality cannot be defined.

#### 11.4 Box erasing and garbage collection

As already pointed out in Remark 14, our definition of box forbids a direct implementation of MELL with weakening. It might seem that this also imply the impossibility to erase boxes. Luckily, the latter point is instead false. In fact, a box may be erased, provided that  $\Sigma$  contain an erasing link suitably shaped for this purpose, say an  $\epsilon$  link. (In Definition 61 we asked that the only rewriting rules involving box door links are  $\beta$  and  $\pi$ . As we need that an  $\epsilon$  link erase box door links too, the use of an erasing link would imply a slight reformulation of the definition of sharing implementation. Besides, this would not hurt Theorem 62, the only consequence would be a more involved statement.)

For instance, let us take the case that the  $\epsilon$  link be a sort of mux with 0 auxiliary ports. According to this, an  $\epsilon$  link at level  $m$  erases any link to which it is connected but box door links of level  $n$ , when  $n \leq m$ , and muxes. (Instantiating the  $\alpha$ -propagation rule to the case of  $\epsilon$ , we see indeed that such an  $\epsilon$ -propagation rule erases the mux involved in it. So, the meaning of the previous restriction is that a mux  $e_\nabla$  is not erased by an  $\epsilon$  link  $e_\epsilon$  when  $\text{doors}(e_\epsilon) \in \text{adoors}(e_\nabla)$ .) According to the restriction on levels, an  $\epsilon$  link stops its erasing at the internal port of a box door link whose level is lower than its threshold. Then, let  $r = (e_1, \{e_2\})$  be a  $\beta$  redex of an  $\ell$ -structure  $G$  (for the sake of simplicity let us start with the case without muxes) s.t. above  $e_2$  there is an  $\epsilon$  link  $e_\epsilon$  with  $\ell_G(e_\epsilon) = \ell_G(e_2) + 1$ . It is readily seen that the  $\beta_s$  reduction of  $r$  might be followed by an erasing reduction sequence ending with the complete

erasing of  $\text{box}^\ell(e_i)$ . Namely,  $G \rightarrow^* G'$ , for some  $G'$  in which in the place of  $\text{box}_G^\ell(e_i)$  there is a set of  $?$  links—the auxiliary doors of  $\text{box}_G^\ell(e_i)$ —each one below an instance of  $e_\epsilon$ . Besides, in the general case, an  $\epsilon$  link might stop at the auxiliary door of a mux, and consequently a subgraph that should be erased might rather remain alive. In order to erase such a garbage, we should lost most of the sharing contained in the net.

Independently from the formulation of the  $\epsilon$  link, the previous example opens a problem of garbage collection completely independent from the one of soundness, which was indeed our main concern in this paper. Anyhow, because of the relevance of such a problem in any practical use of sharing graphs, a detailed study of garbage collection is one of our future goals.

## 12 Conclusions and further work

The box nesting property is the minimal (and natural) requirement under which the sharing reductions can be used to get a local and distributed implementation of  $\beta$ -like rules. It is our aim to study how the class of the calculi having the box nesting property relates with the classes already studied by the term graph rewriting community. As shown by the relevant examples of  $\lambda$ -calculus and MELL, a wide set of calculi fits in this class. Asperti and Laneve [AL94] proved that sharing graphs can be used for the so-called Interaction Systems, a subset of the Combinatory Reduction Systems for which there is a corresponding intuitionistic logic. Our requirement seems weaker and more general, so we expect to find a much wider class.

Lafont's Interaction Nets are the natural system to compare with sharing graphs [Laf90]. Sharing graphs are interaction nets whose interactions are no more restricted to the principal ports of the interacting elements and with the additional information of levels. We think that the relation between interaction nets and sharing graphs is similar to the relation between Combinatory Logic and  $\lambda$ -calculus. Both such systems are Turing complete and based on an applicative principle, but to simulate the reduction of a  $\lambda$ -term inside Combinatory Logic may greatly increase the length of the reduction. In spite of this, Lafont's interaction combinators [Laf95] and the work of Fernández and Mackie [FM96b,FM96a] on how to encode term rewriting systems into interaction nets might give useful insights for the determination of the class of systems implementable by sharing graphs.

One of the restriction we imposed to  $\beta$  rule is that in a  $\beta$  redex  $(e_i, \{e_j^1, \dots, e_j^k\})$  no  $e_j^i$  can be an auxiliary door link of  $\text{box}^\ell(e_i)$ , that is, we forbade a box to interact with itself. All the results of the paper should hold even dropping such a constraint. Nevertheless, we chose to eliminate such cases since their presence

would have involved the description of  $\beta$ . Furthermore, the analog of a box interacting with itself is the  $\mu$  rule  $\mu x.T \rightarrow_{\mu} T[\mu x.T/x]$ , that is, a calculus with an explicit recursion operator. Hence, we preferred to maintain the distinction between  $\beta$  and  $\mu$  also in sharing graphs and to reserve a separate study for the  $\mu$ -like rules in which an object interacts with itself.

We already stated that  $\beta_s$  plus  $\pi$  give an abstract machine by which to get sharing implementations. Further, such a sharing graph machine seems the natural low-level model of  $\lambda$ -calculus. We hope that this would finally lead to a satisfactory measure for the cost of  $\lambda$ -calculus reductions, and that this would finally lead to a way to compare  $\lambda$ -calculus with the computational models used in complexity theory. We think that the interest in the definition of a suitable notion of cost and of complexity classes for sharing computations is even more appealing after the results of Asperti [Asp96] and Lawall and Mairson [LM96] showing that Lévy families are unlike to be the cost model of  $\lambda$ -calculus reductions.

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